



US00D825777S

(12) **United States Design Patent** (10) **Patent No.:** **US D825,777 S**
Quick et al. (45) **Date of Patent:** **** Aug. 14, 2018**

(54) **VIAL PROCESSOR**

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(US)

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(73) Assignee: **HOLOGIC, INC.**, Marlborough, MA
(US)

(**) Term: **15 Years**

(21) Appl. No.: **29/601,578**

(22) Filed: **Apr. 24, 2017**

(51) **LOC (11) Cl.** **24-01**

(52) **U.S. Cl.**
USPC **D24/232**

(58) **Field of Classification Search**
USPC D24/107, 169, 185, 186, 216–219,
D24/231–234; D10/81
CPC G01N 21/0483; G01N 21/0303; G01N
35/00029; G01N 35/00019; G01N
2035/00326; G01N 2030/027; G01N
2035/00306; G01N 2035/00336

See application file for complete search history.

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OTHER PUBLICATIONS

ThinPrep 2000 Processor image from ThinPrep 2000 Operator's Manual, copyright 2014.

Design application specification and drawings for U.S. Appl. No. 29/583,396, filed Nov. 4, 2016, entitled "Vial Processor", inventors Michael D. Quick and Suzanne M. Werneke, 8 pages.

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(57) **CLAIM**

The ornamental design for a vial processor, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of the vial processor, showing our new design;

FIG. 2 is a front elevation view thereof;

FIG. 3 is a rear elevation view thereof;

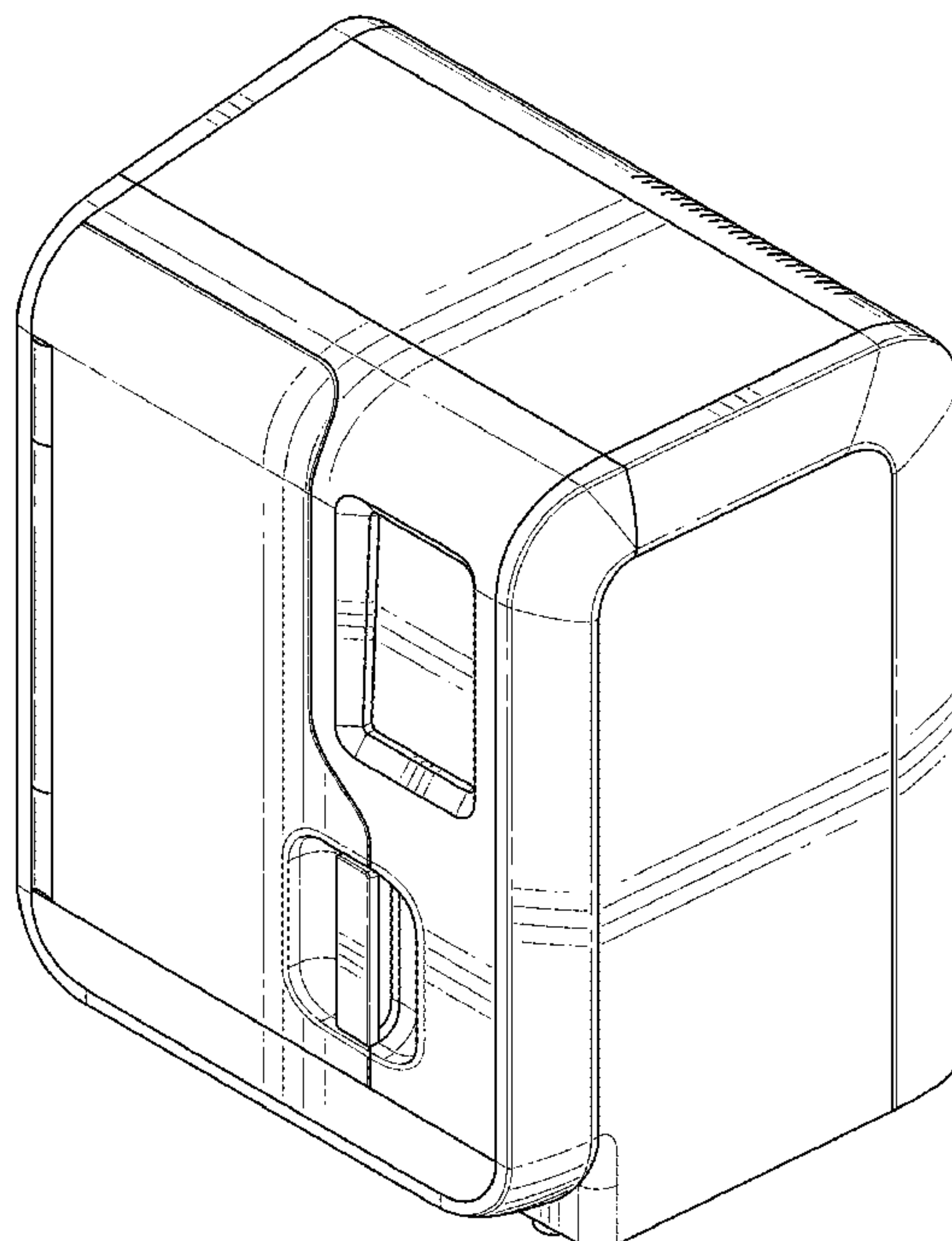
FIG. 4 is a left side elevation view thereof;

FIG. 5 is a right side elevation view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

1 Claim, 7 Drawing Sheets



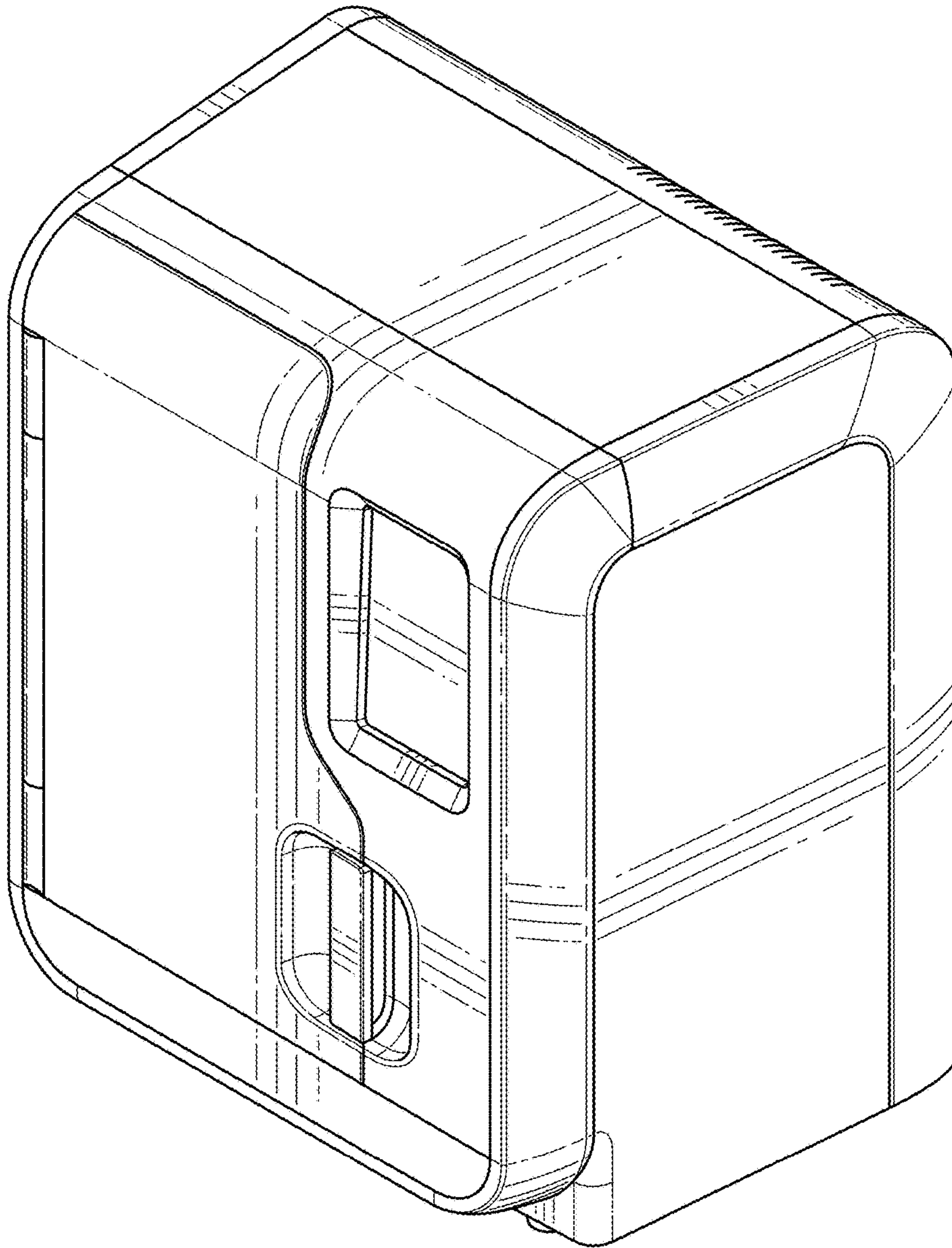


FIG. 1

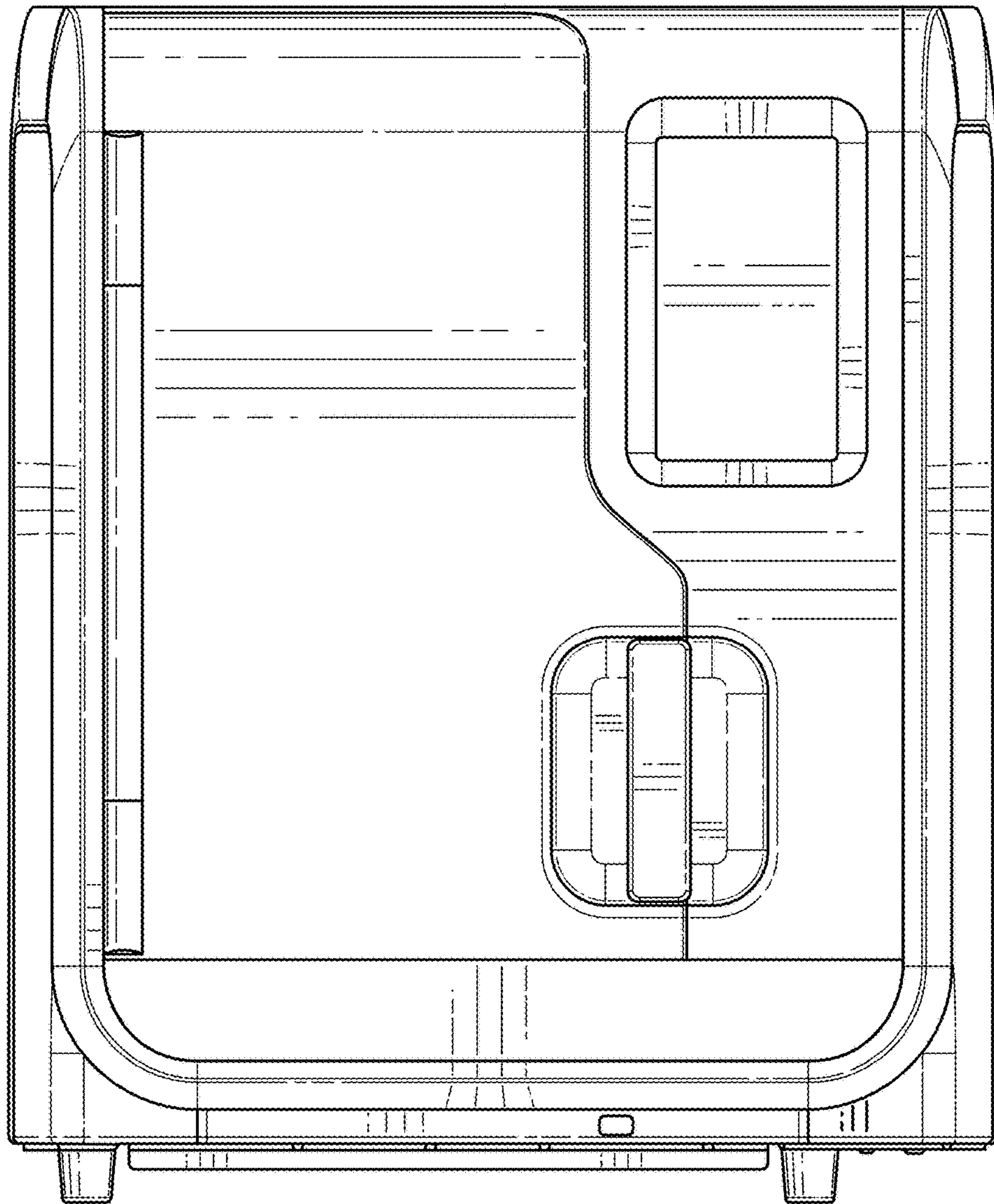


FIG. 2

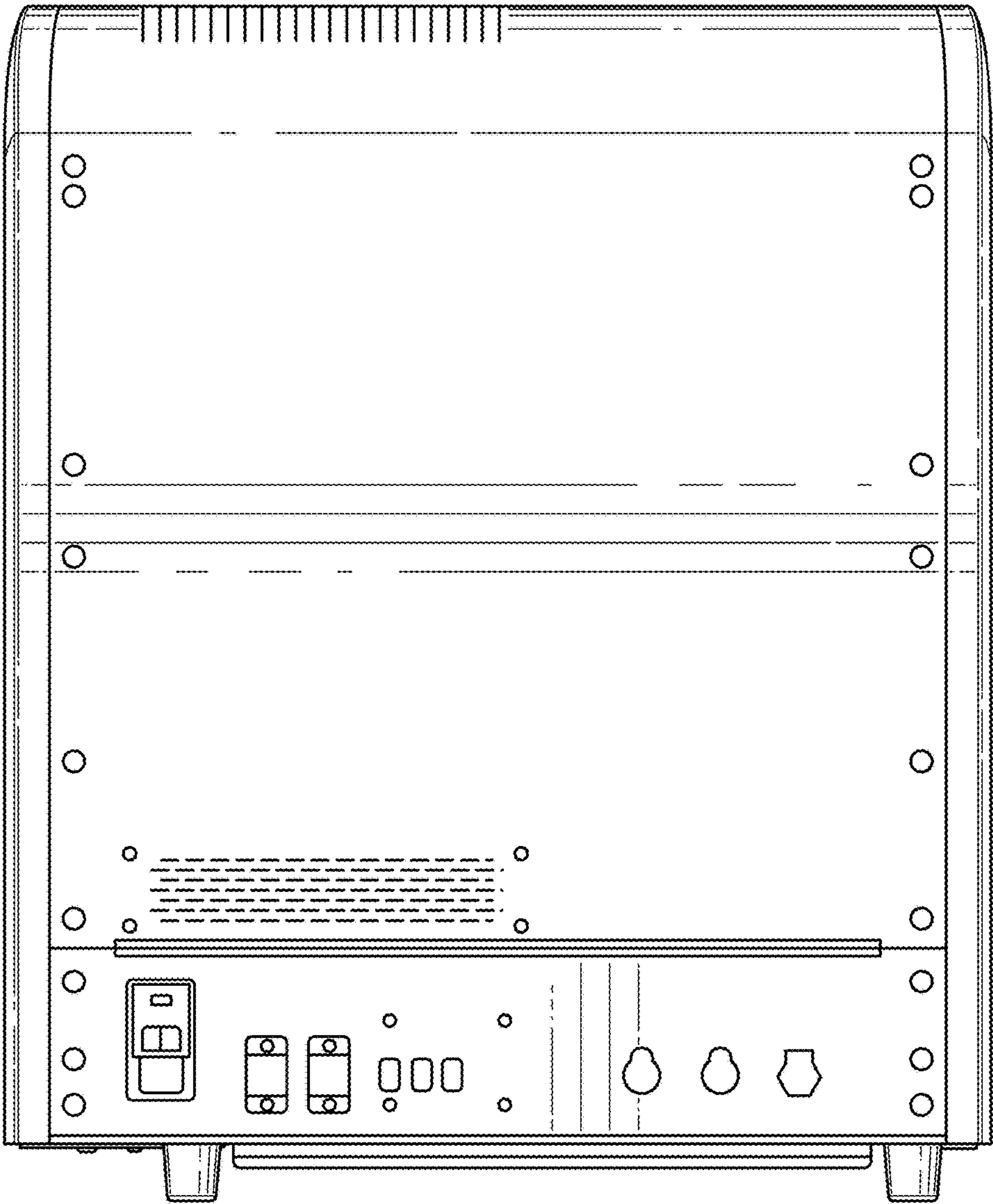


FIG. 3

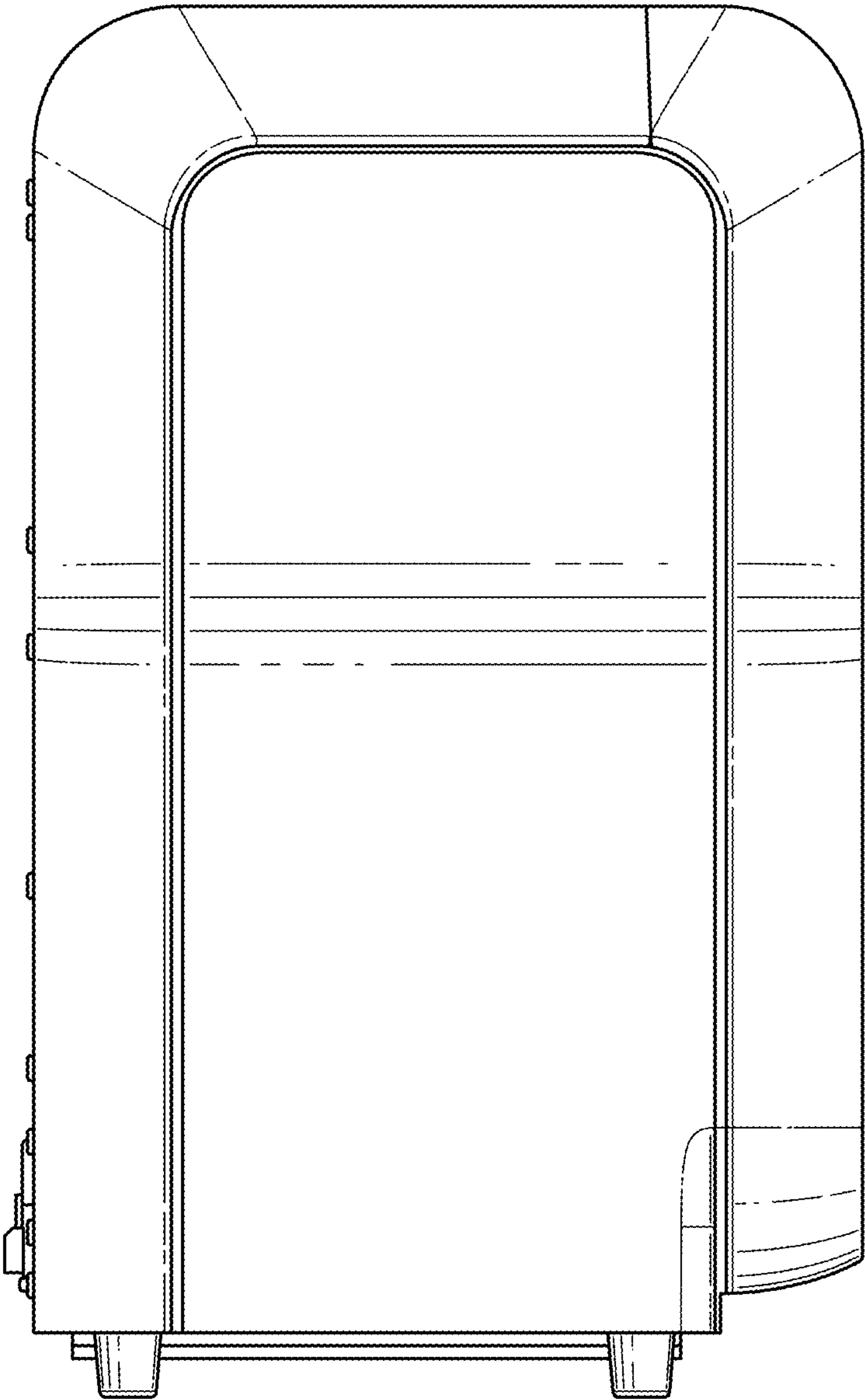


FIG. 4

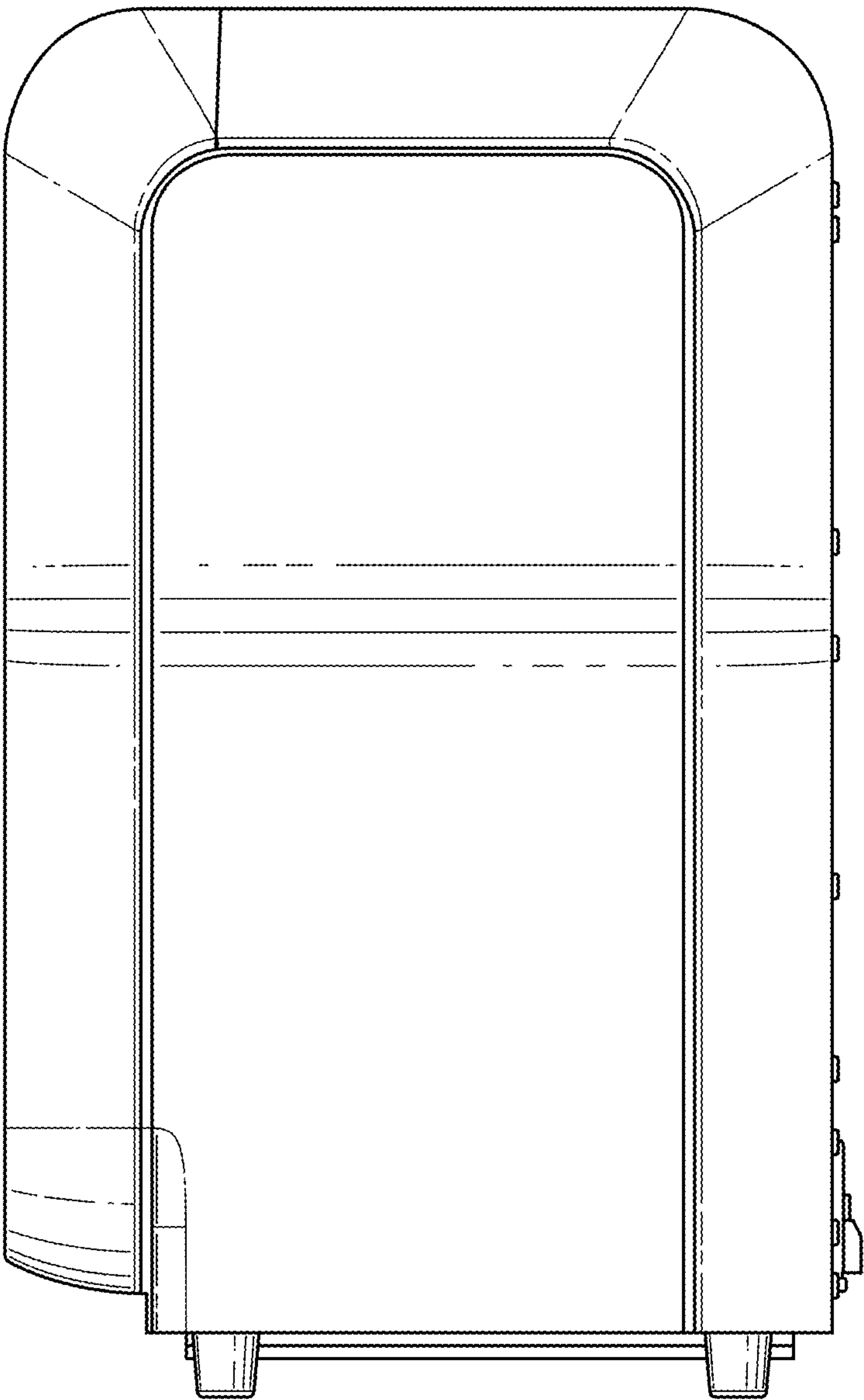


FIG. 5

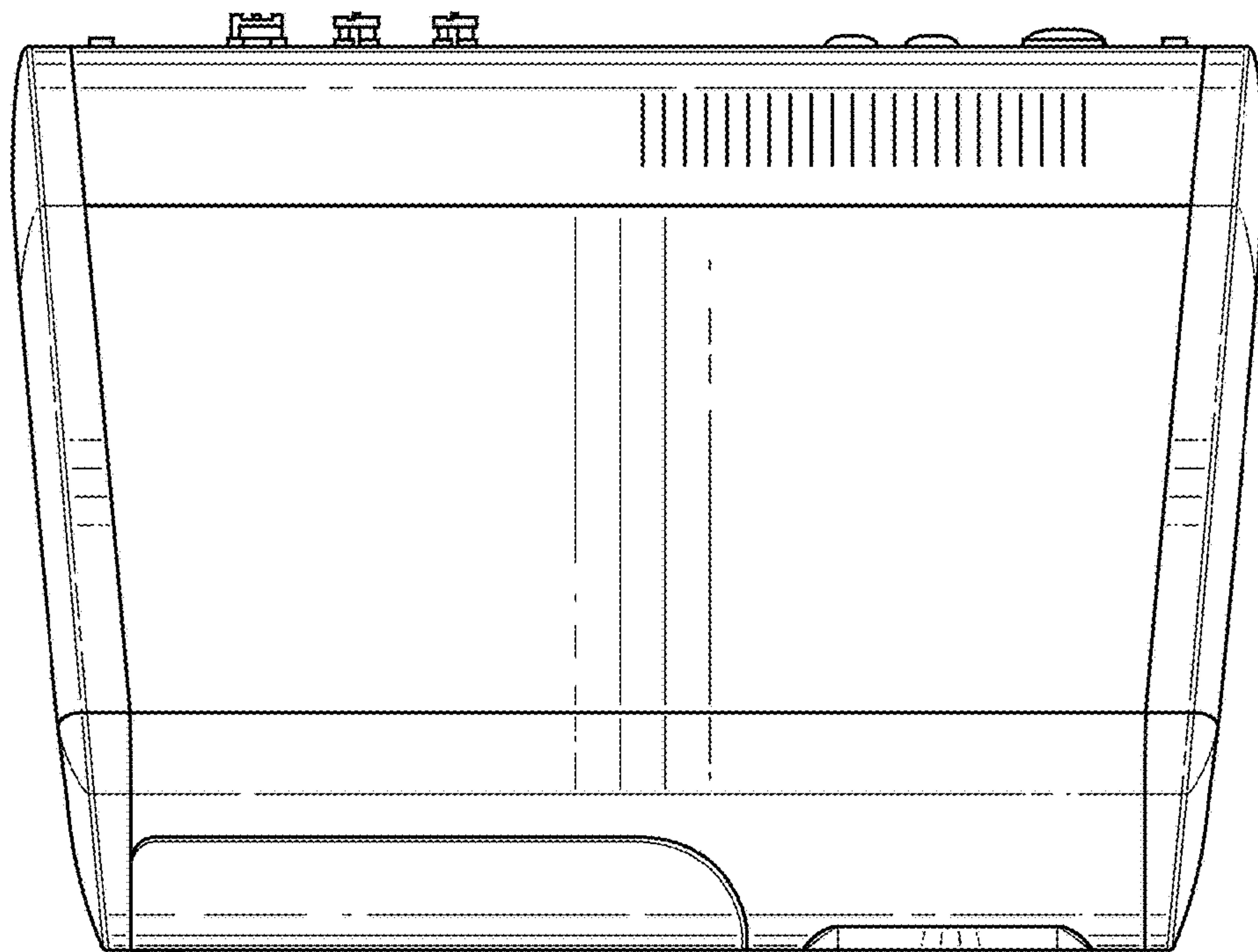


FIG. 6

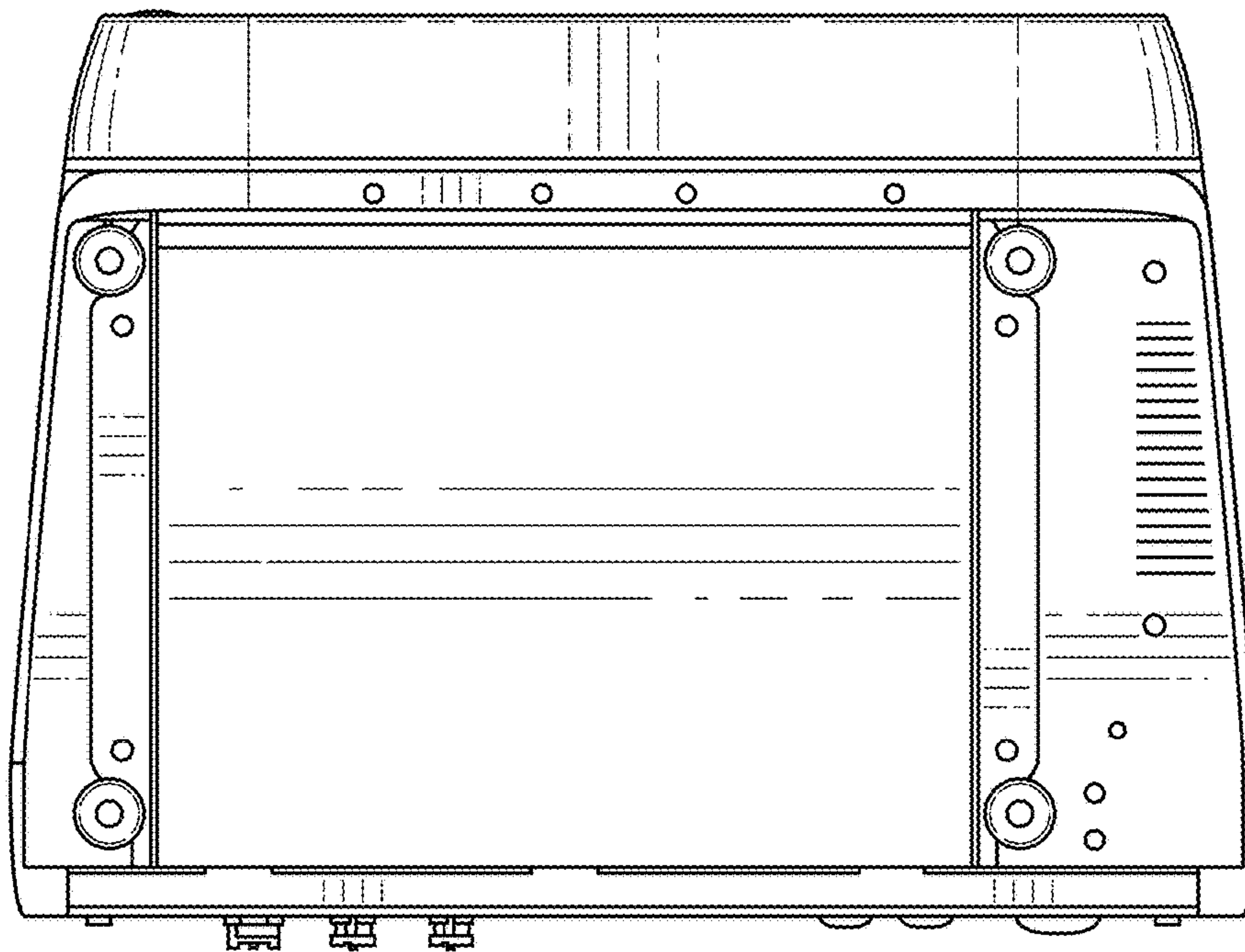


FIG. 7