



US00D824897S

(12) **United States Design Patent** (10) **Patent No.:** **US D824,897 S**
Collins et al. (45) **Date of Patent:** **** Aug. 7, 2018**

(54) **MODULAR COMPUTING DEVICE**

(56) **References Cited**

(71) Applicant: **Intel Corporation**, Santa Clara, CA
(US)

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(US)

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(**) Term: **15 Years**

Primary Examiner — Freda S Nunn

(21) Appl. No.: **29/589,160**

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(22) Filed: **Dec. 28, 2016**

(57) **CLAIM**

The ornamental design for a modular computing device, as shown and described.

(51) **LOC (11) Cl.** **14-02**

DESCRIPTION

(52) **U.S. Cl.**

USPC **D14/315**

(58) **Field of Classification Search**

USPC D14/240, 300, 301, 308, 313, 314, 344,
D14/315, 348–356, 364–368, 432;
D13/123, 158, 162, 184, 199

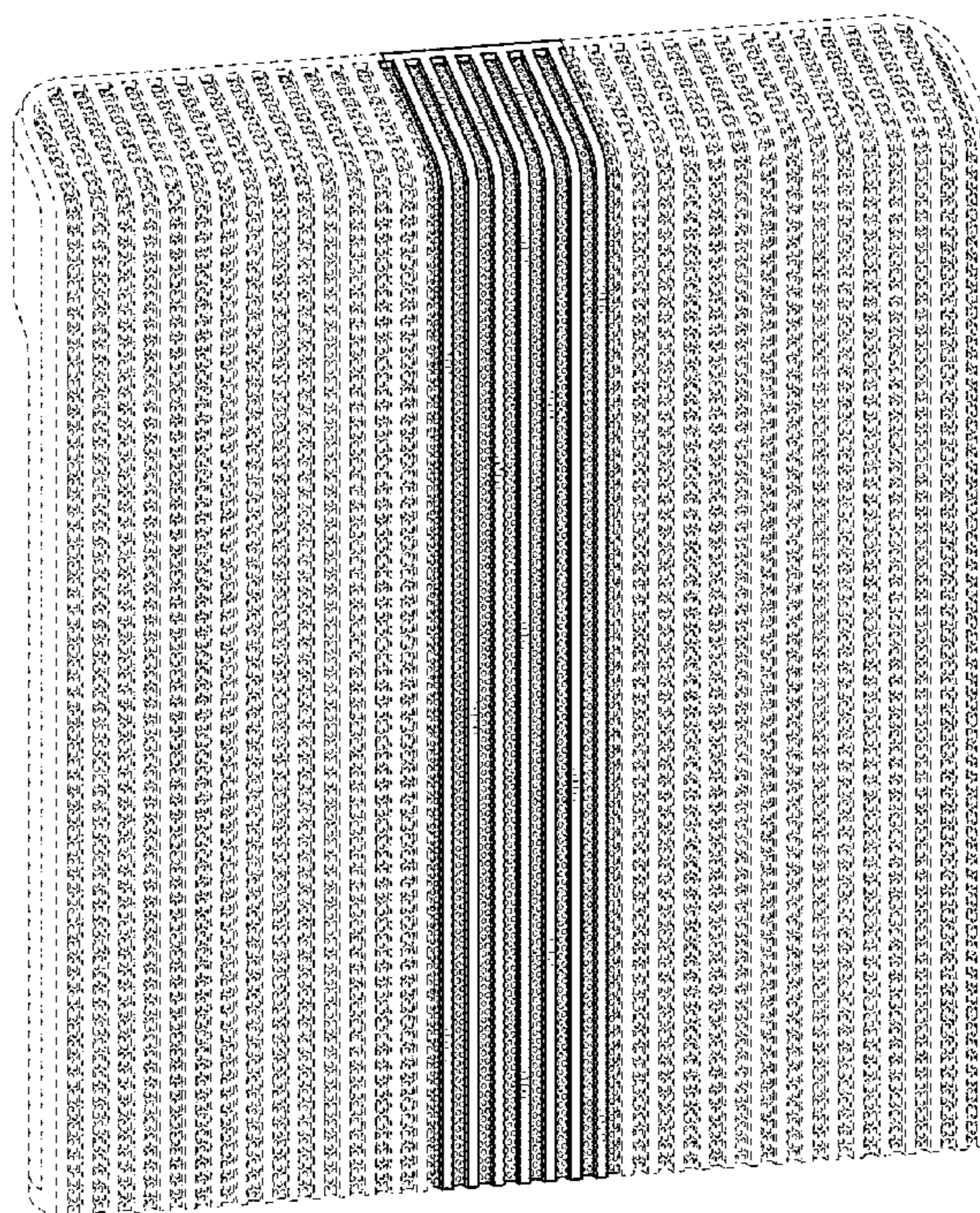
CPC . G06F 1/20; G06F 1/181; G06F 1/187; G06F
1/183; G06F 1/184; G06F 1/16; H05K
7/16; H05K 7/20; H05K 7/1424; H05K
7/1409; H05K 7/20545; H05K 7/20727;
H05K 7/1425; H05K 7/1488; H05K
7/183; H05K 7/14; H05K 5/00; H05K
7/00; H05H 7/20; G11B 33/126; G11B
33/128; G11B 33/08; G11B 33/02

FIG. 1 is a front-left-perspective view of a first embodiment of our modular computing device; FIG. 2 is a front view thereof; FIG. 3 is a back view thereof; FIG. 4 is a right-side view thereof; FIG. 5 is a left-side view thereof; FIG. 6 is a top view thereof; and, FIG. 7 is a bottom view thereof.

It is to be understood that the bold, dash-dash broken lines located adjacent to the right and left side edges of the seven, protruding, vertical elements represent the bounds of the claimed design while all other broken lines are directed to environment only and form no part of the claimed design.

See application file for complete search history.

1 Claim, 4 Drawing Sheets



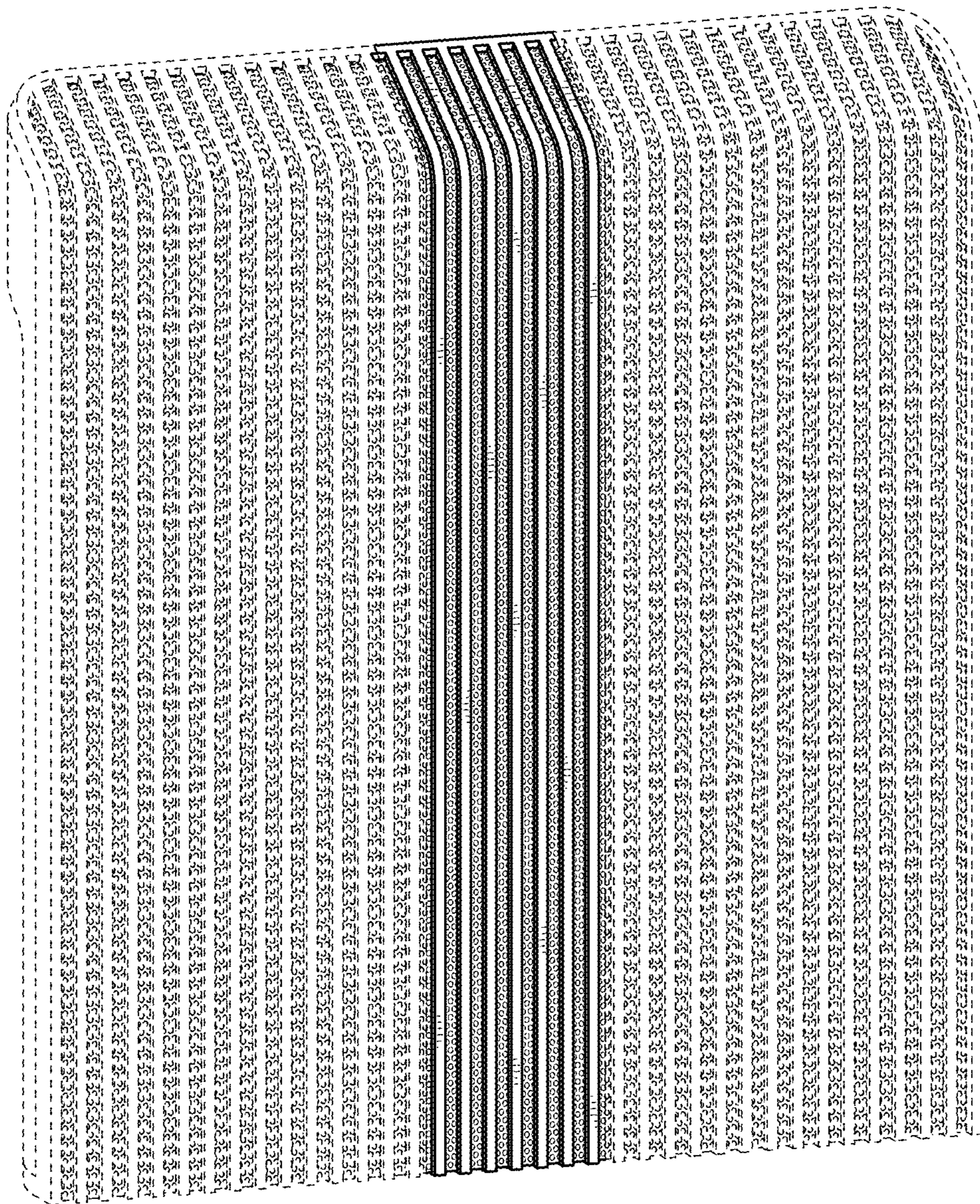


FIG. 1

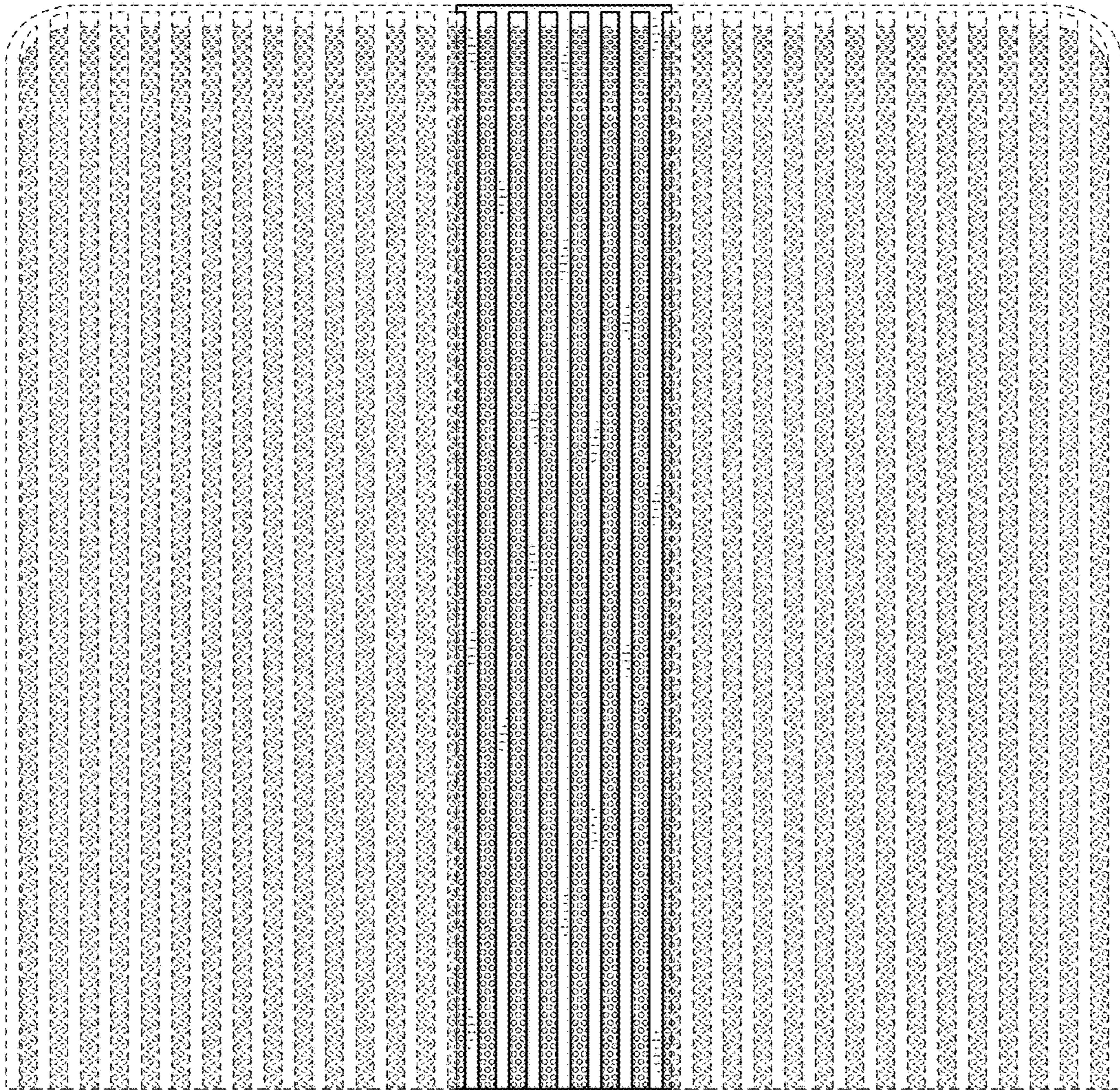


FIG. 2

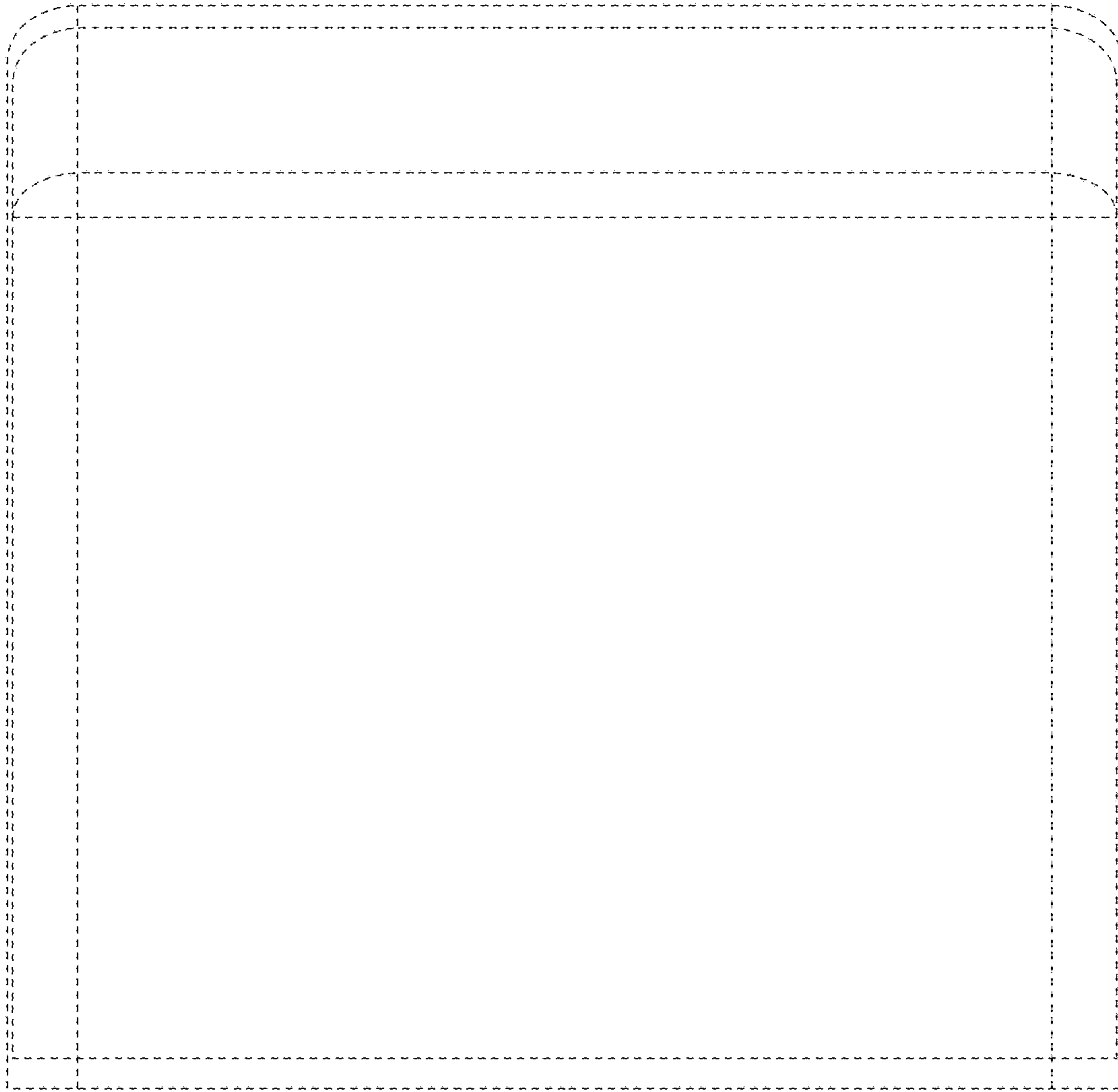


FIG. 3

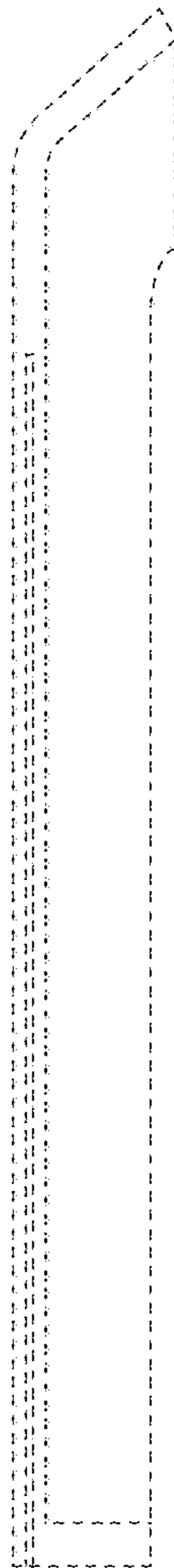


FIG. 4

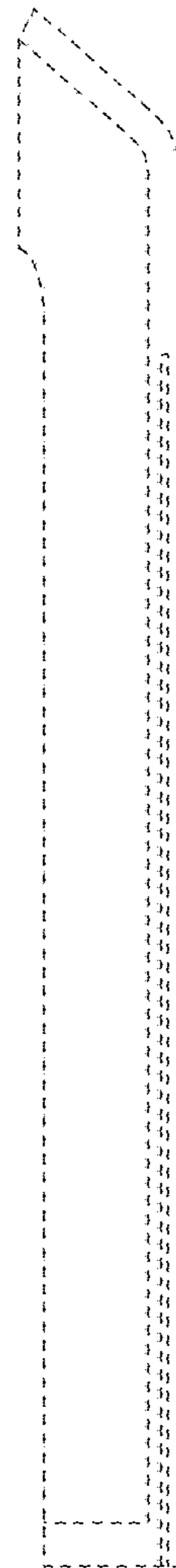


FIG. 5

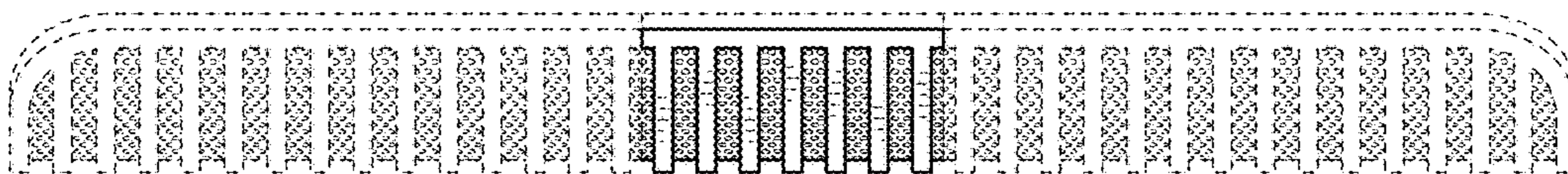


FIG. 6

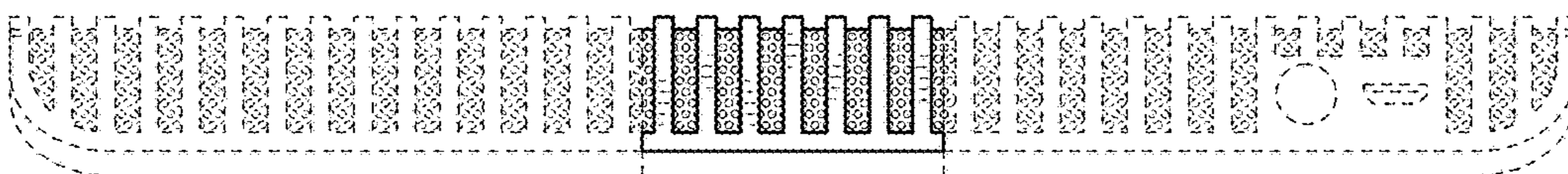


FIG. 7