



US00D824865S

(12) **United States Design Patent**
Chen

(10) **Patent No.:** **US D824,865 S**

(45) **Date of Patent:** **** Aug. 7, 2018**

(54) **DUAL POWER INPUT CIRCUIT BOARD**

(71) Applicant: **inXtron, Inc.**, New Taipei (TW)

(72) Inventor: **Szu-Tien Chen**, New Taipei (TW)

(73) Assignee: **inXtron, Inc.**, New Taipei (TW)

(**) Term: **15 Years**

(21) Appl. No.: **29/592,949**

(22) Filed: **Feb. 3, 2017**

(30) **Foreign Application Priority Data**

Nov. 25, 2016 (TW) 105307162

(51) **LOC (11) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; 174/68.1, 250, 253, 255, 256;
318/567, 568.1; 361/600, 601, 718, 719,
361/720, 728, 736, 748, 751, 752, 760,
361/761, 807; 439/55, 65, 68, 69, 76.1,
439/92, 93, 95

CPC H05K 3/00; H05K 3/30; H05K 3/301;
H05K 3/303; H05K 3/34; H05K 3/3405;
H05K 3/341; H05K 3/36; H05K 3/361;
H05K 3/363; H05K 3/40; H05K 7/14;
H05K 7/1422; H05K 7/00; H05K 1/18;
H05K 1/02; H05K 1/181; H05K 1/182;
H05K 1/183; H05K 1/184; H05K 1/189;
H05K 1/00

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

4,651,416 A * 3/1987 DePaul H05K 1/18
174/264
D292,698 S * 11/1987 DeVita D13/182

D397,093 S * 8/1998 Kim D13/182
D430,856 S * 9/2000 Wilkerson D13/182
6,760,225 B1 * 7/2004 Chen H05K 1/0209
174/252
D505,925 S * 6/2005 Koo D13/182
D508,681 S * 8/2005 Enderlein D13/182
D524,763 S * 7/2006 Nakano D13/182
D525,213 S * 7/2006 Enderlein D13/182
D647,072 S * 10/2011 Bentley D13/182
D739,350 S * 9/2015 Kuwabara D13/118
D745,476 S * 12/2015 Ronse D13/182
D794,586 S * 8/2017 Takahashi D13/182
D799,438 S * 10/2017 Takahashi D13/182
2009/0268390 A1 * 10/2009 King G06F 13/409
361/679.33

* cited by examiner

Primary Examiner — Elizabeth J Oswecki

(74) *Attorney, Agent, or Firm* — Juan Carlos A. Marquez;
Marquez IP Law Office, PLLC

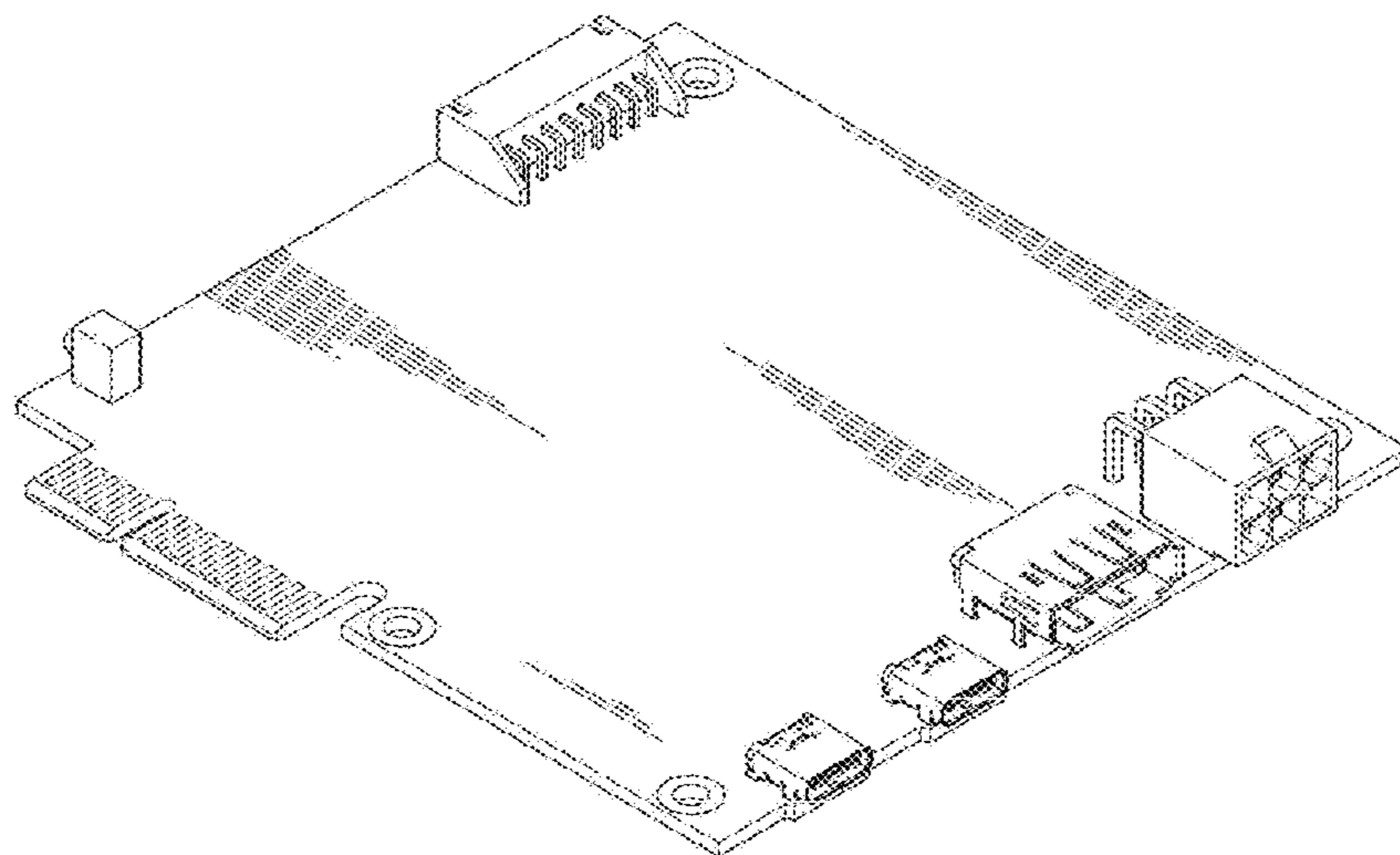
(57) **CLAIM**

The ornamental design for a dual power input circuit board,
as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a dual power input circuit board showing my new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a left side elevational view thereof;
FIG. 5 is a right side elevational view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is another perspective view thereof.

1 Claim, 6 Drawing Sheets



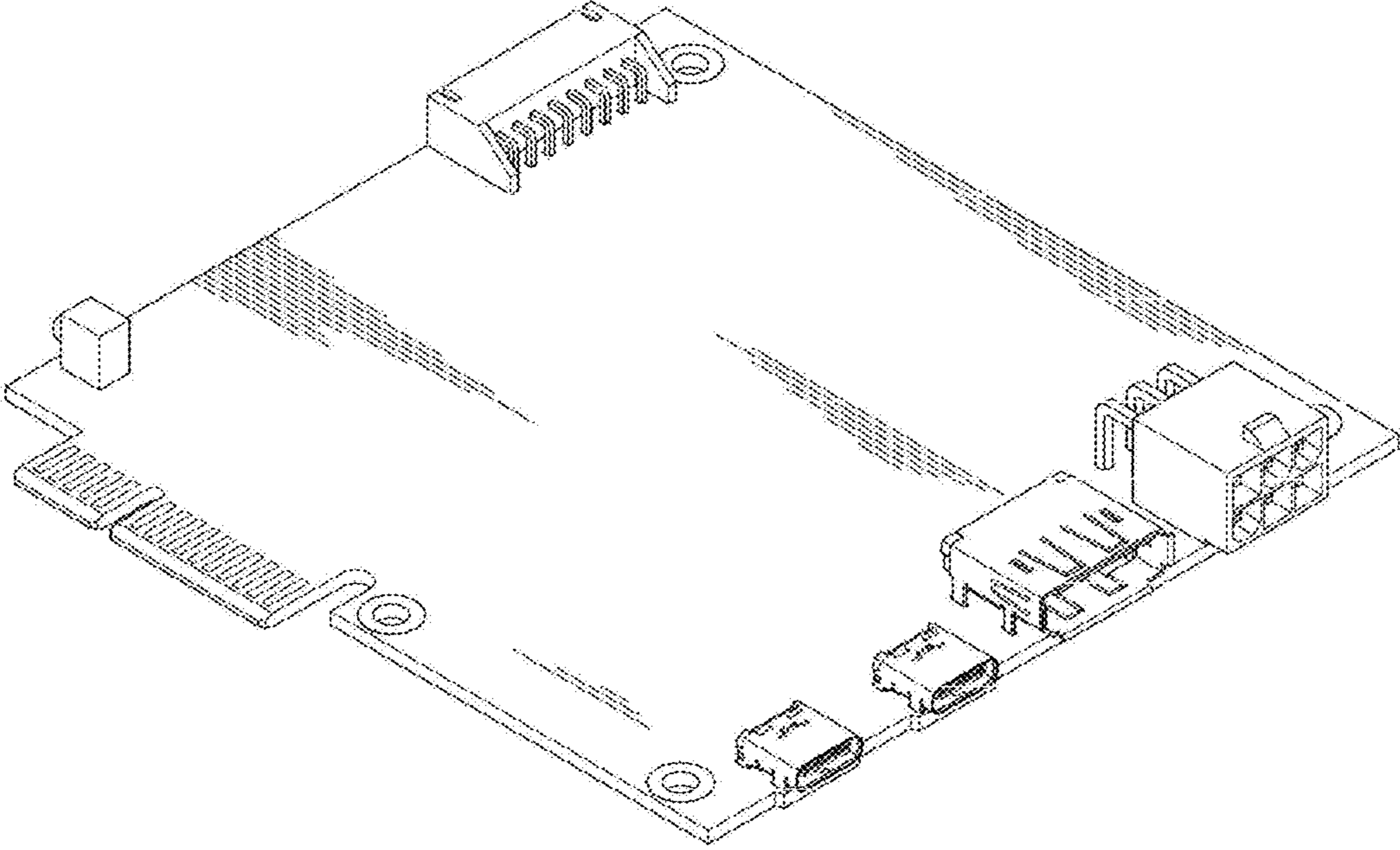


FIG. 1

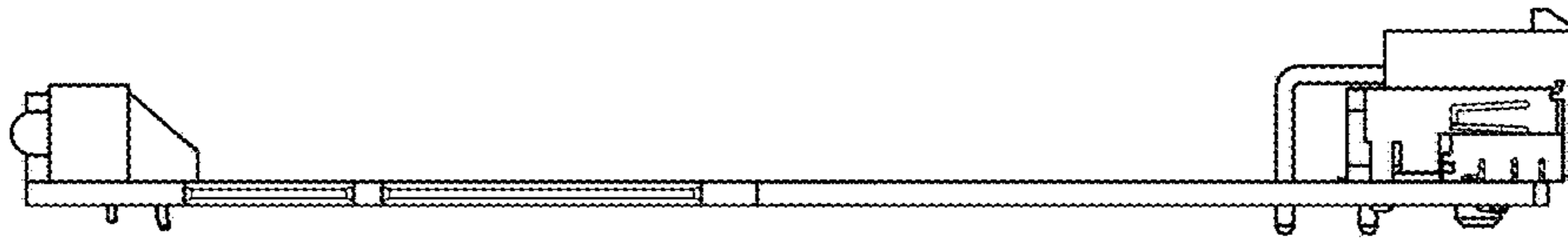


FIG. 2

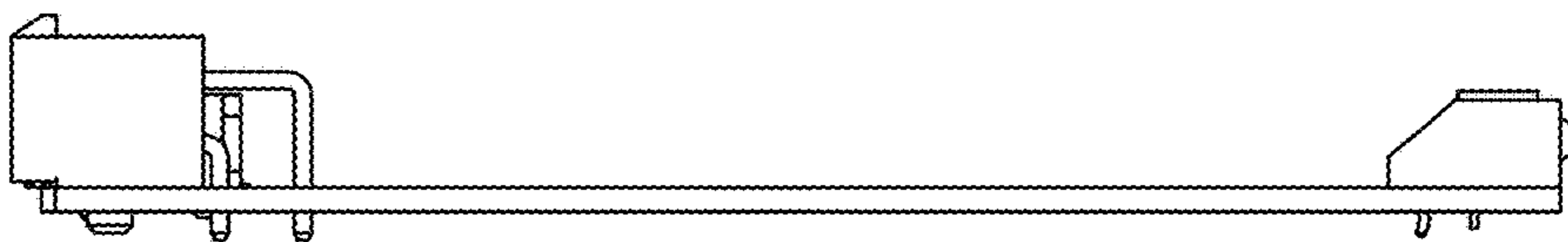


FIG. 3

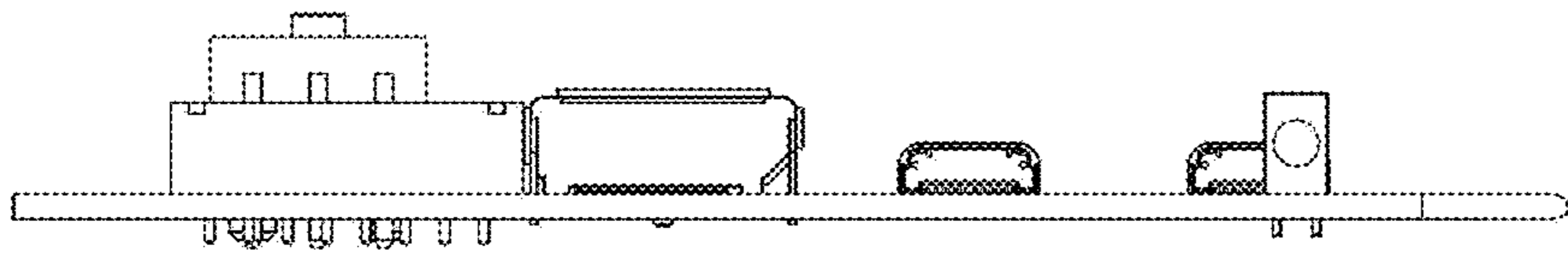


FIG. 4

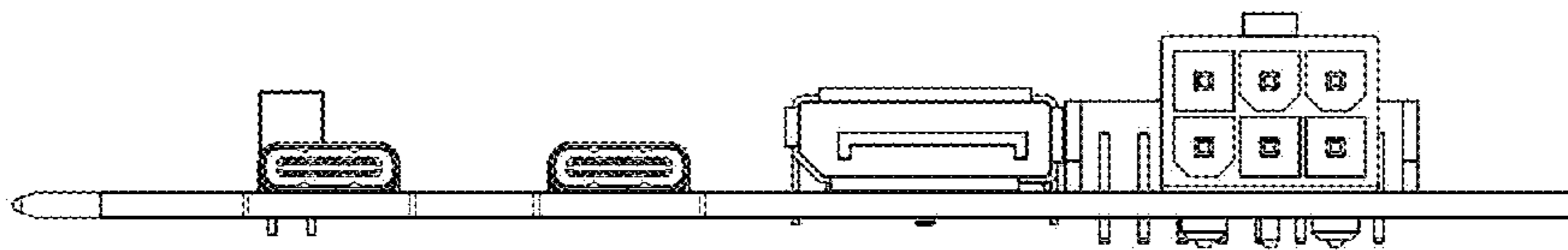


FIG. 5

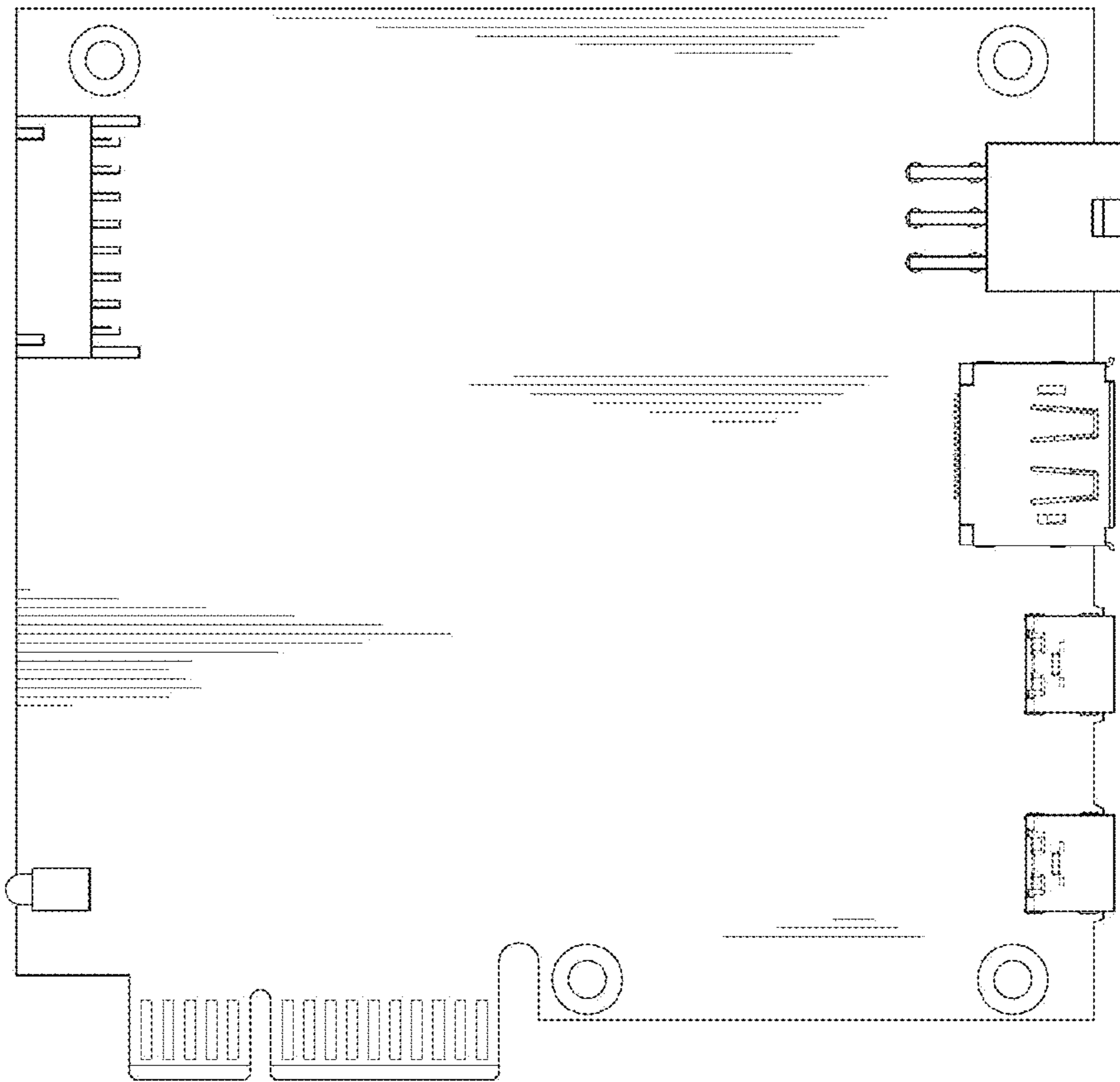


FIG. 6

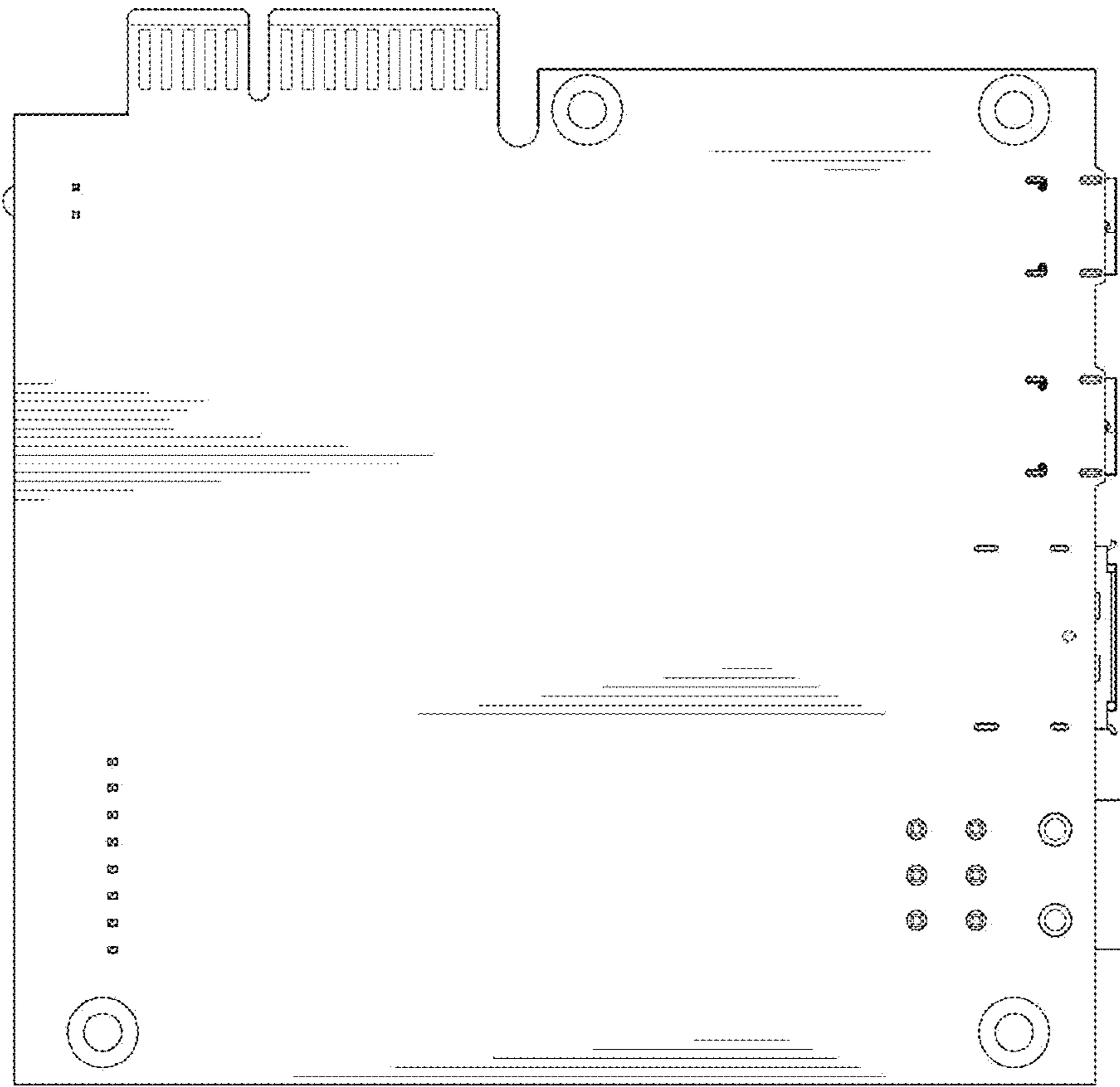


FIG. 7

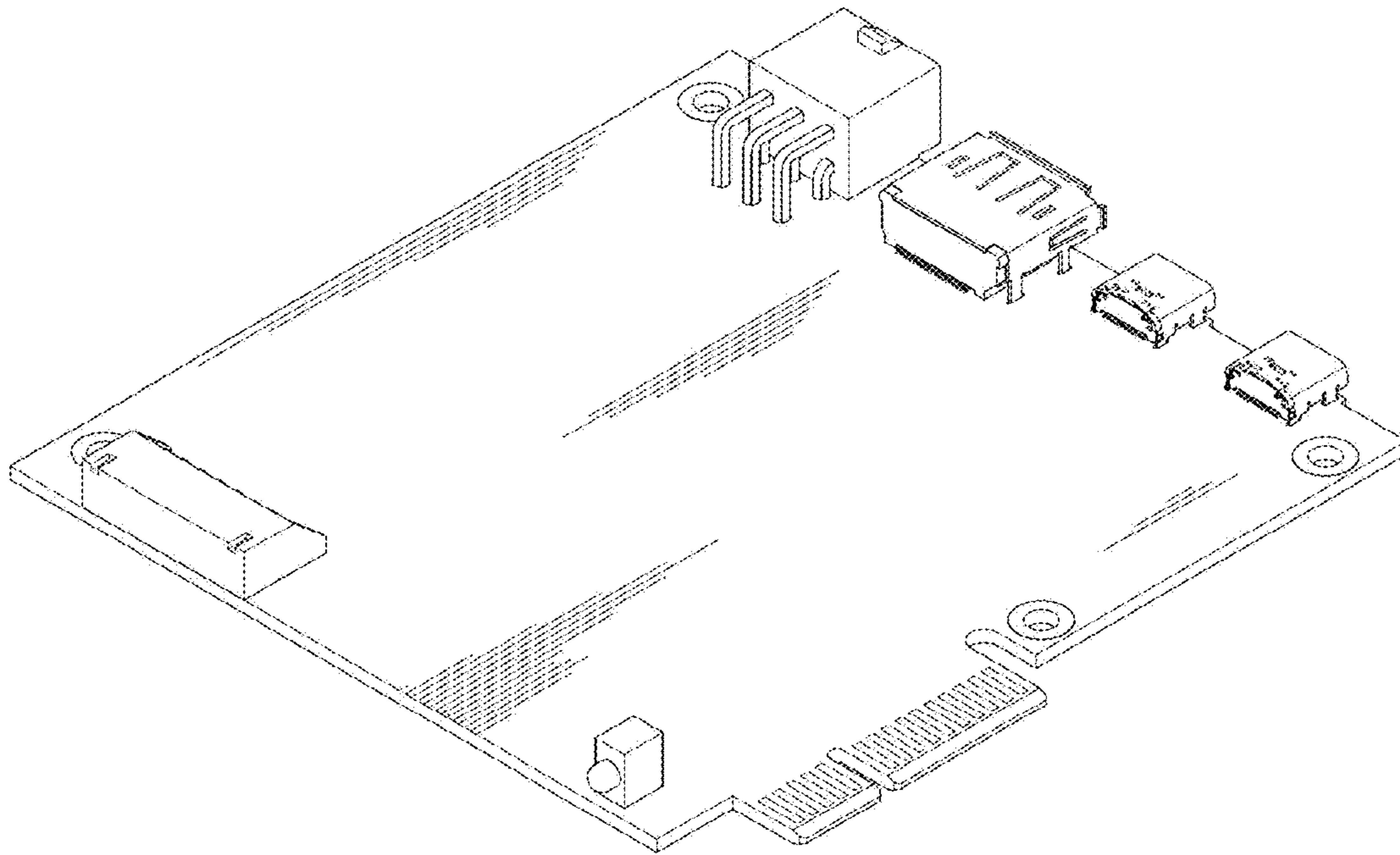


FIG. 8