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(12) **United States Design Patent**  
**Collins et al.**

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(54) **MODULAR COMPUTER**

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(US)

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(US)

(73) Assignee: **Intel Corporation**, Santa Clara, CA  
(US)

(\*\*) Term: **15 Years**

(21) Appl. No.: **29/589,163**

(22) Filed: **Dec. 28, 2016**

(51) **LOC (11) Cl.** ..... **14-02**

(52) **U.S. Cl.**  
USPC ..... **D14/315**

(58) **Field of Classification Search**  
USPC ..... D14/240, 300, 301, 308, 313, 314, 344,  
D14/315, 348-356, 364-368, 432;  
D13/123, 158, 162, 184, 199  
CPC . G06F 1/20; G06F 1/181; G06F 1/187; G06F  
1/183; G06F 1/184; G06F 1/16; H05K  
7/16; H05K 7/20; H05K 7/1424; H05K  
7/1409; H05K 7/20545; H05K 7/20727;  
H05K 7/1425; H05K 7/1488; H05K  
7/183; H05K 7/14; H05K 5/00; H05K  
7/00; H05H 7/20; G11B 33/126; G11B  
33/128; G11B 33/08; G11B 33/02  
See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D596,175 S \* 7/2009 Viertola ..... D14/314  
D615,969 S \* 5/2010 Arbisi ..... D14/315  
(Continued)

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(74) *Attorney, Agent, or Firm* — Schwegman Lundberg & Woessner, P.A.

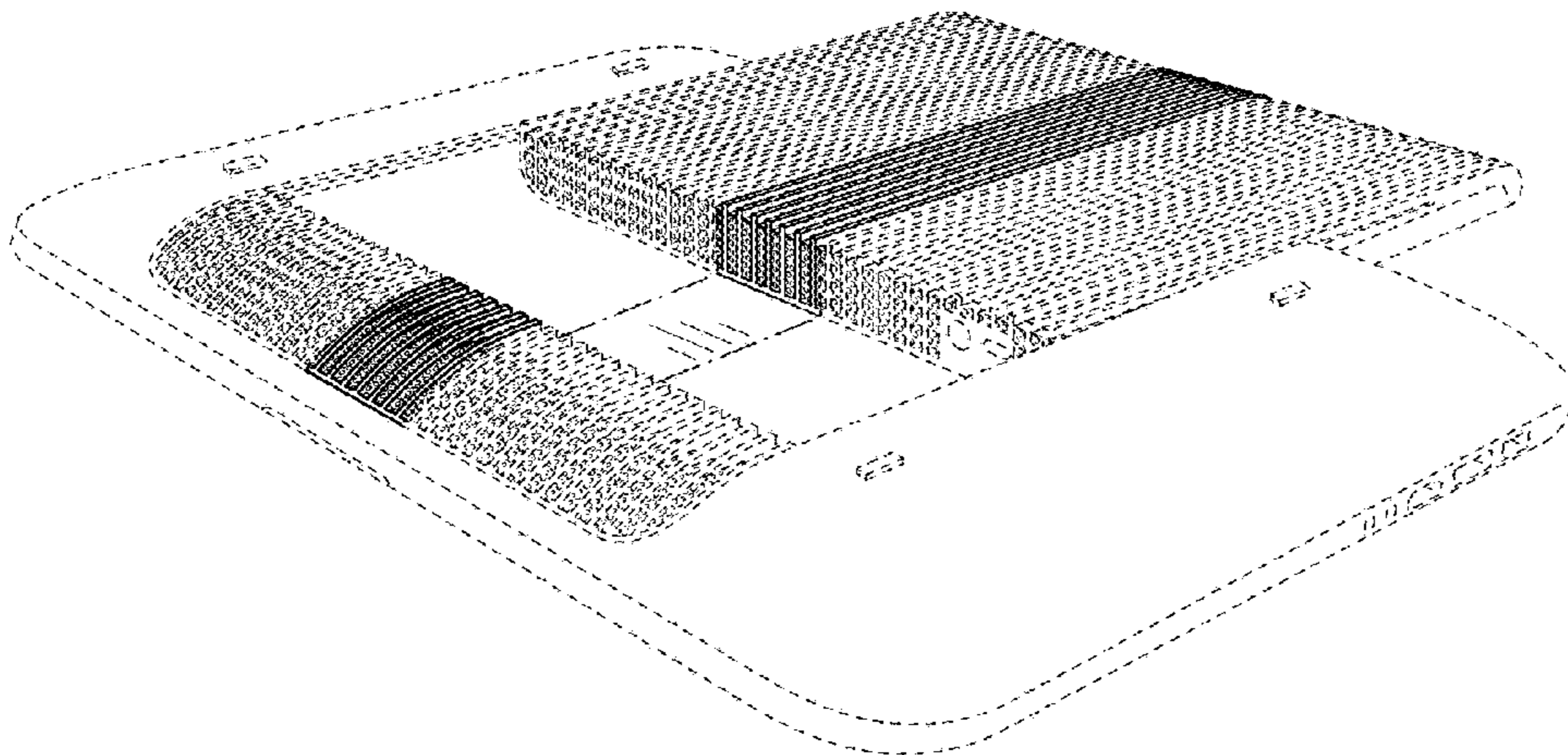
(57) **CLAIM**

The ornamental design for a modular computer, as shown and described.

**DESCRIPTION**

FIG. 1 is a back-right-perspective view of a first embodiment of our modular computer;  
FIG. 2 is a back-right-perspective view thereof showing the modular computing device of the modular computer removed from the body of the modular computer;  
FIG. 3 is a front view thereof;  
FIG. 4 is a back view thereof showing the modular computing device of the modular computer removed from its body;  
FIG. 5 is a back view thereof showing the modular computing device of the modular computer inserted into its body;  
FIG. 6 is a bottom-front-perspective view thereof showing the modular computing device of the modular computer in the process of being removed from its body;  
FIGS. 7-8 are bottom-front-perspective views thereof, with FIG. 8 showing the modular computing device of the modular computer completely removed from its body;  
FIG. 9 is a back-right-perspective view thereof showing the screen of the modular computer in an open condition;  
FIG. 10 is a front-left-perspective view thereof showing the screen of the modular computer in the open condition; and,  
FIGS. 11-17 are perspective, front, back, right, left, top and bottom views of the modular computing device, wherein the modular computing device is shown separately for clarity of illustration only.  
The dot-dash broken lines represent the bounds of the claimed design while all other broken lines are directed to environment only and form no part of the claimed design. It is to be understood that in the case of the dot-dash boundary lines, the claimed design extends to but does not include the defined boundary.

**1 Claim, 8 Drawing Sheets**



(56)

**References Cited**

U.S. PATENT DOCUMENTS

D647,903 S \* 11/2011 McClelland ..... D14/371  
D650,815 S \* 12/2011 Pellervo Viertola ..... D15/28  
D683,728 S \* 6/2013 Xia ..... D14/315  
D719,984 S \* 12/2014 Viertola ..... D15/28

\* cited by examiner

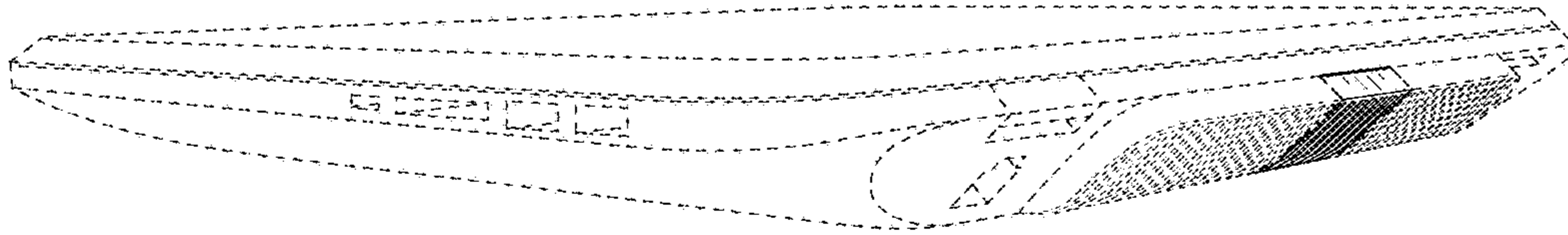


FIG. 1

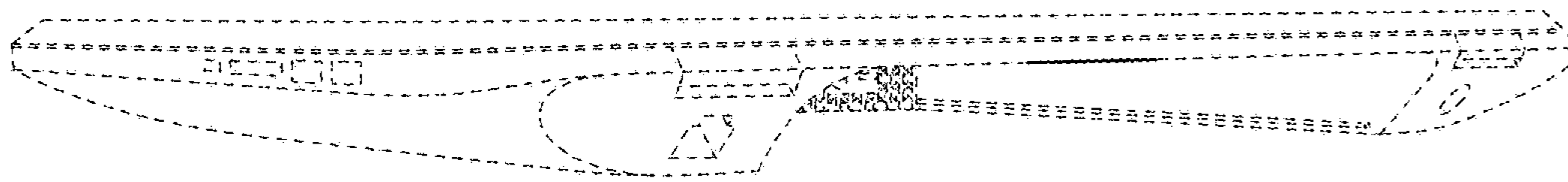


FIG. 2

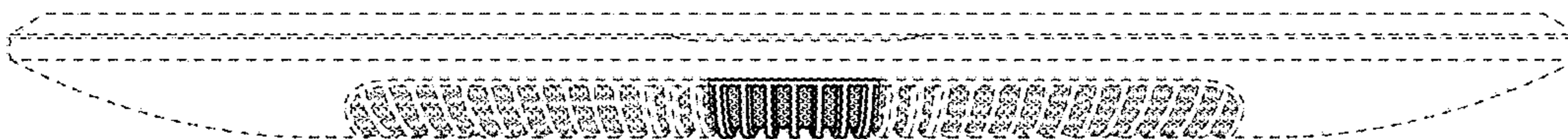


FIG. 3

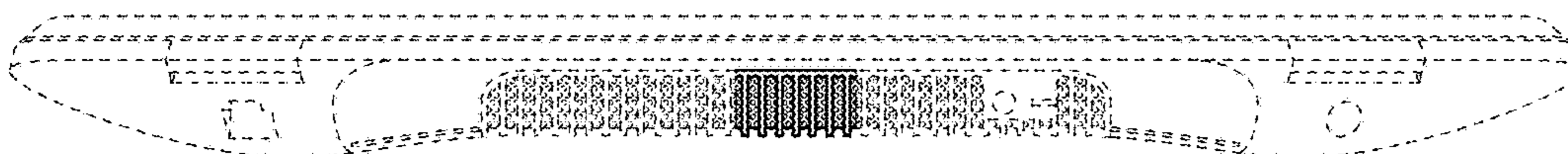


FIG. 4

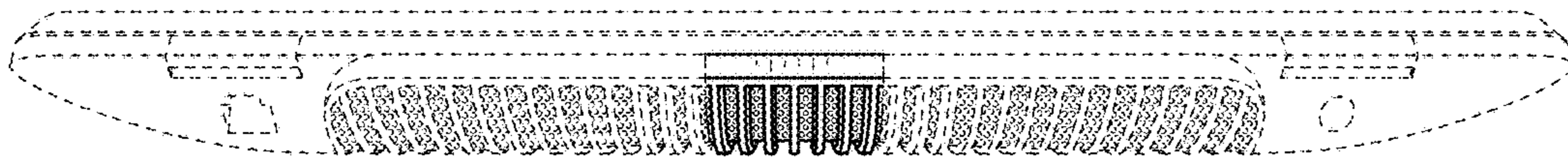


FIG. 5

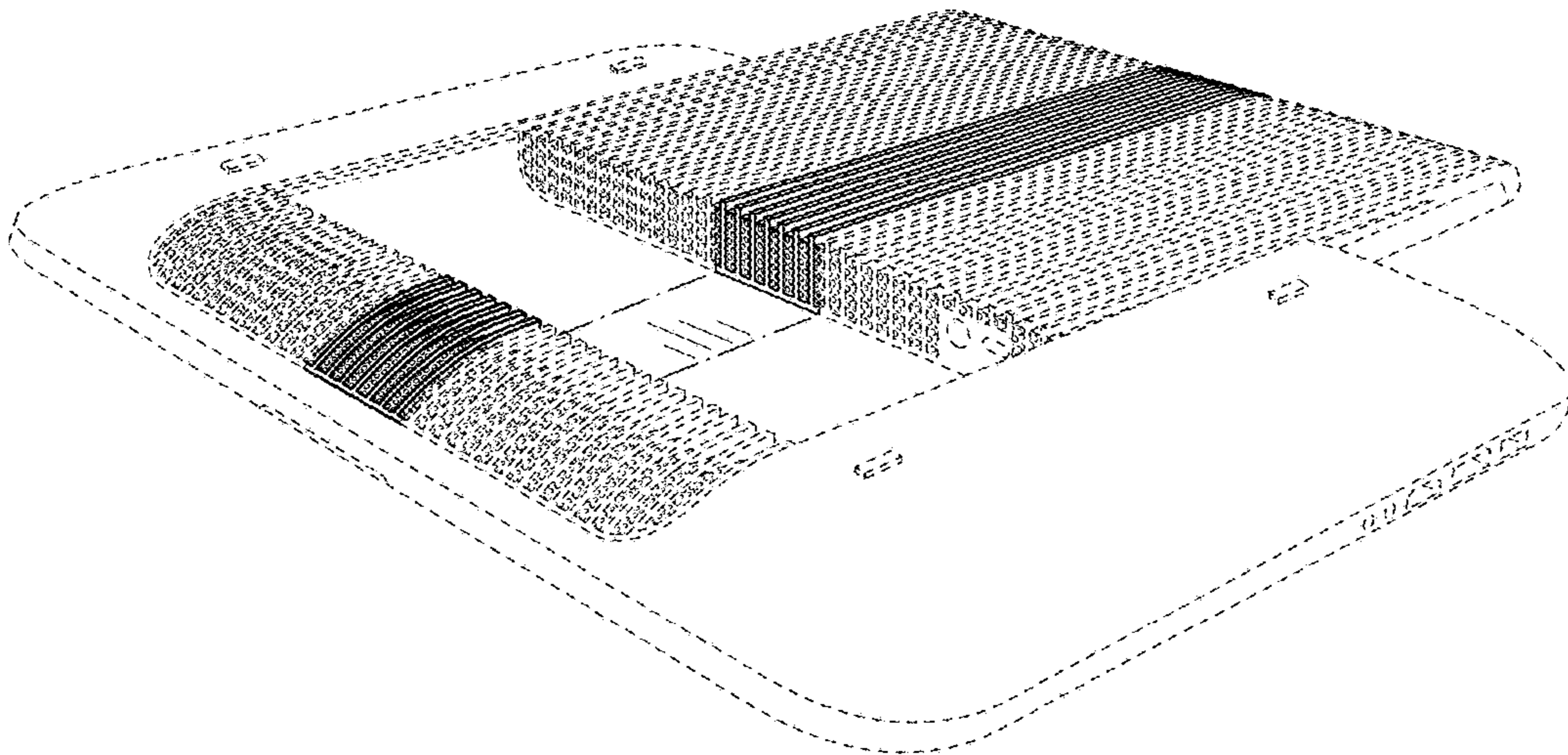


FIG. 6

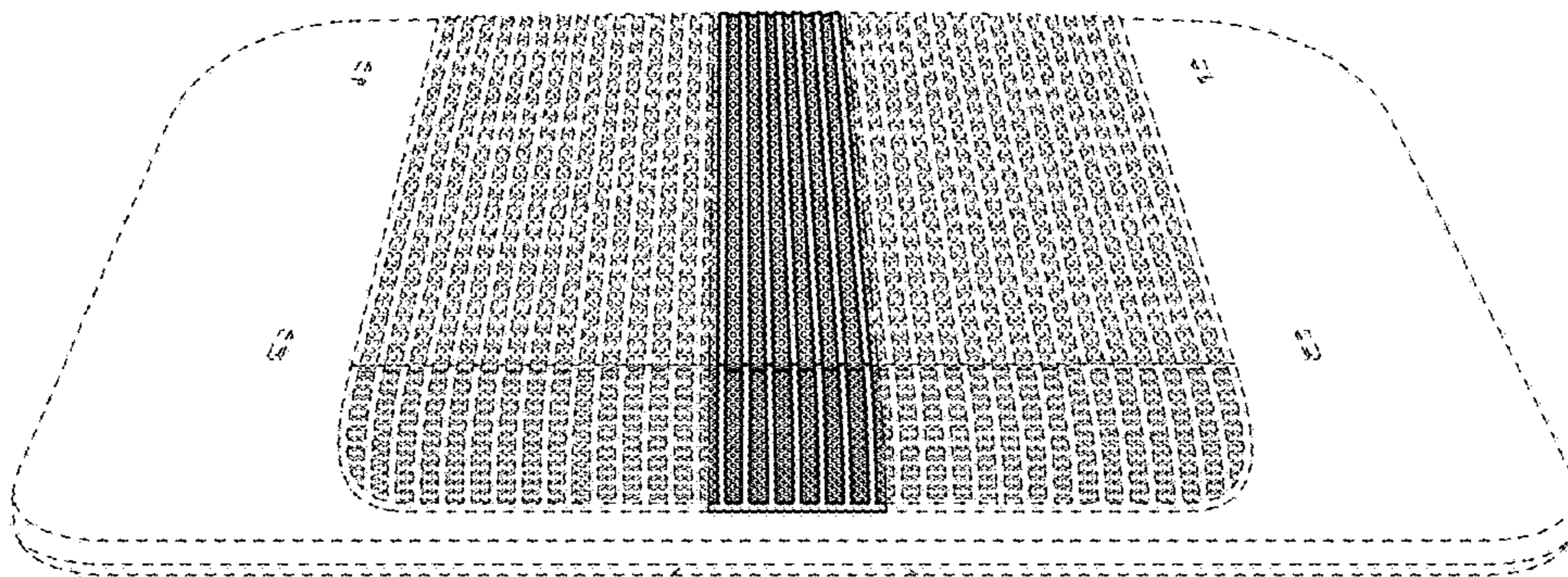


FIG. 7

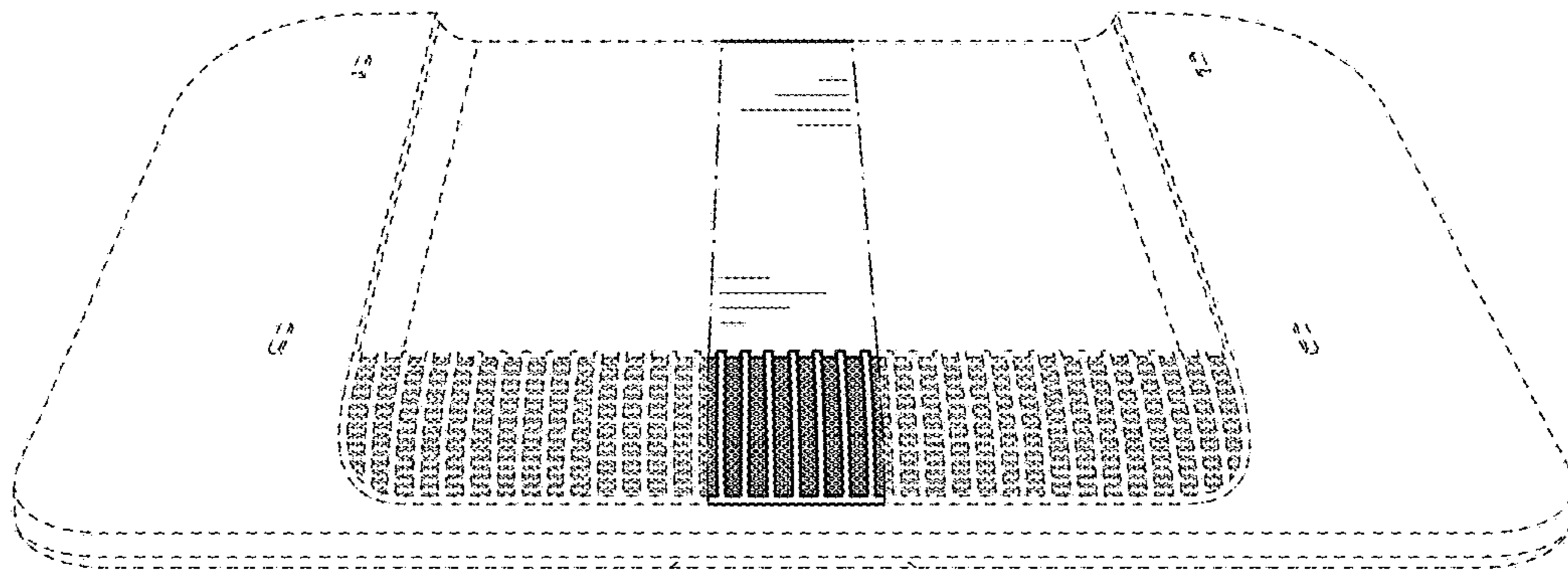


FIG. 8

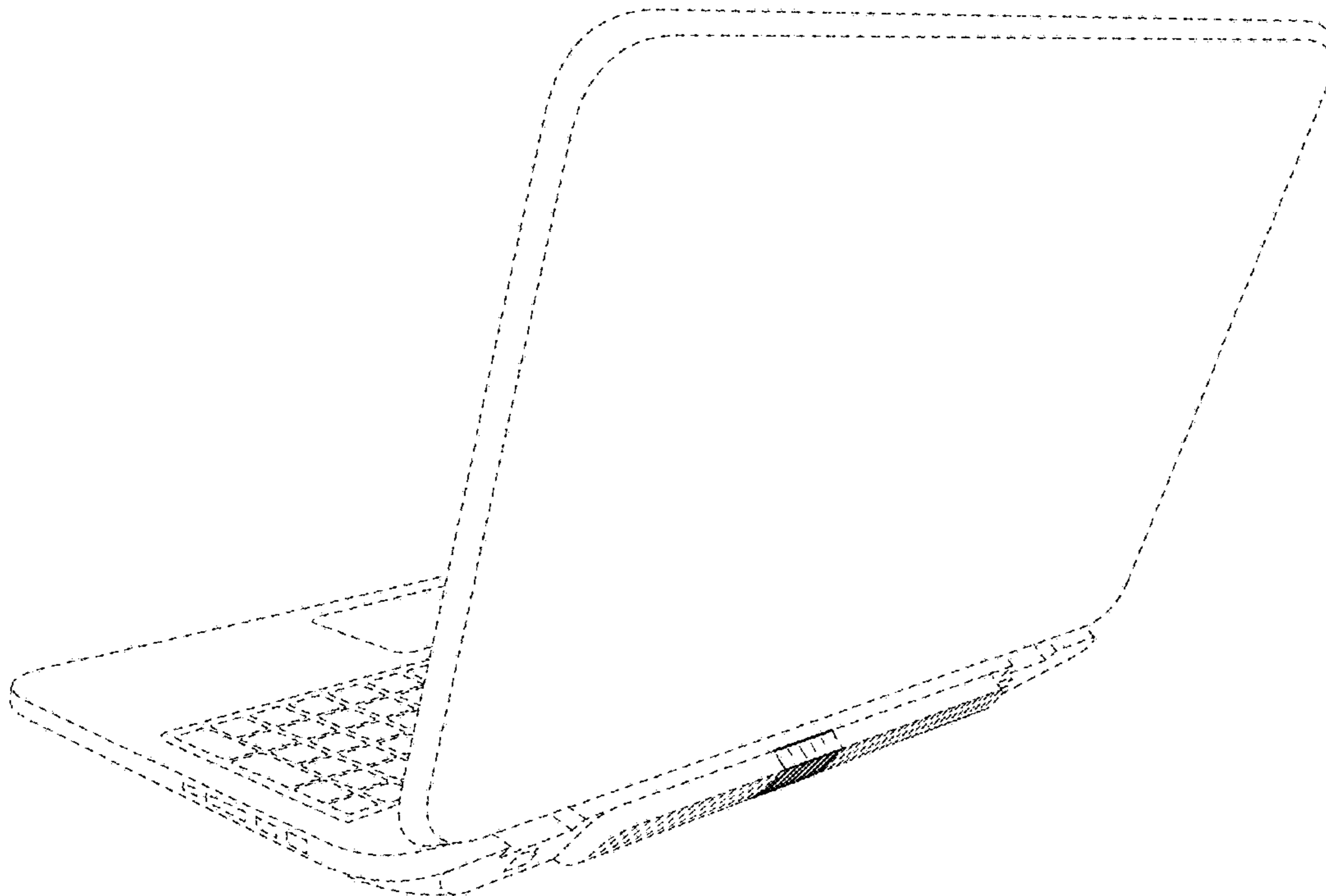


FIG. 9

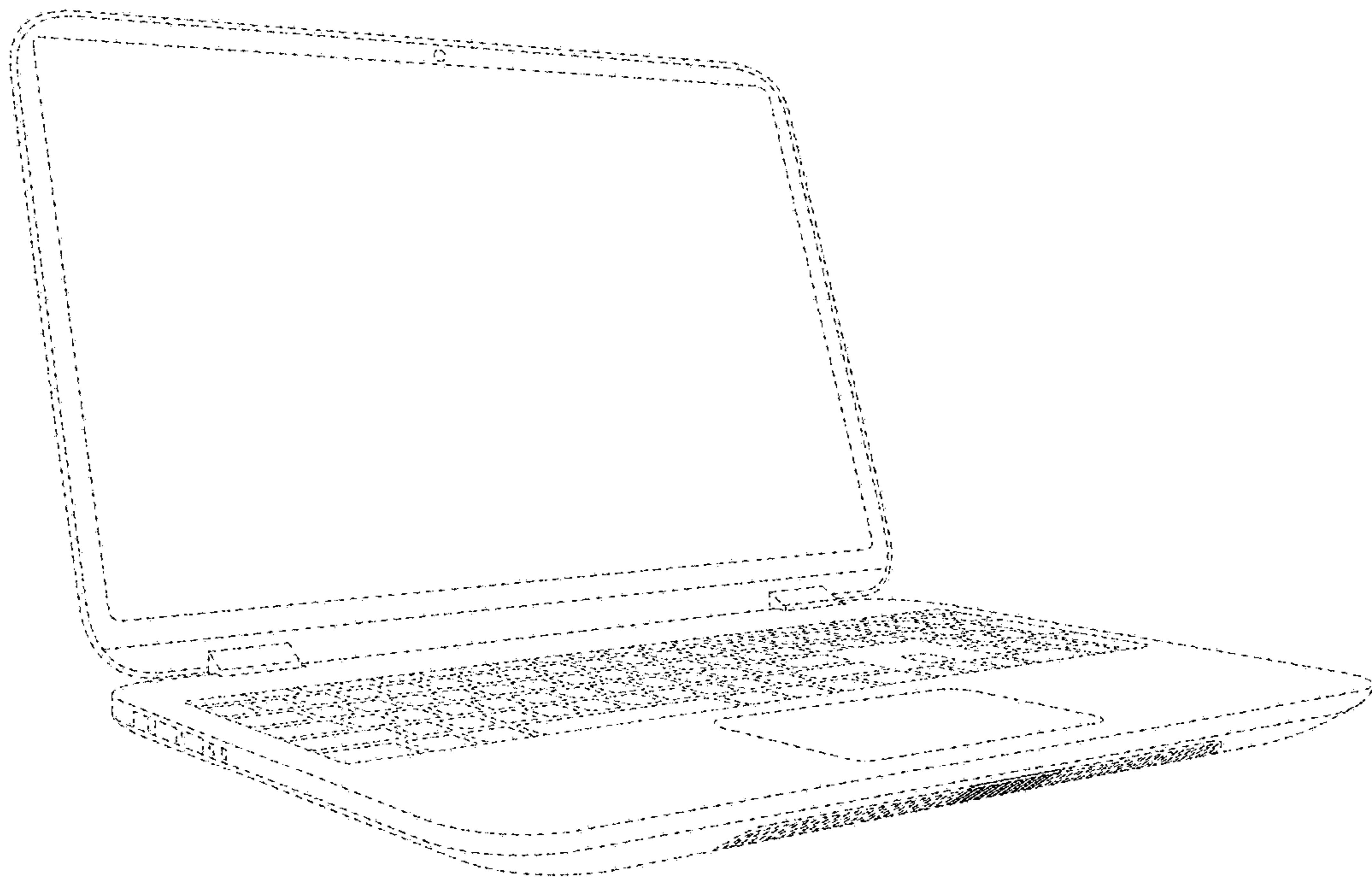


FIG. 10

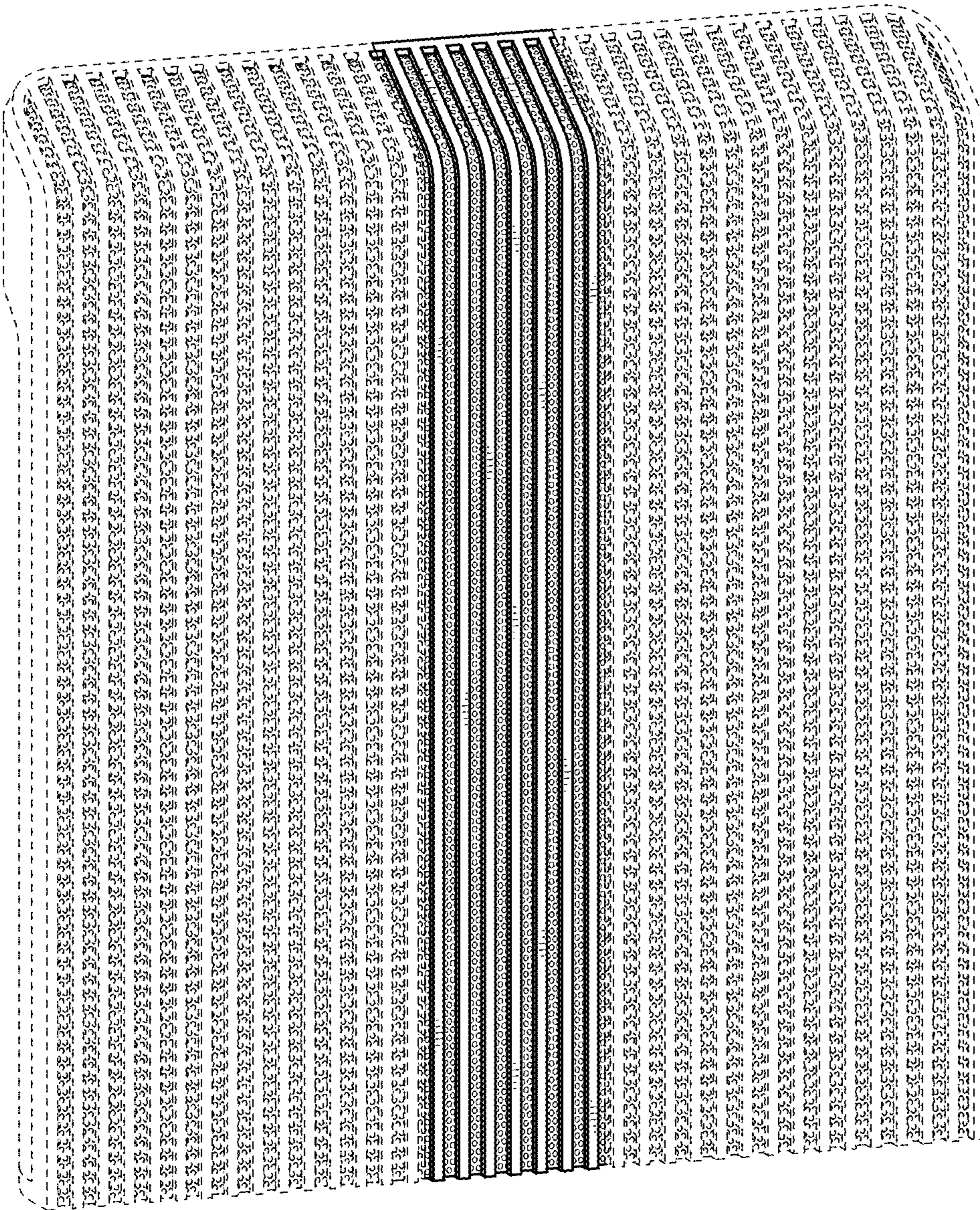


FIG. 11

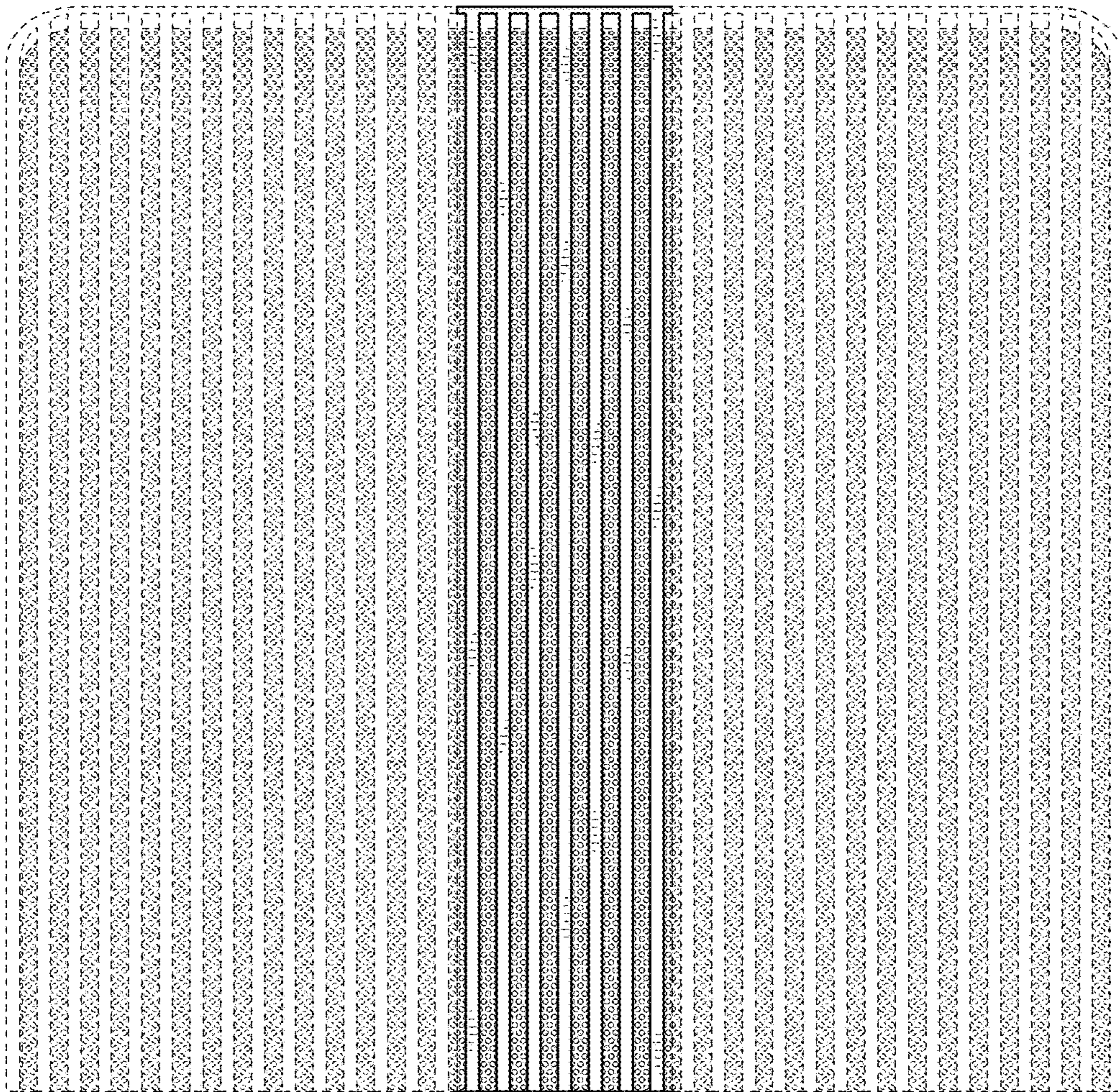


FIG. 12



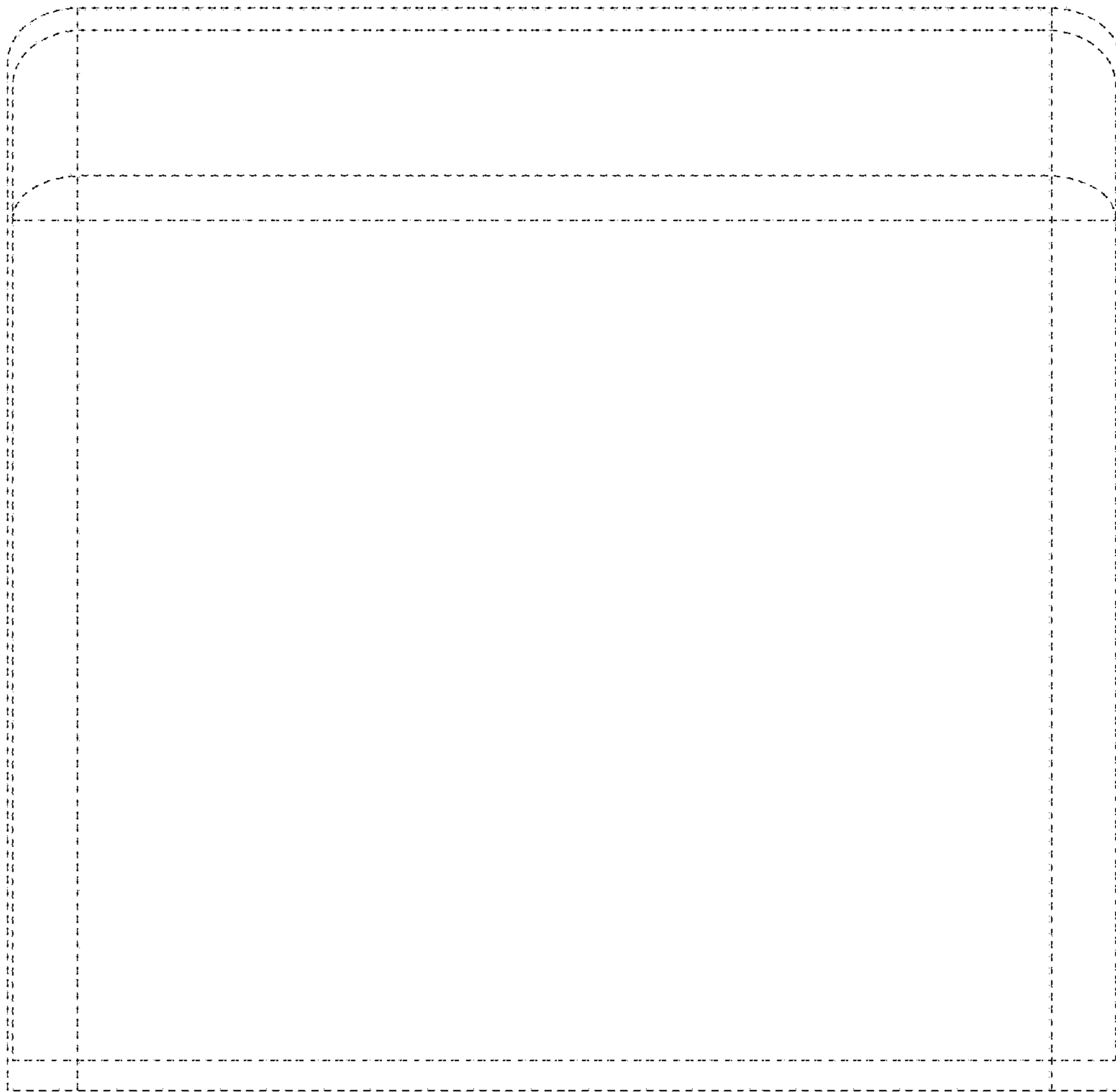


FIG. 13

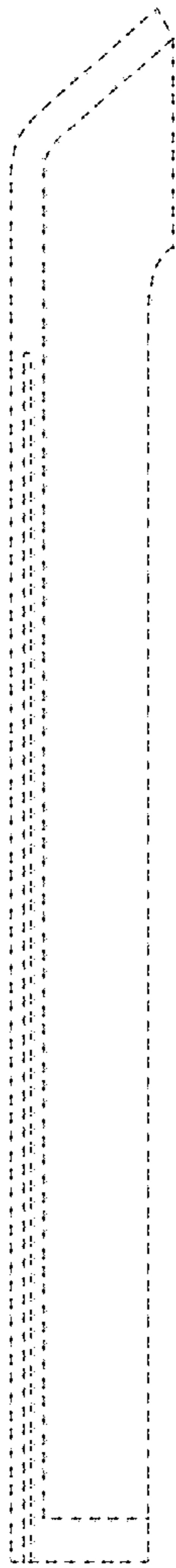


FIG. 14

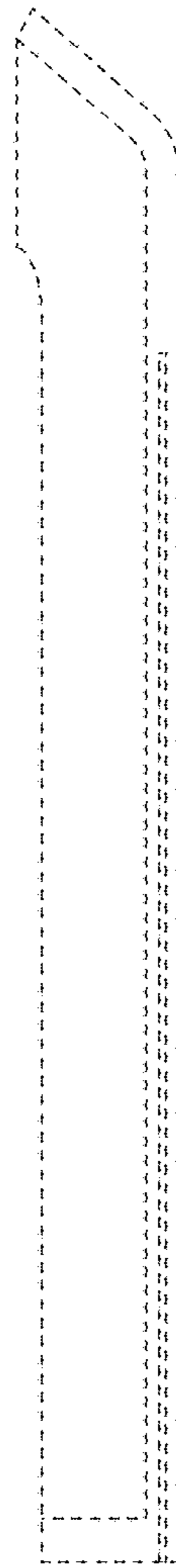


FIG. 15

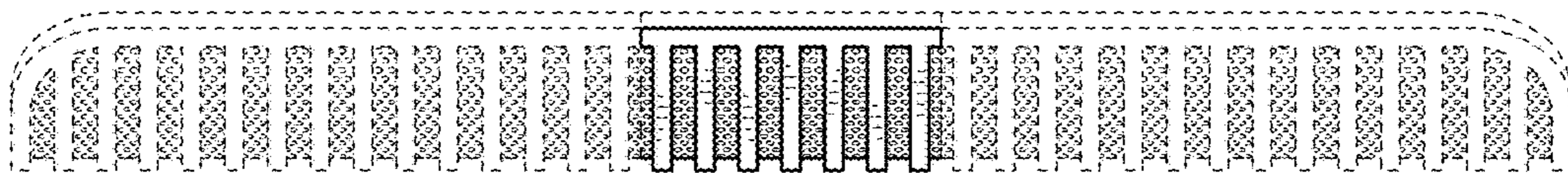


FIG. 16

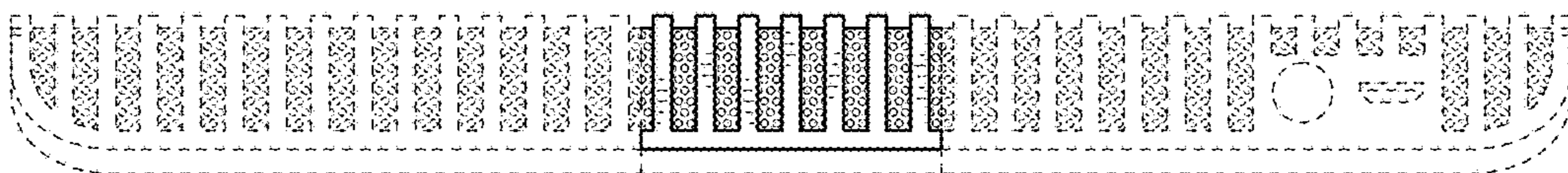


FIG. 17