



US00D820244S

(12) **United States Design Patent**
Cowles et al.

(10) **Patent No.:** **US D820,244 S**
(45) **Date of Patent:** **** *Jun. 12, 2018**

(54) **ELECTRONIC HOUSING**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **15 Years**

(21) Appl. No.: **29/542,181**

(22) Filed: **Oct. 12, 2015**

Related U.S. Application Data

(63) Continuation-in-part of application No. 29/480,293, filed on Jan. 24, 2014, now Pat. No. Des. 765,066.

(51) **LOC (11) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/239**

(58) **Field of Classification Search**

USPC D14/135, 137, 139, 140-140.9, 155, 167, D14/240, 242, 243, 348, 349, 351, 354, D14/355, 356, 357, 358, 385, 433, 435, D14/435.1, 436, 474, 480, 496; D13/103, D13/108, 110, 147; D10/75

CPC H04L 12/00; H03K 17/00; H04W 88/00; H04W 88/005; H04W 88/02; H04W 88/08;

(Continued)

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(57) **CLAIM**

The ornamental design for an electronic housing, as shown and described.

DESCRIPTION

FIG. 1 shows a front perspective view of an electronic housing.

FIG. 2 shows a top plan view of the electronic housing.

FIG. 3 shows a bottom plan view of the electronic housing.

FIG. 4 shows a front elevation view of the electronic housing.

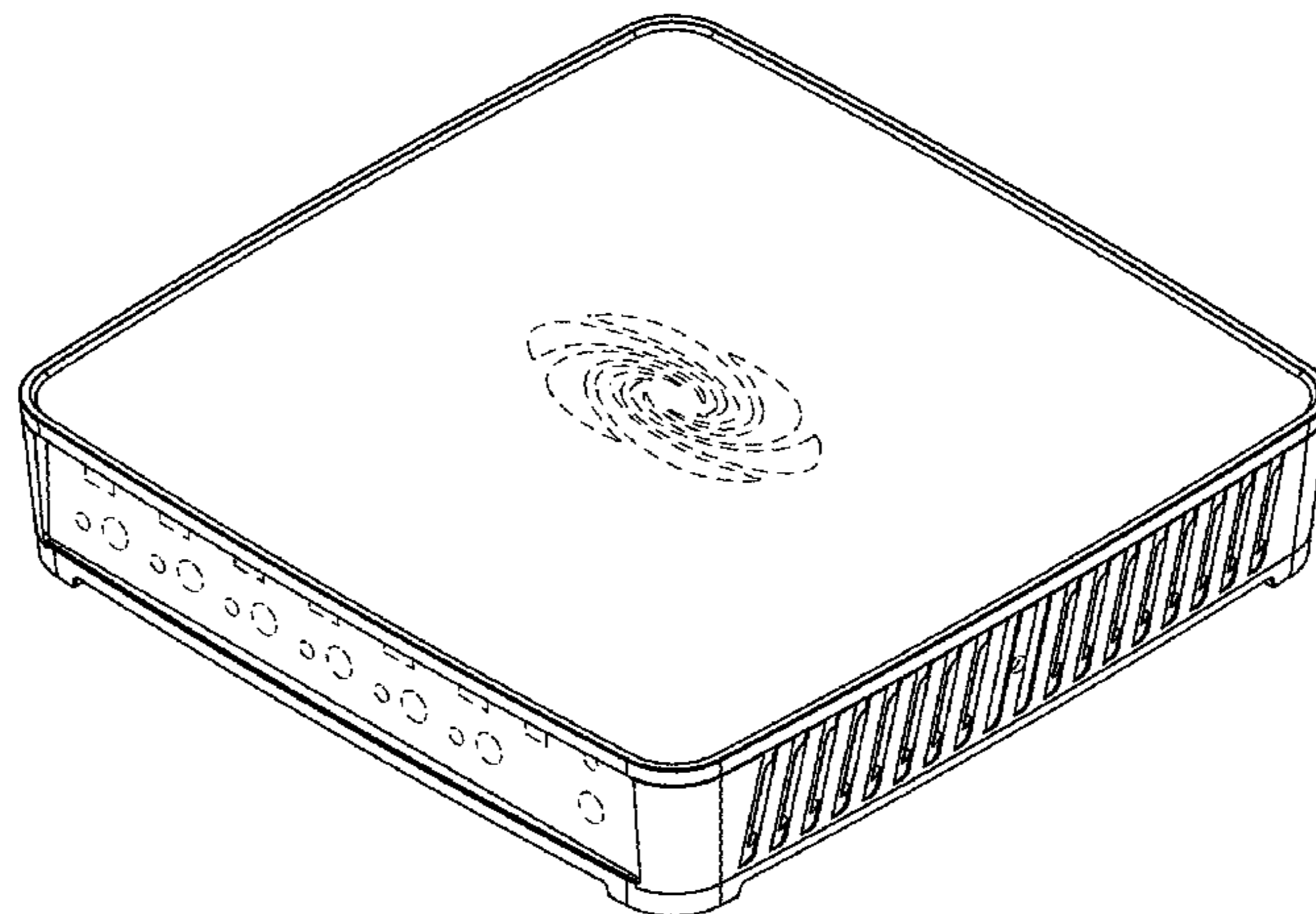
FIG. 5 shows a back elevation view of the electronic housing; and,

FIG. 6 shows a right side elevation view of the electronic housing, a left side elevation view of the electronic housing being a mirror image thereof.

The broken lines that are immediately adjacent to the claimed surfaces shown in the figures define unshaded regions which represent the bounds of the claim. All other broken lines, including the logo superimposed atop a continuously claimed surface, show environmental subject matter; the broken lines form no part of the claimed design.

The “logo” shown in the drawings (e.g., FIGS. 1 and 2) is a registered trademark of Crestron Electronics, Inc., Rockleigh, NJ, USA.

1 Claim, 6 Drawing Sheets



(58) **Field of Classification Search**

CPC .. H04W 88/085; H04W 88/10; H04W 88/12;
H04W 88/14; H04B 1/38

See application file for complete search history.

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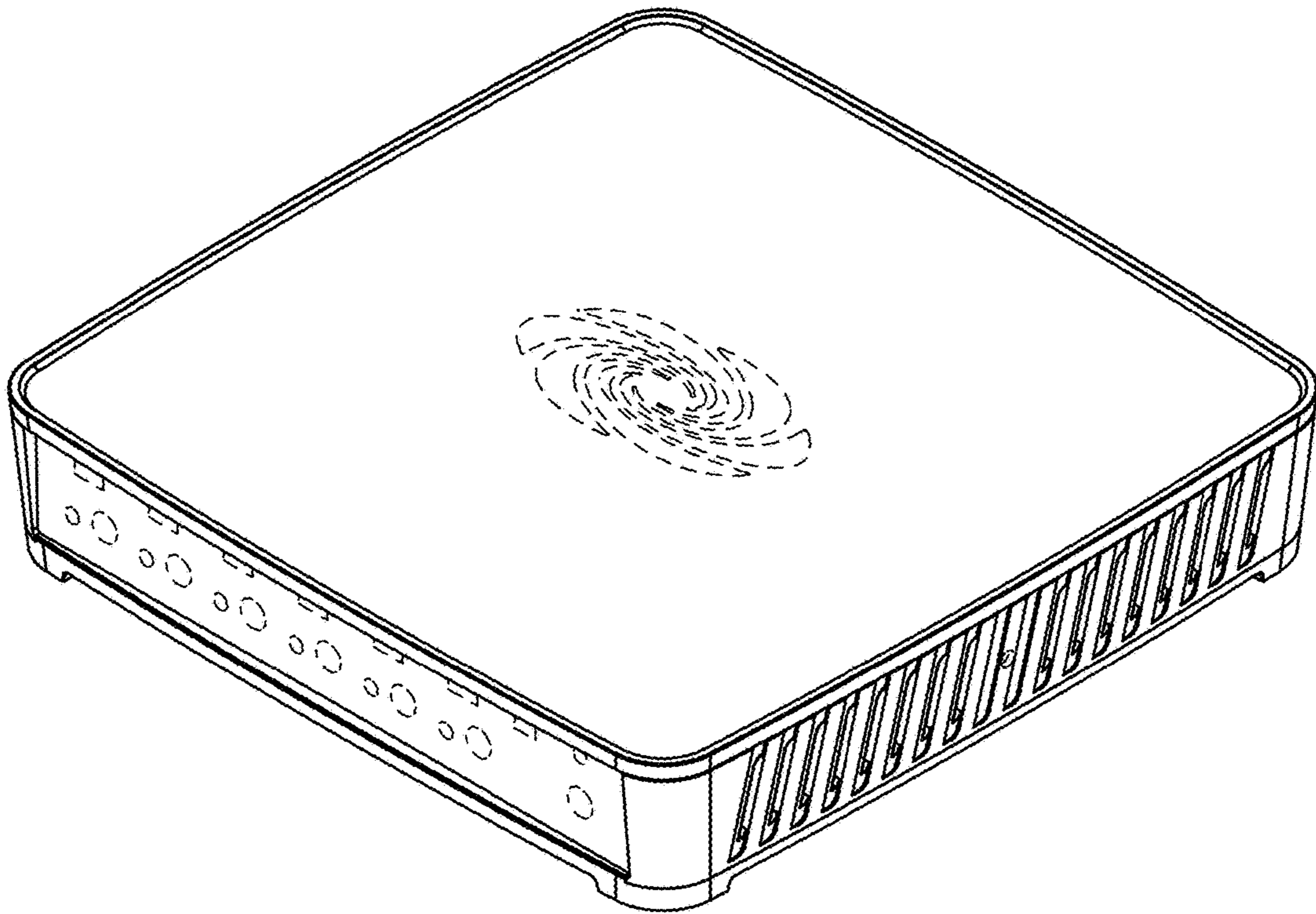


FIG. 1

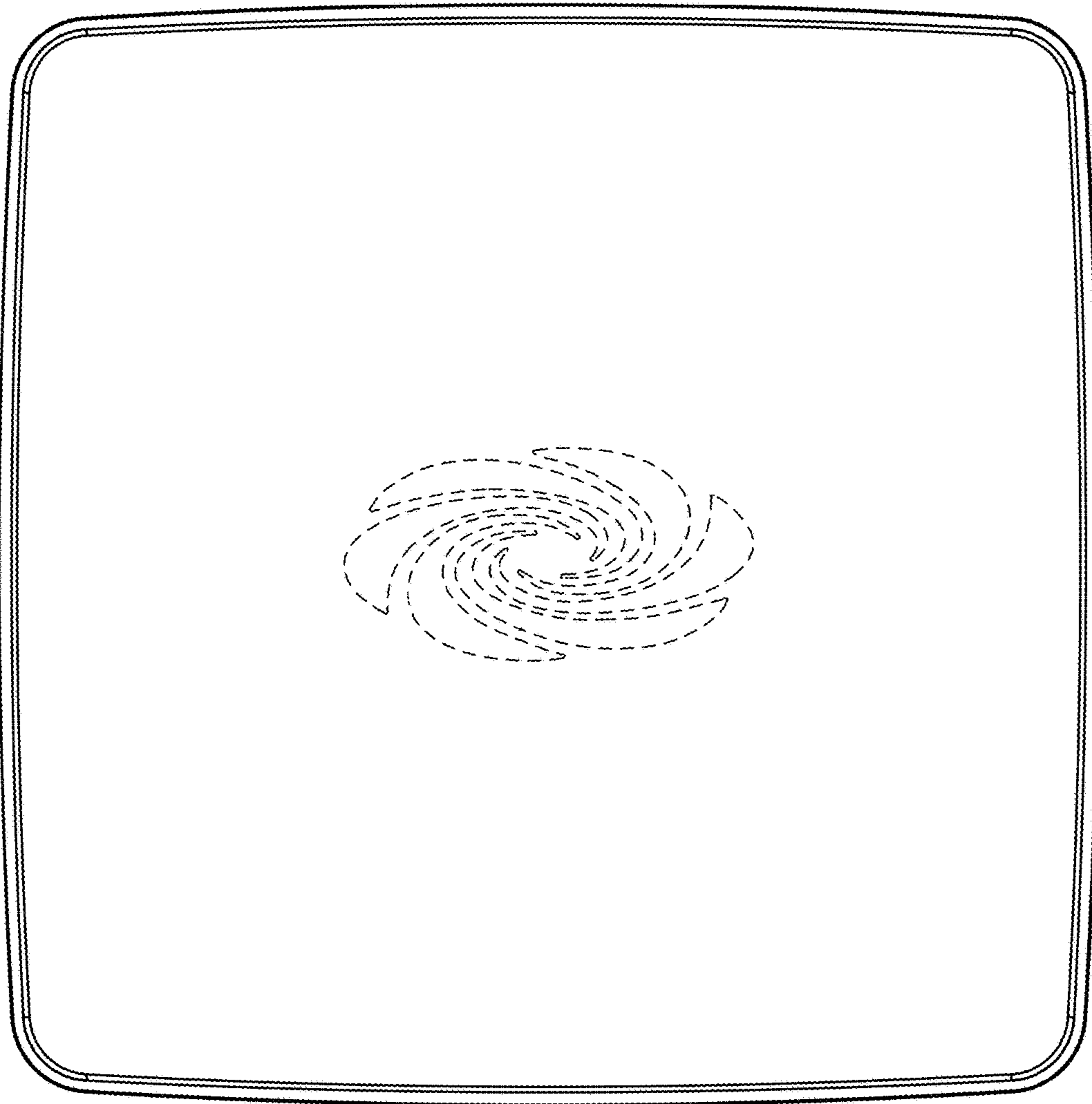


FIG. 2

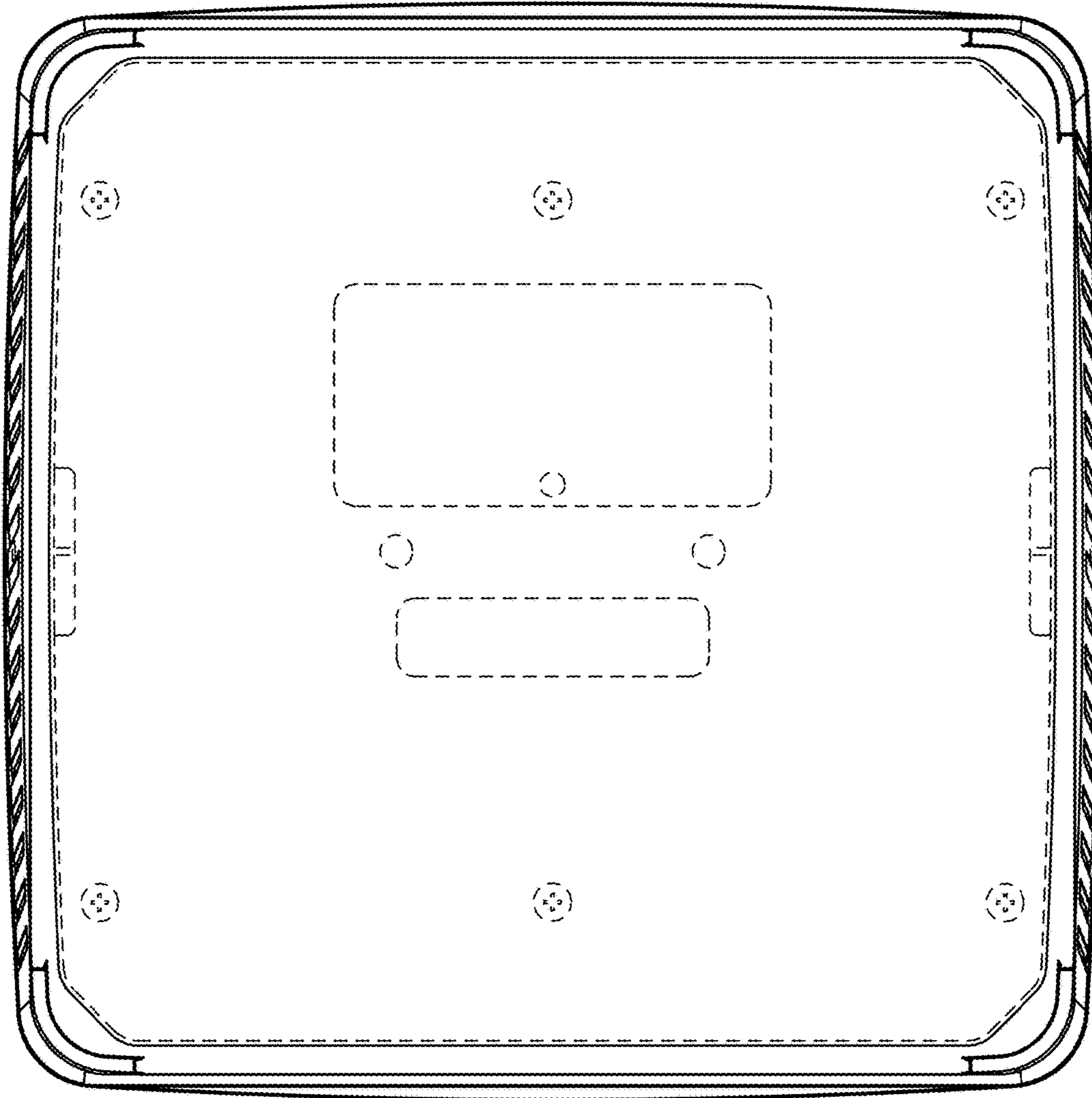


FIG. 3

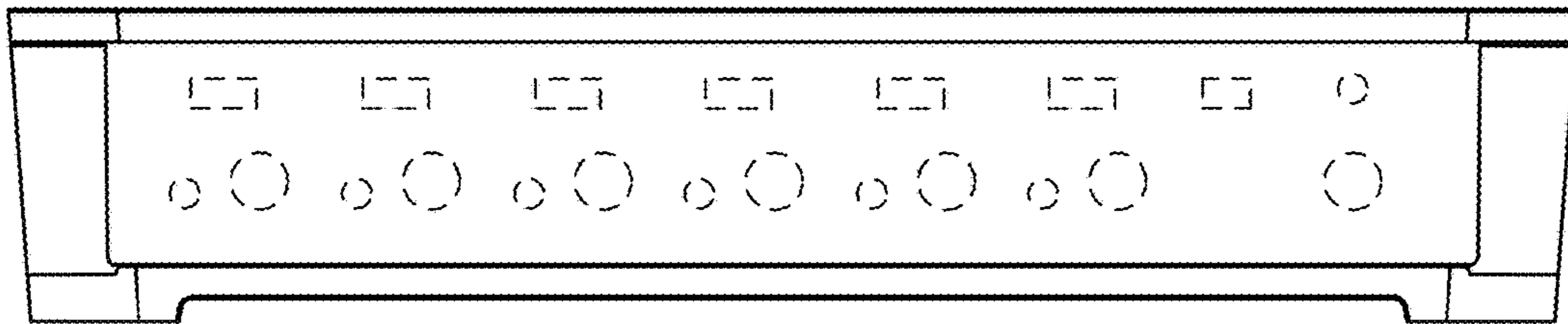


FIG. 4

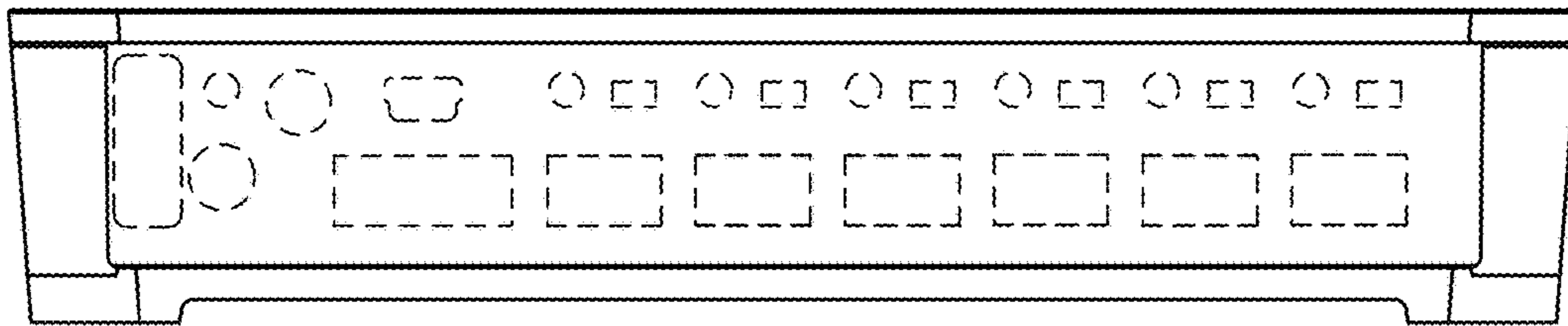


FIG. 5

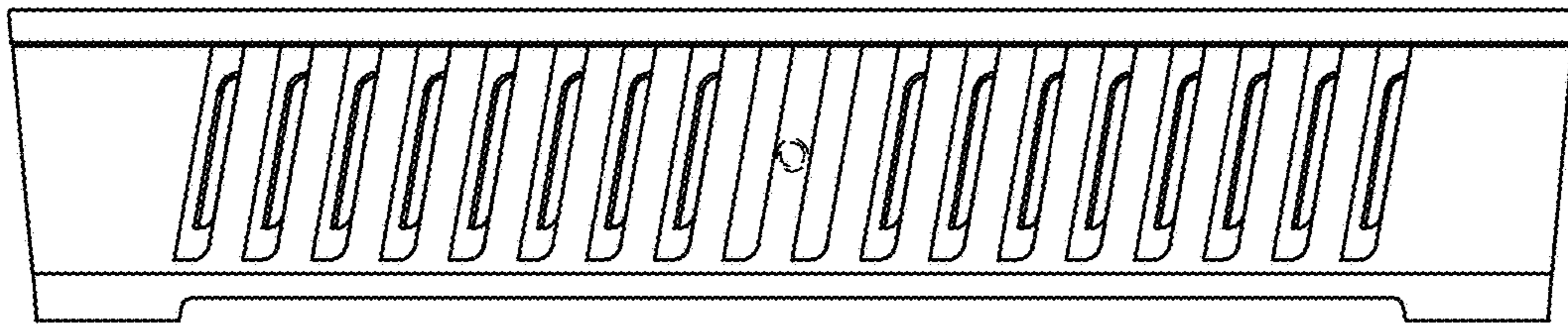


FIG. 6