



US00D819645S

(12) **United States Design Patent** (10) **Patent No.:** **US D819,645 S**
Roberts et al. (45) **Date of Patent:** **** Jun. 5, 2018**

(54) **ELECTRONIC DEVICE COVER**
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(**) Term: **15 Years**

(21) Appl. No.: **29/580,592**

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(30) **Foreign Application Priority Data**

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(51) **LOC (11) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/440**

(58) **Field of Classification Search**
USPC D14/440, 250, 447, 426, 330; 206/45.23,
206/320, 45.2; 361/679.55; 294/25
CPC G06F 1/1628; G06F 1/1626; G06F 1/669;
A47B 23/044; H04B 1/3888
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electronic device cover, as shown and described.

DESCRIPTION

This application is related to co-pending applications U.S. Ser. No. 29/580,567 and U.S. Ser. No. 29/580,599, filed on the same day as this application, both herein incorporated by reference.

FIG. 1 is a perspective view taken from the rear, top and right side of the electronic device cover of the present design.

FIG. 2 is a perspective view taken from the front, top and right side thereof.

FIG. 3 is a front elevation view thereof.

FIG. 4 is a front elevation view thereof.

FIG. 5 is a left side elevation view thereof.

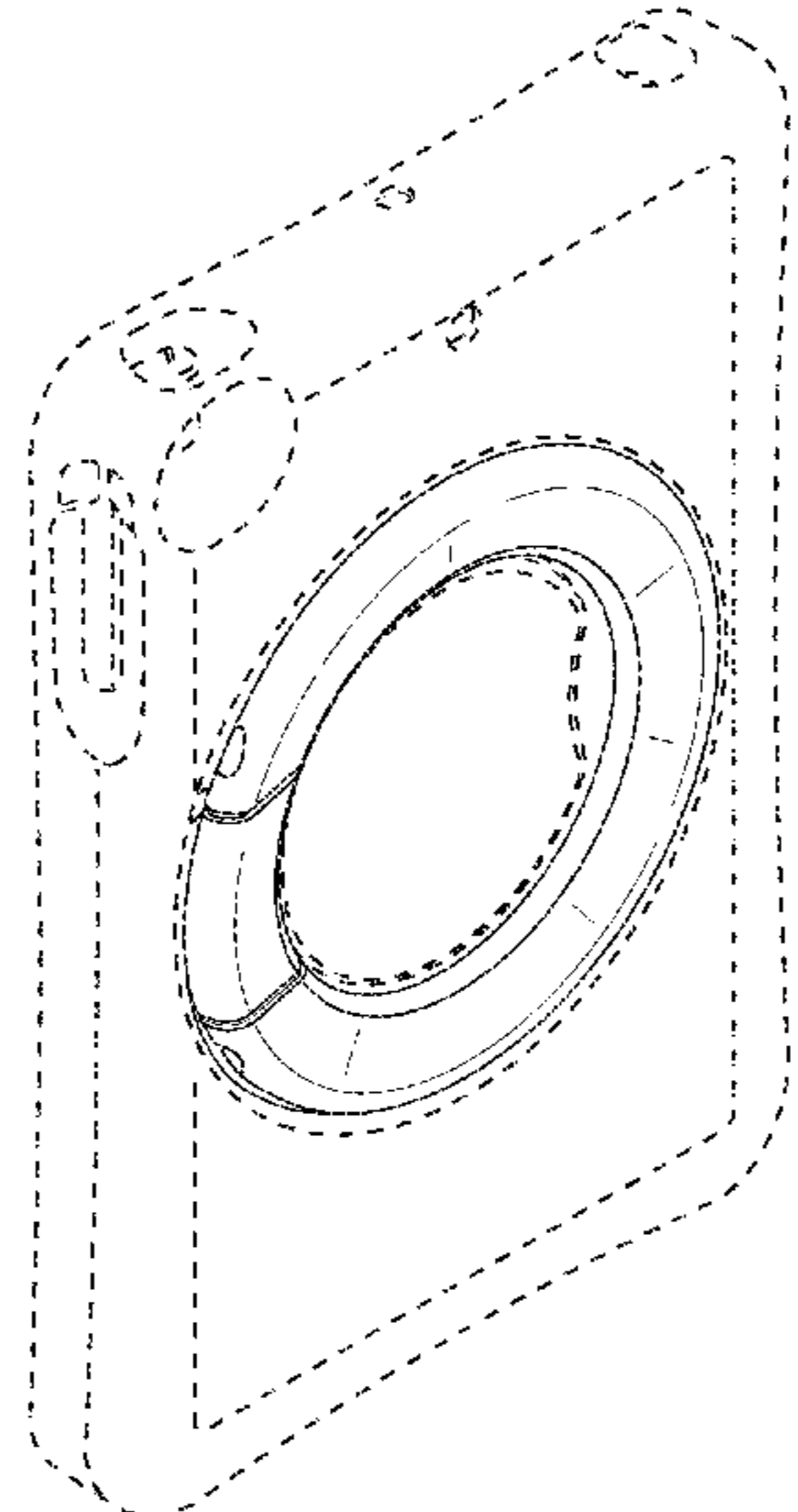
FIG. 6 is a right side elevation view thereof.

FIG. 7 is a top plan view thereof; and,

FIG. 8 is a bottom plan view thereof.

Portions drawn in broken lines in the figures represent environmental aspects of how the design is applied but form no part of the claimed design.

1 Claim, 7 Drawing Sheets



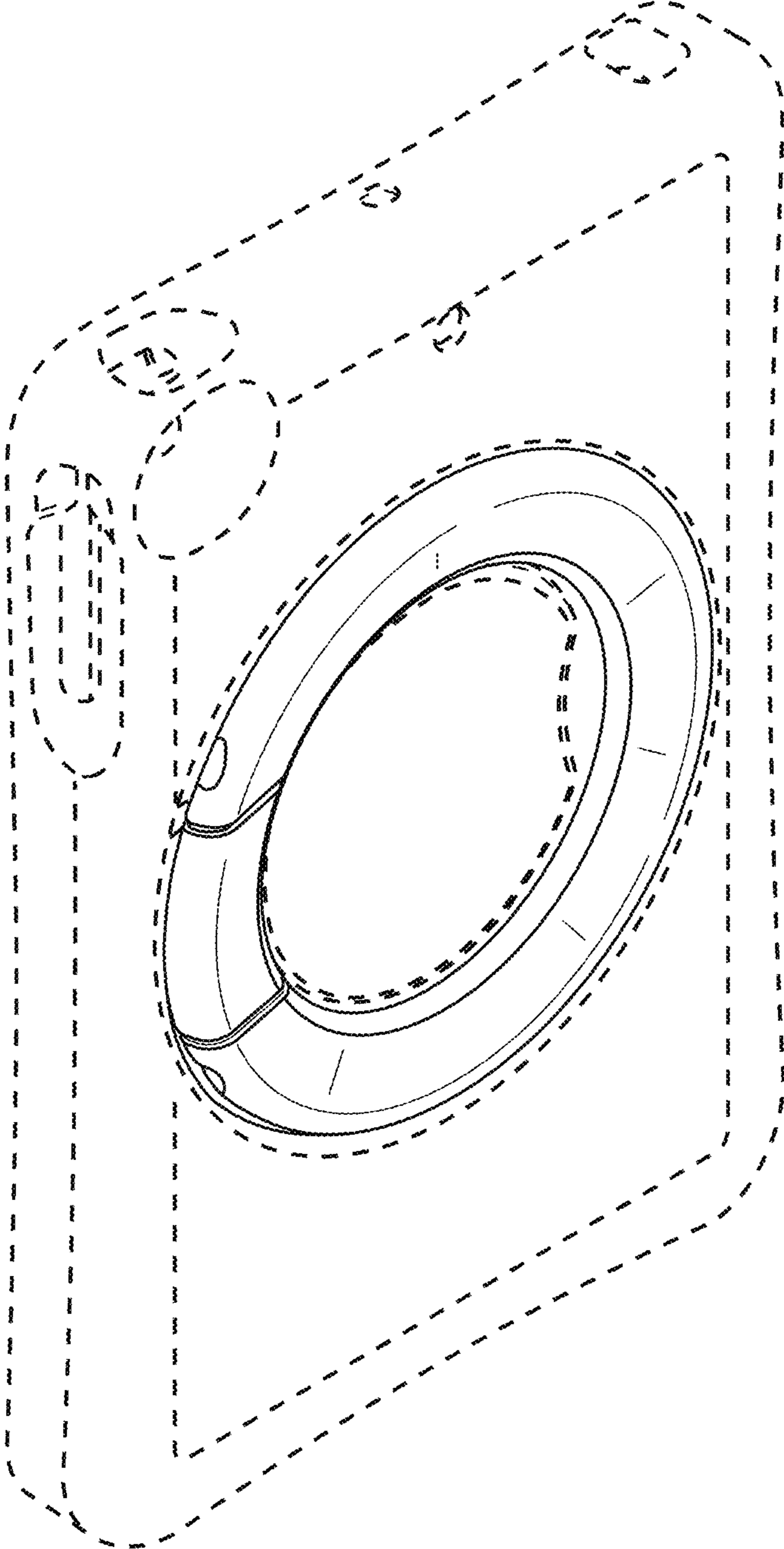


FIG. 1

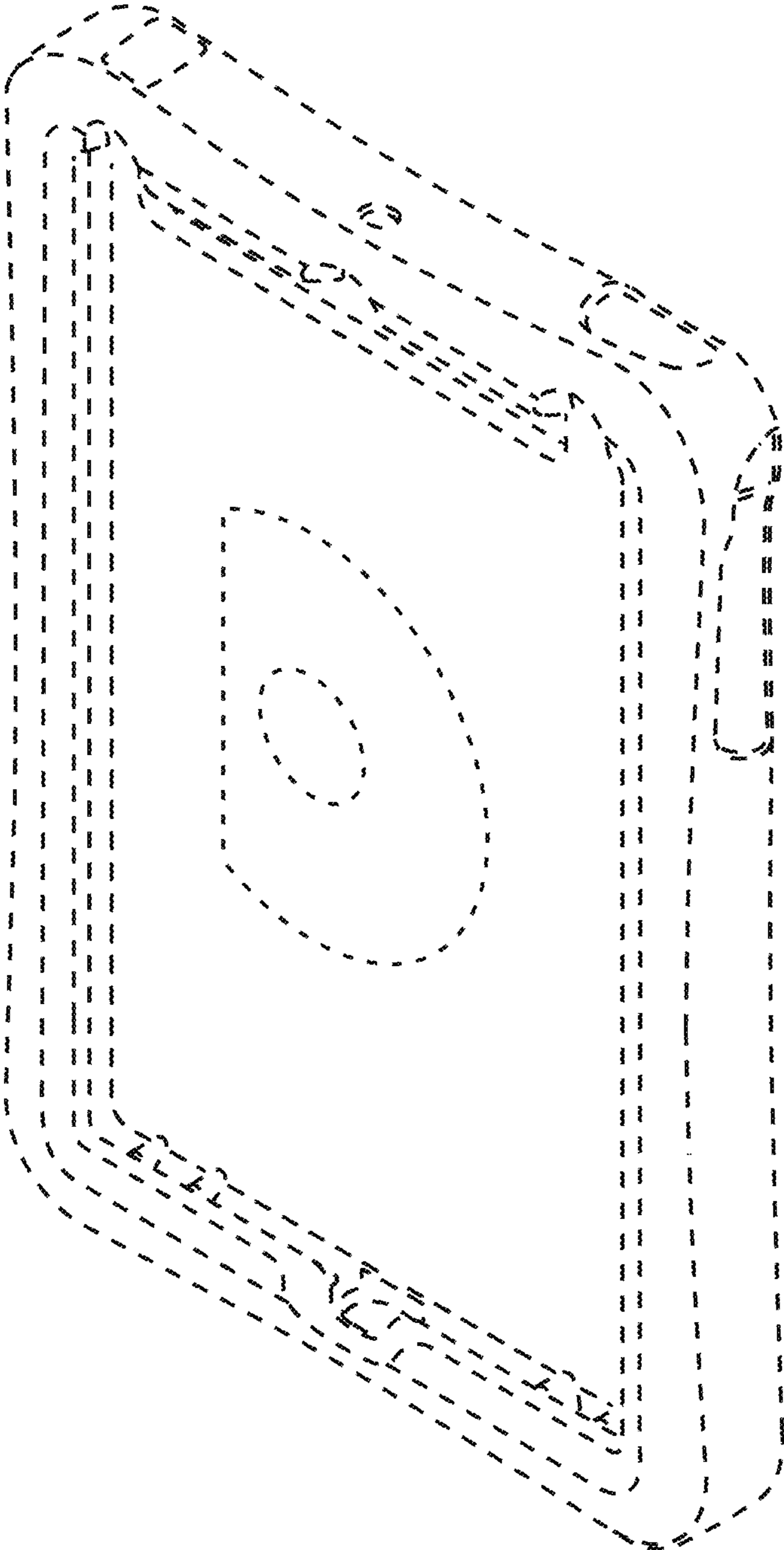


FIG. 2

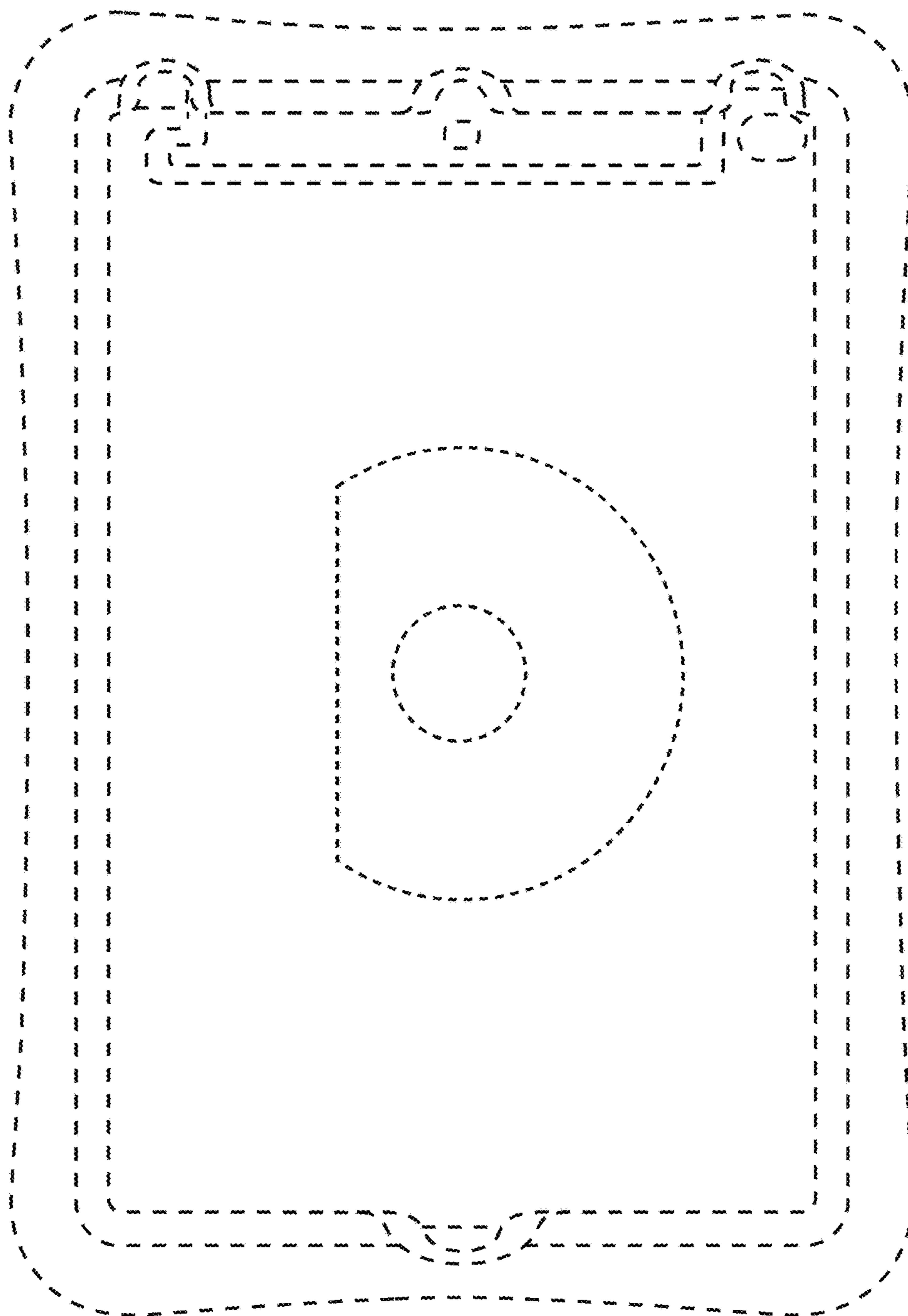


FIG. 3

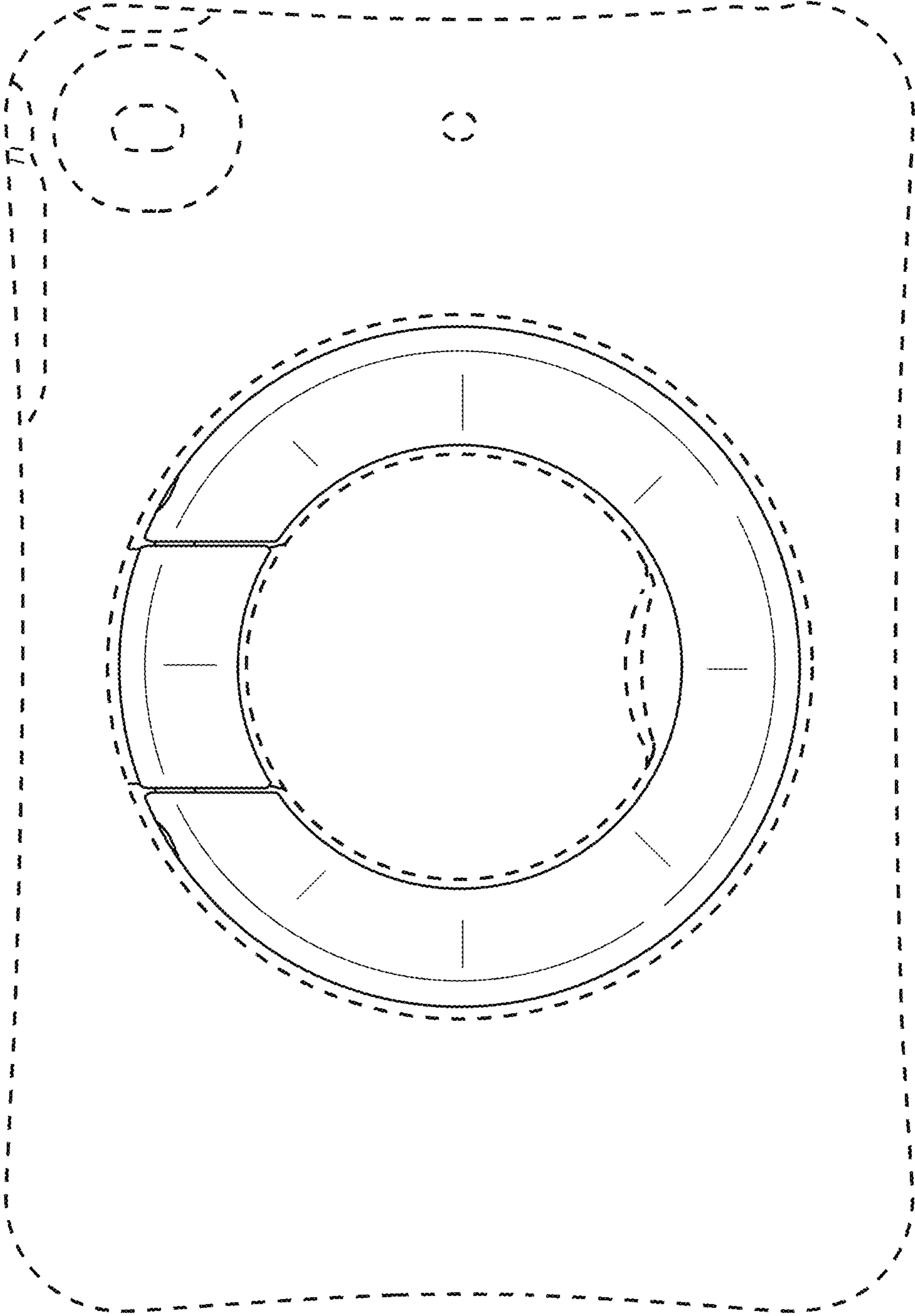


FIG. 4

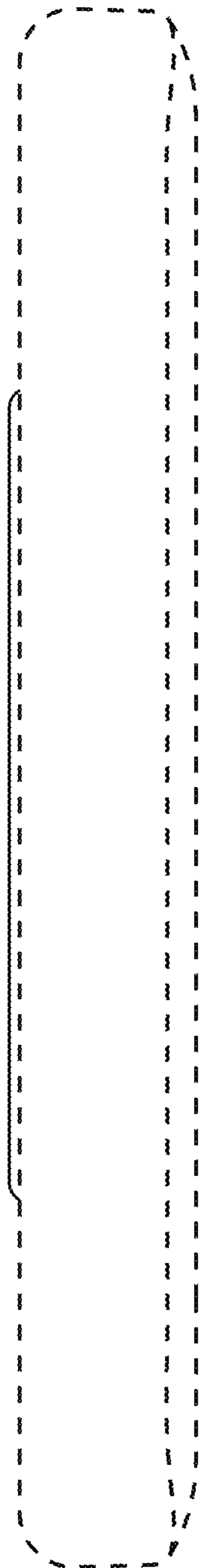


FIG. 5

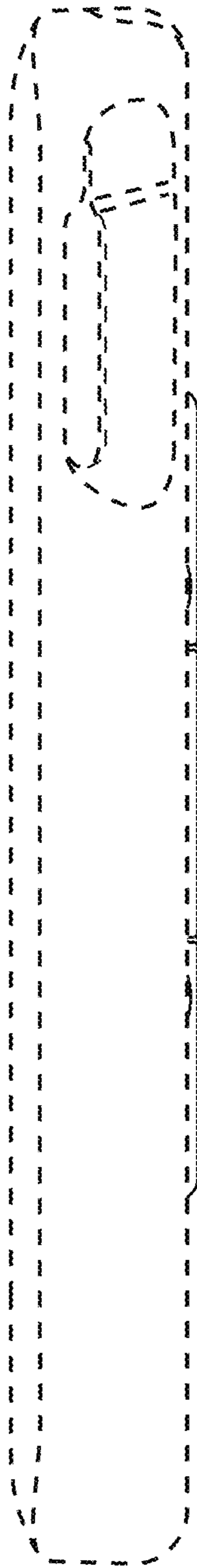


FIG. 6

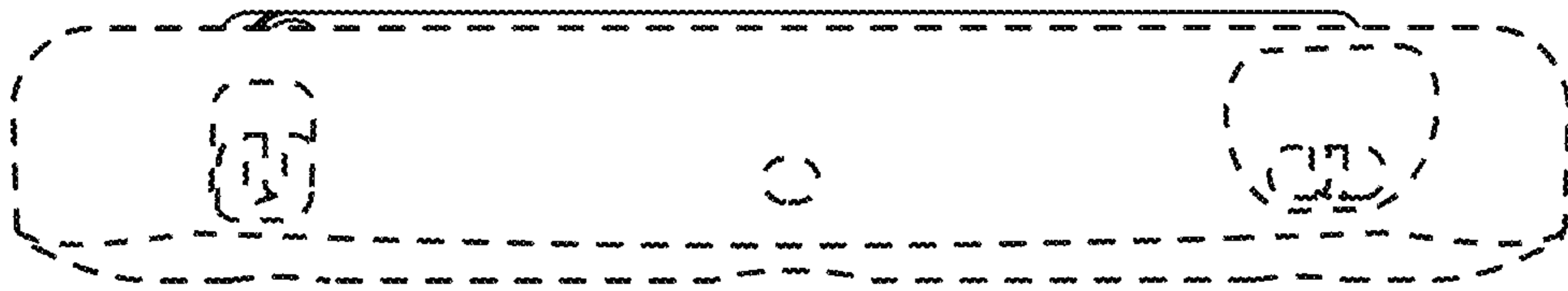


FIG. 7

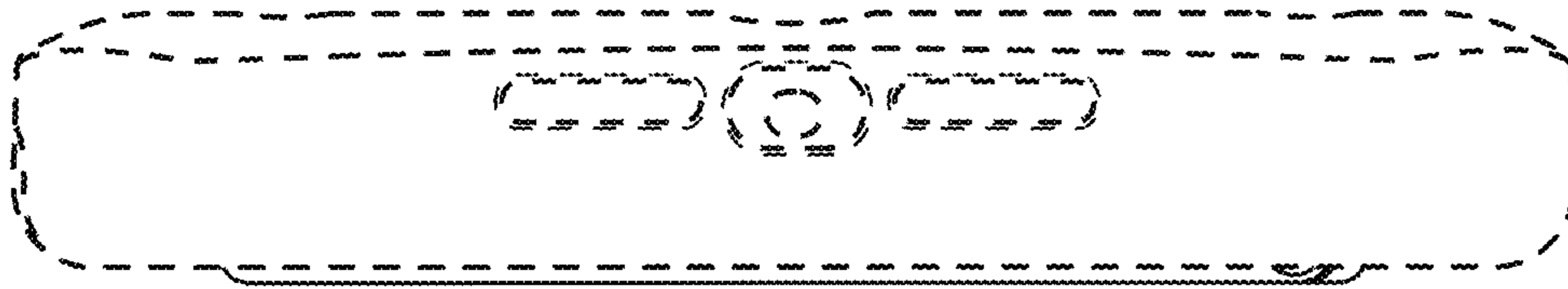


FIG. 8