



US00D817884S

(12) **United States Design Patent** (10) **Patent No.:** **US D817,884 S**
Salas et al. (45) **Date of Patent:** **** May 15, 2018**

(54) **ELECTRICAL OUTLET**
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(73) Assignee: **EATON INTELLIGENT POWER LIMITED**, Dublin (IE)
(**) Term: **15 Years**
(21) Appl. No.: **29/547,555**
(22) Filed: **Dec. 4, 2015**
(51) **LOC (11) Cl.** **13-03**
(52) **U.S. Cl.**
USPC **D13/137.2**
(58) **Field of Classification Search**
USPC D13/137.1-137.2, 138.1, 138.2, 139.6, D13/152, 184, 108, 177, 156, 154, 119, D13/103, 162, 133, 110; D8/349, 350, D8/354; D14/240, 383, 239, 385; 174/53
CPC H02H 13/16; H02G 3/088
See application file for complete search history.

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(Continued)

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(57) **CLAIM**

The ornamental design for an electrical outlet, as shown and described.

DESCRIPTION

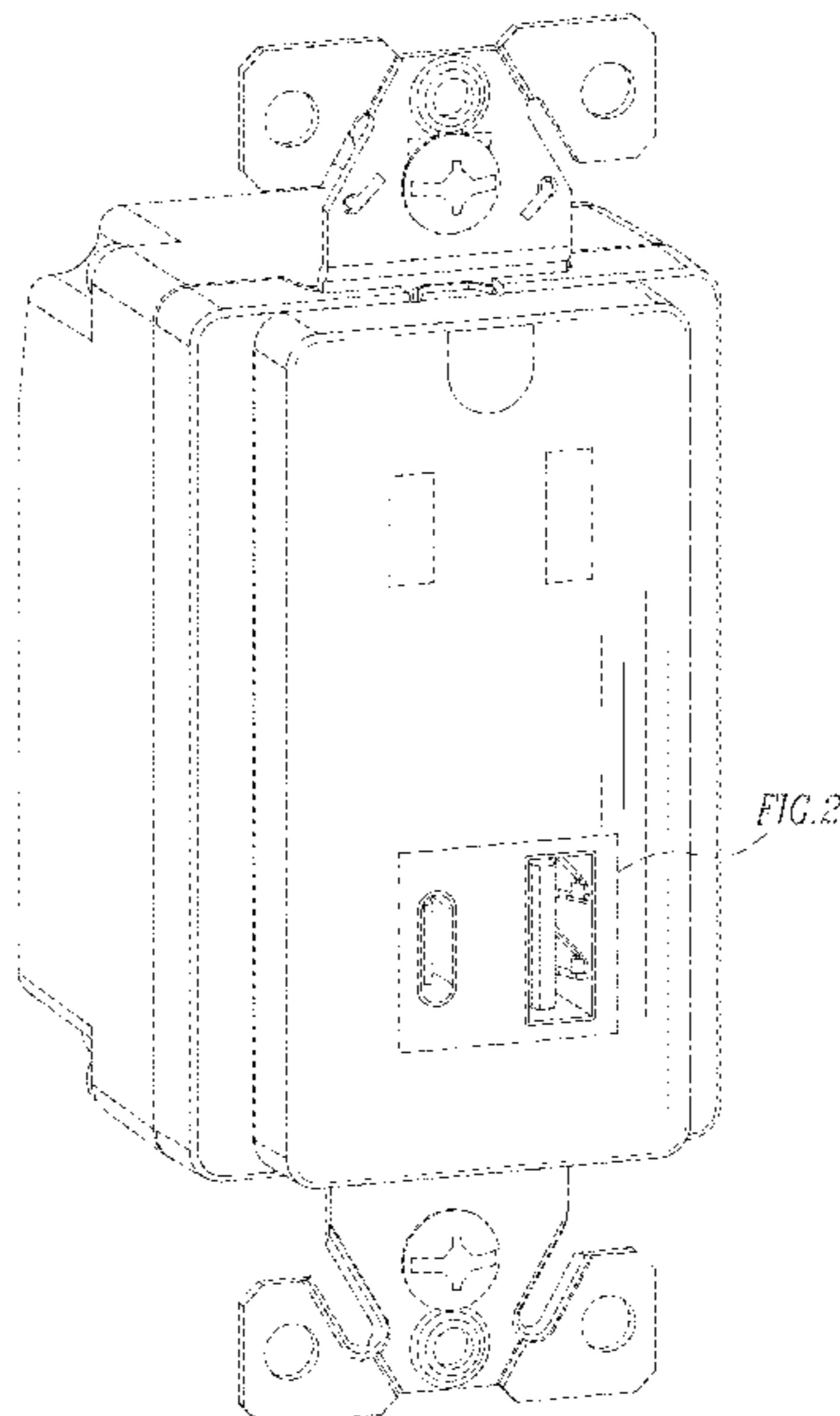
FIG. 1 is a left perspective view of the electrical outlet; FIG. 2 an enlargement of the indicated portion of FIG. 1; FIG. 3 is a right perspective view of the electrical outlet; FIG. 4 an enlargement of the indicated portion of FIG. 3; FIG. 5 is a front elevational view of the electrical outlet; FIG. 6 an enlargement of the indicated portion of FIG. 5; FIG. 7 is a left side elevational view of the electrical outlet; FIG. 8 is a right side elevational view of the electrical outlet; FIG. 9 is a rear elevational view of the electrical outlet; FIG. 10 is a top plan view of the electrical outlet; and, FIG. 11 is a bottom plan view of the electrical outlet. In the drawings, the dot-dash-dot broken lines are for the purpose of illustrating boundaries between the claimed design and environment. The evenly spaced broken lines are for the purpose of illustrating environment only. No subject matter shown in either the dot-dash-dot broken lines or evenly spaced broken lines forms a part of the claimed design.

1 Claim, 11 Drawing Sheets

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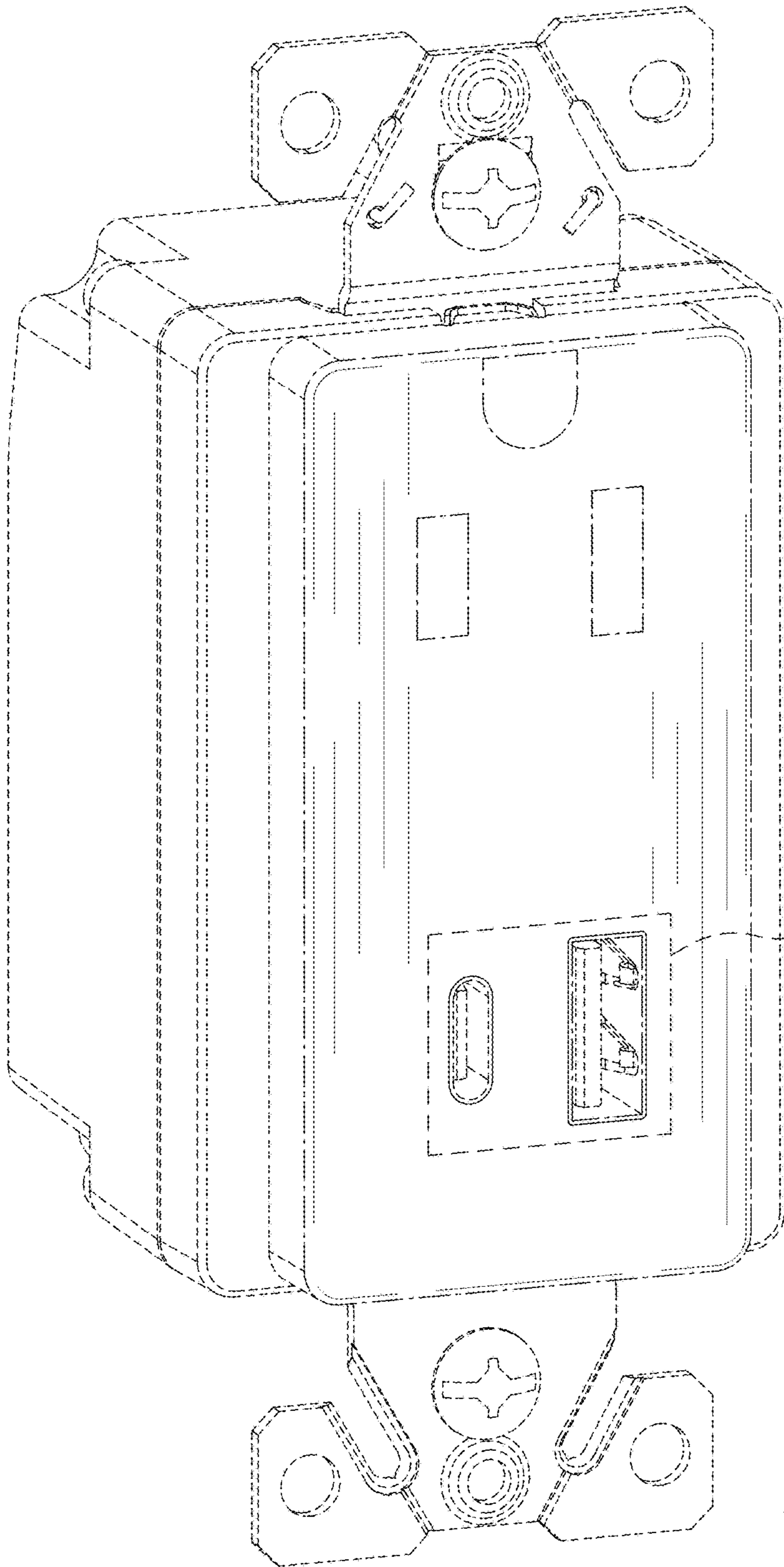
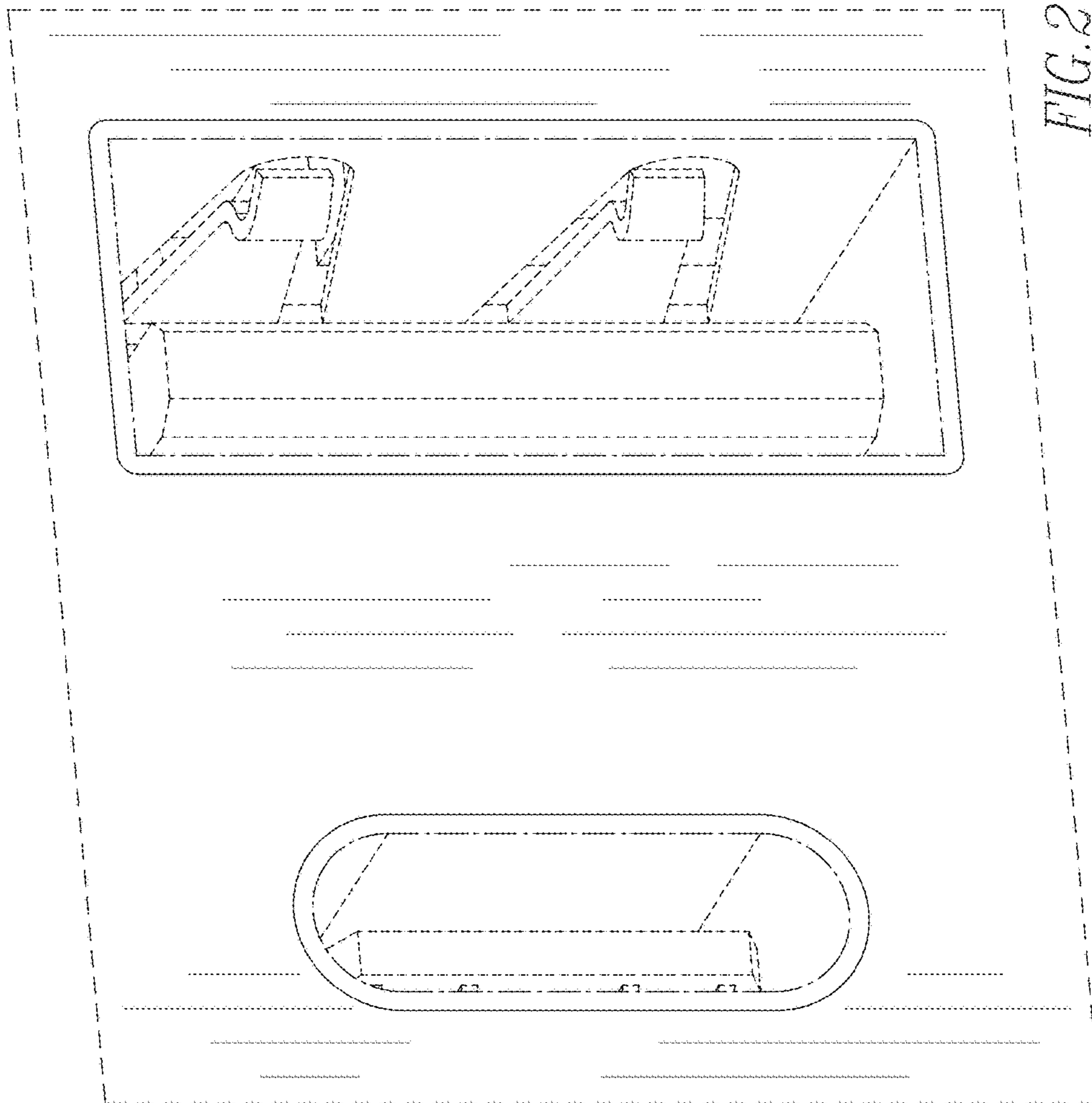
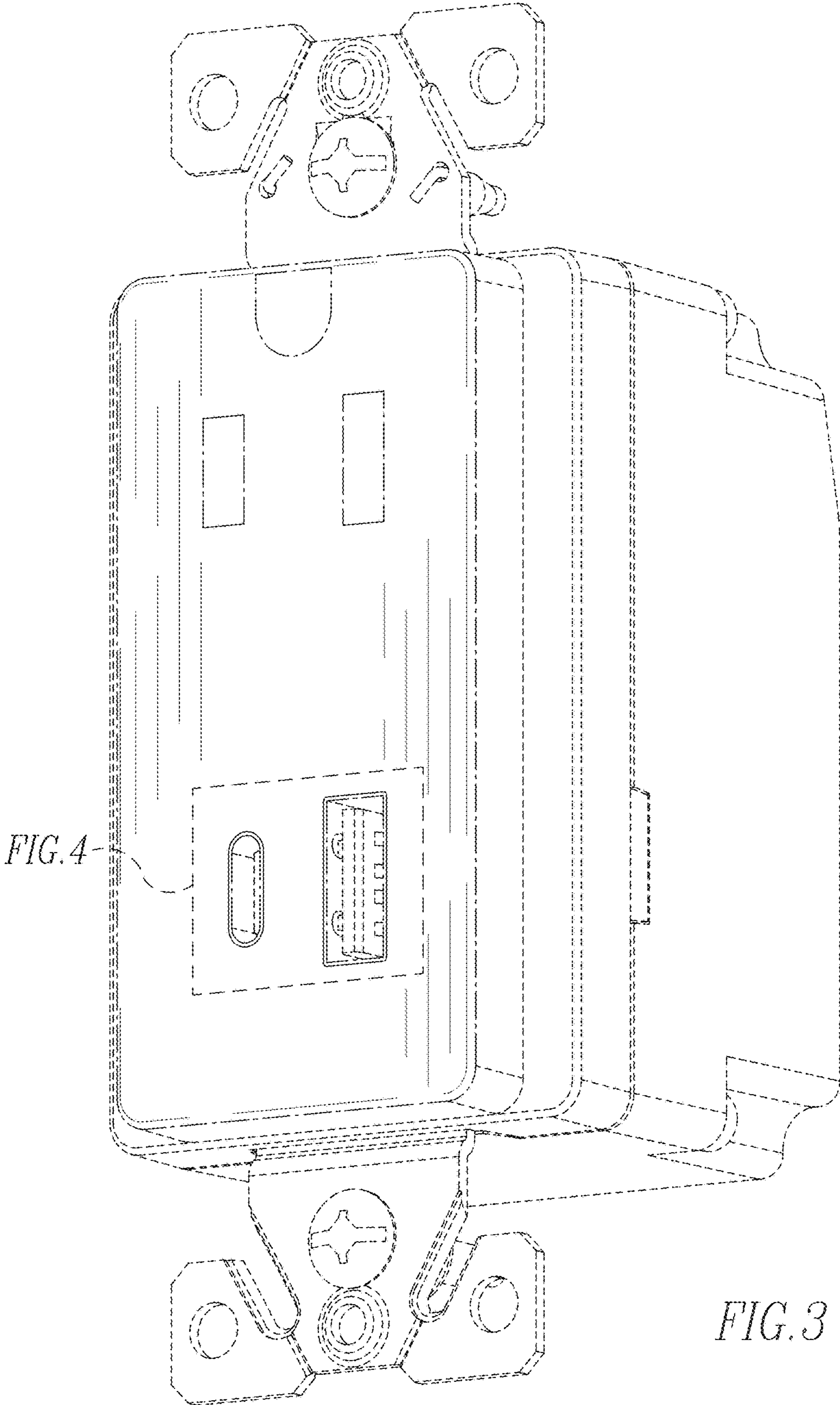


FIG. 2

FIG. 1





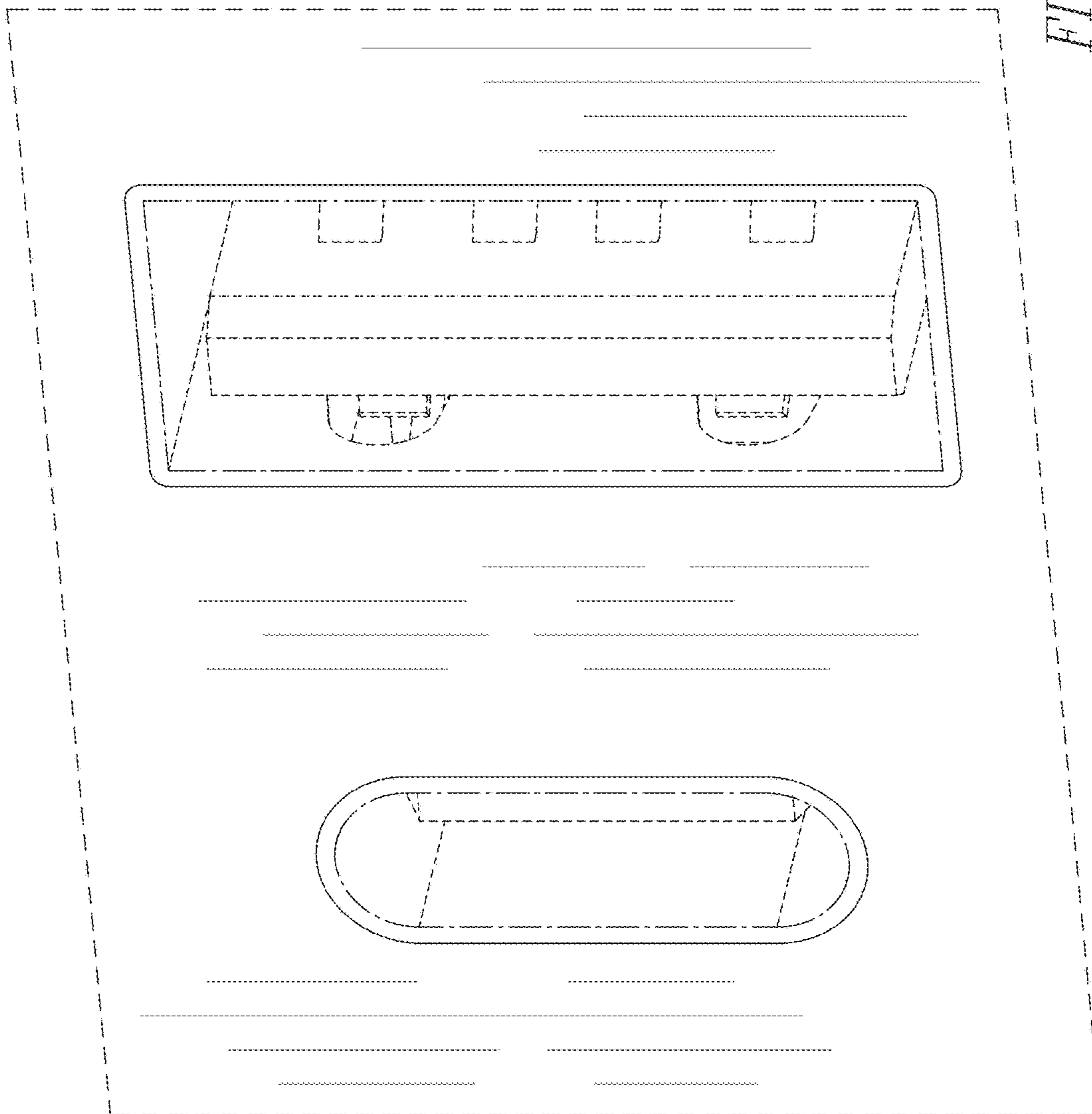


FIG. 4

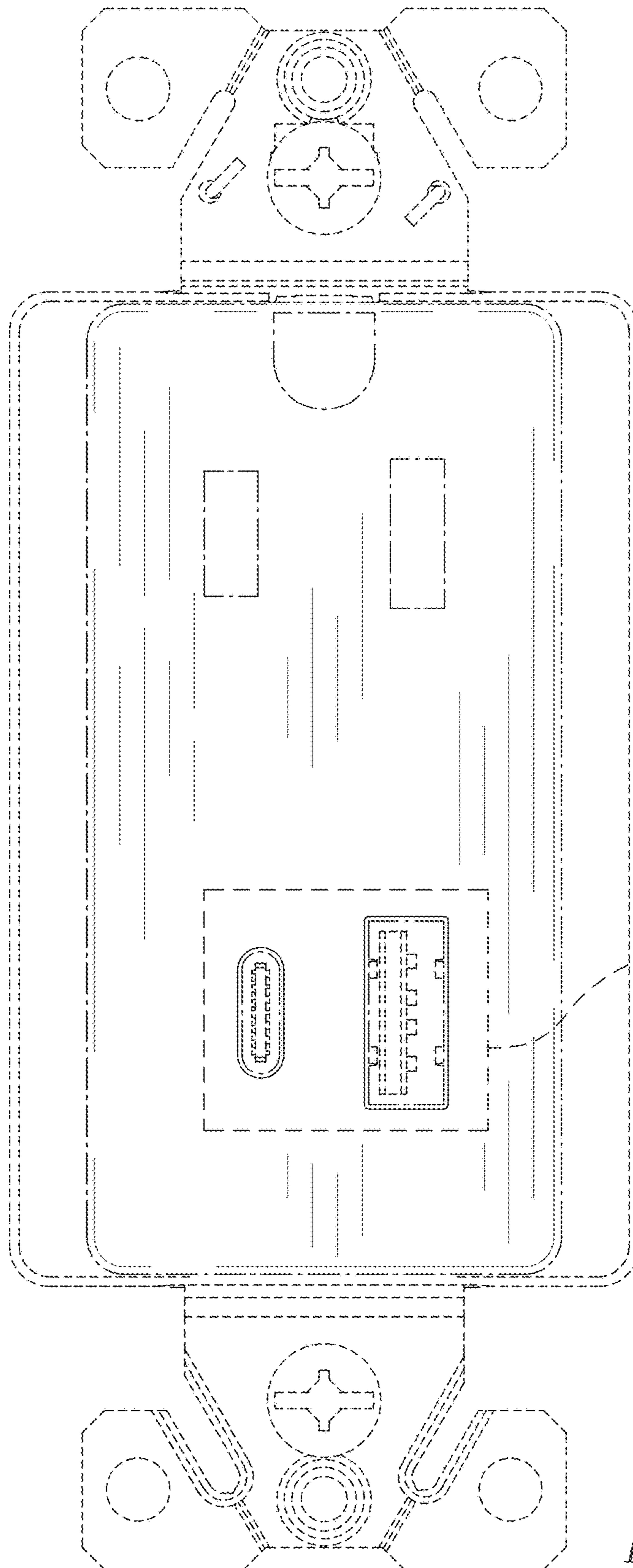
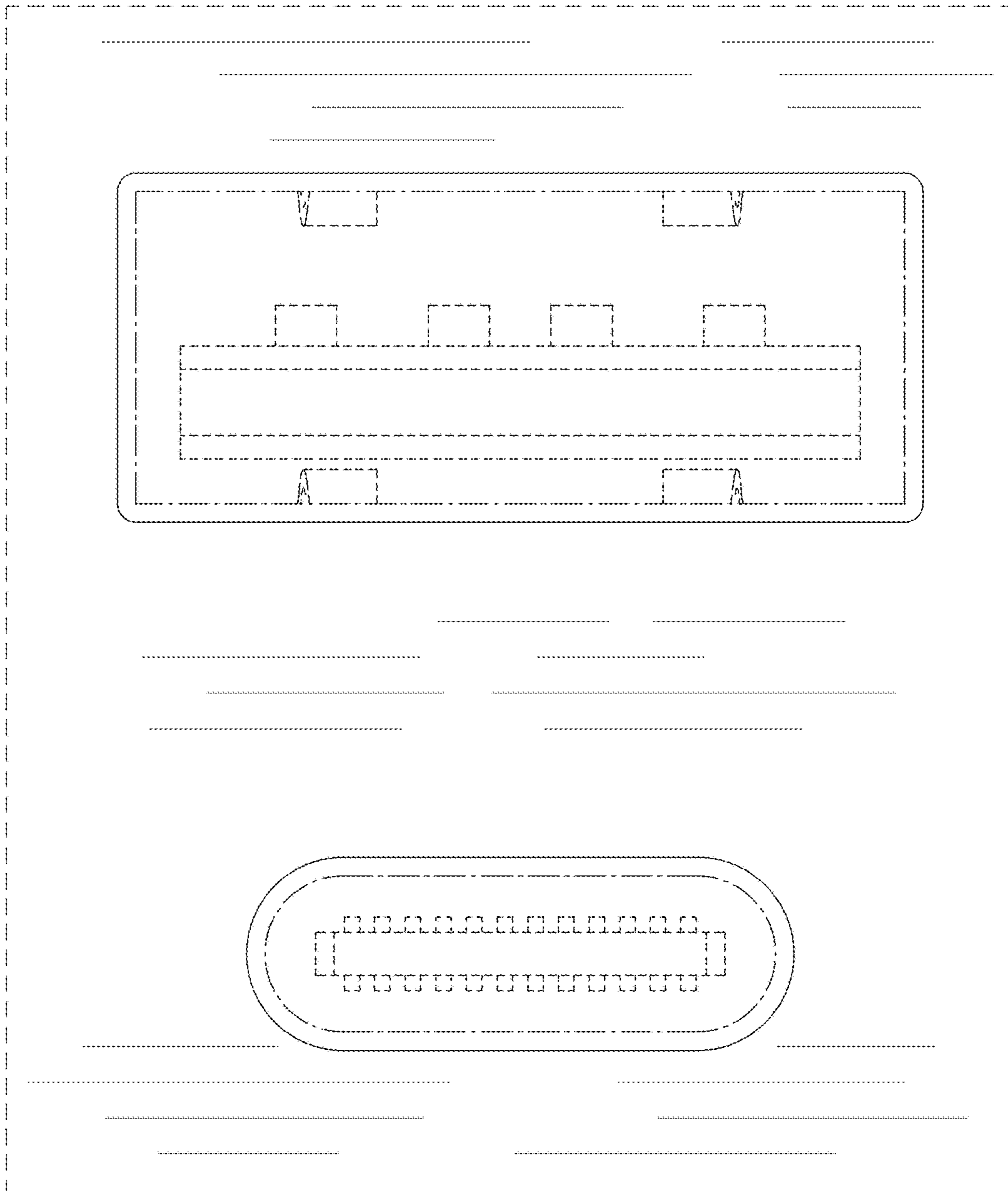


FIG. 6

FIG. 5

FIG. 6



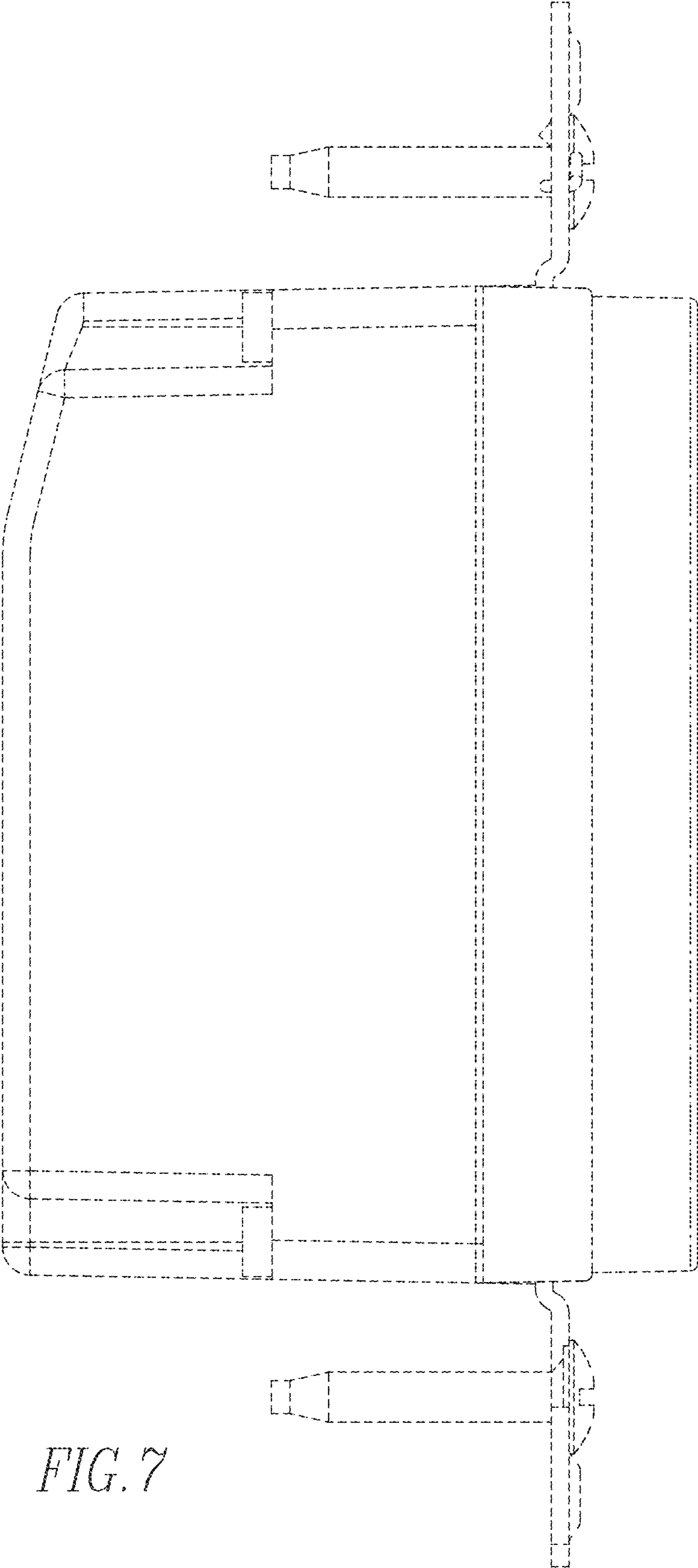
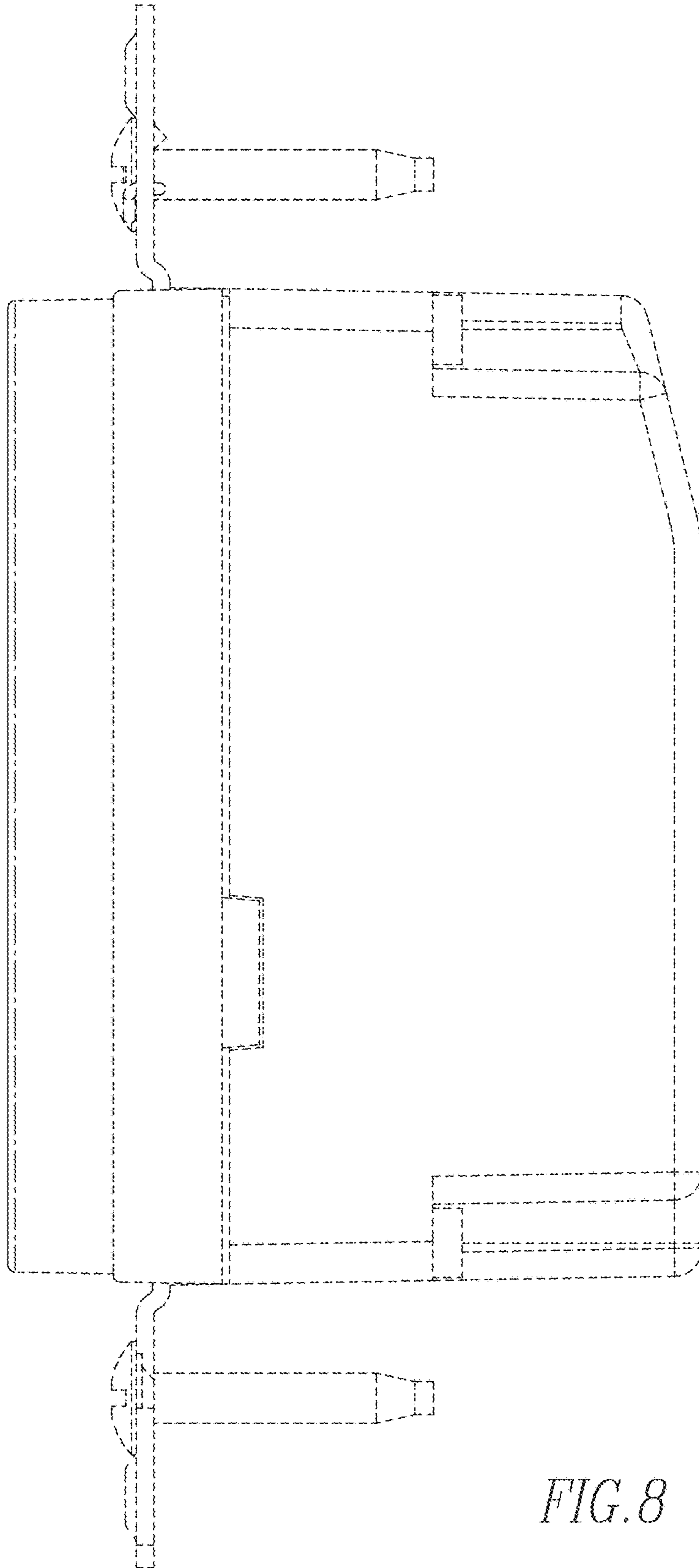


FIG. 7



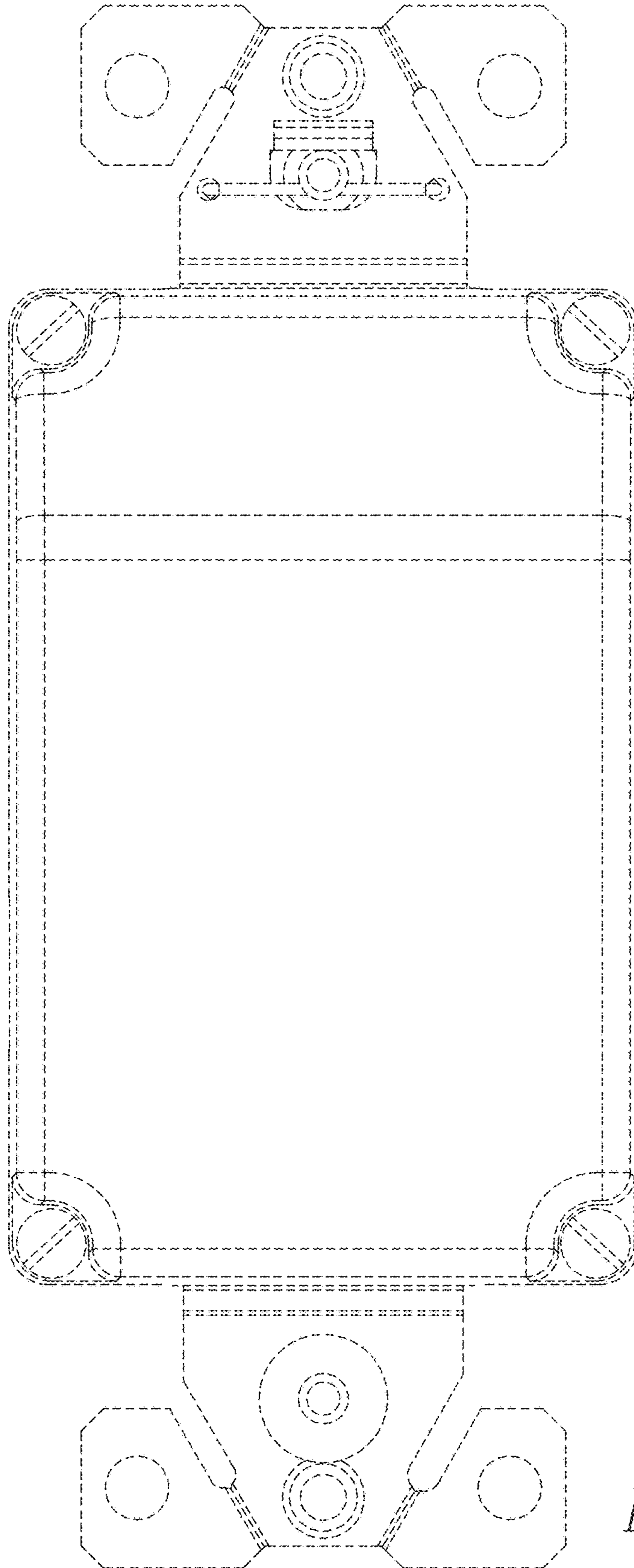


FIG. 9

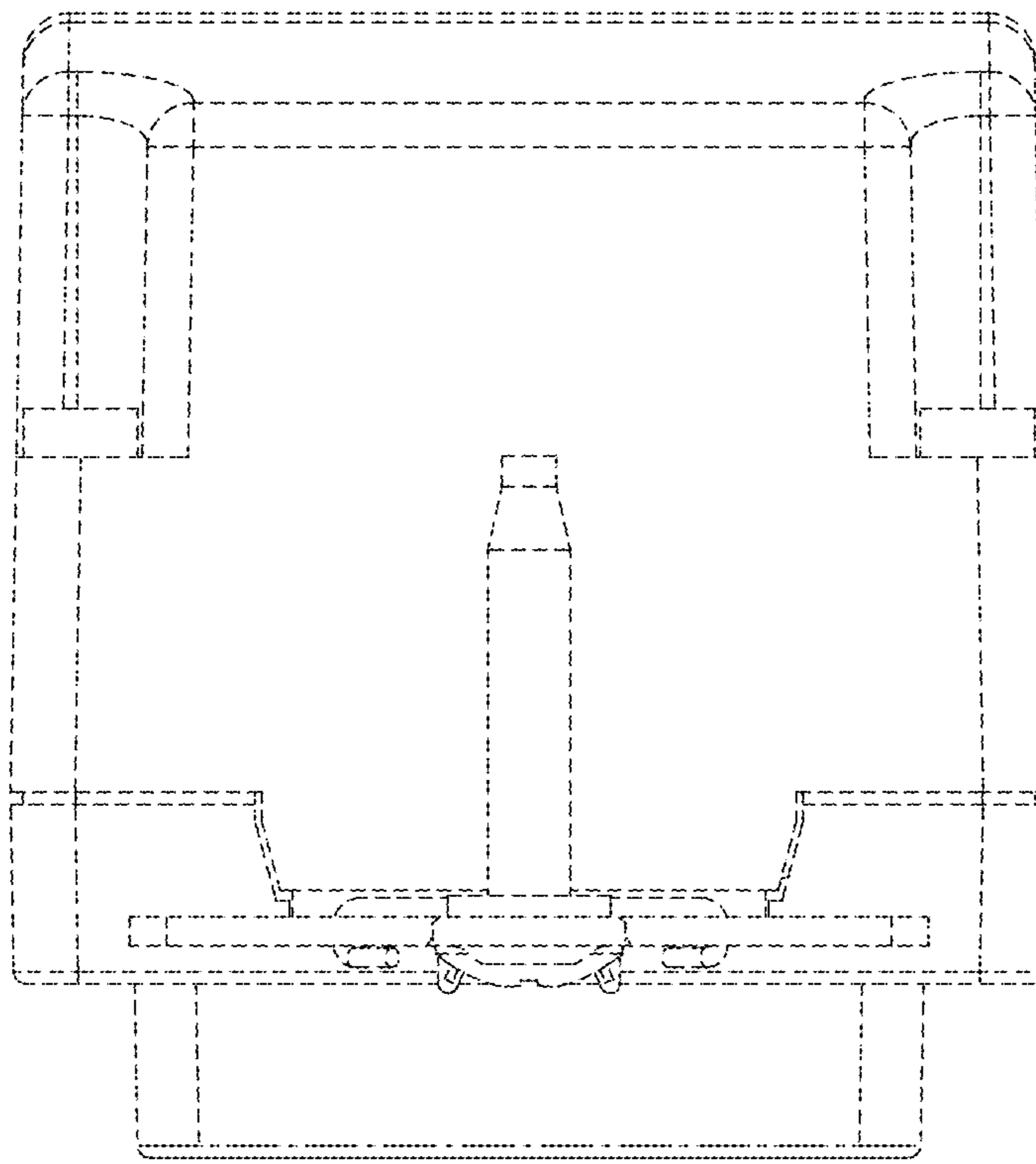


FIG. 10

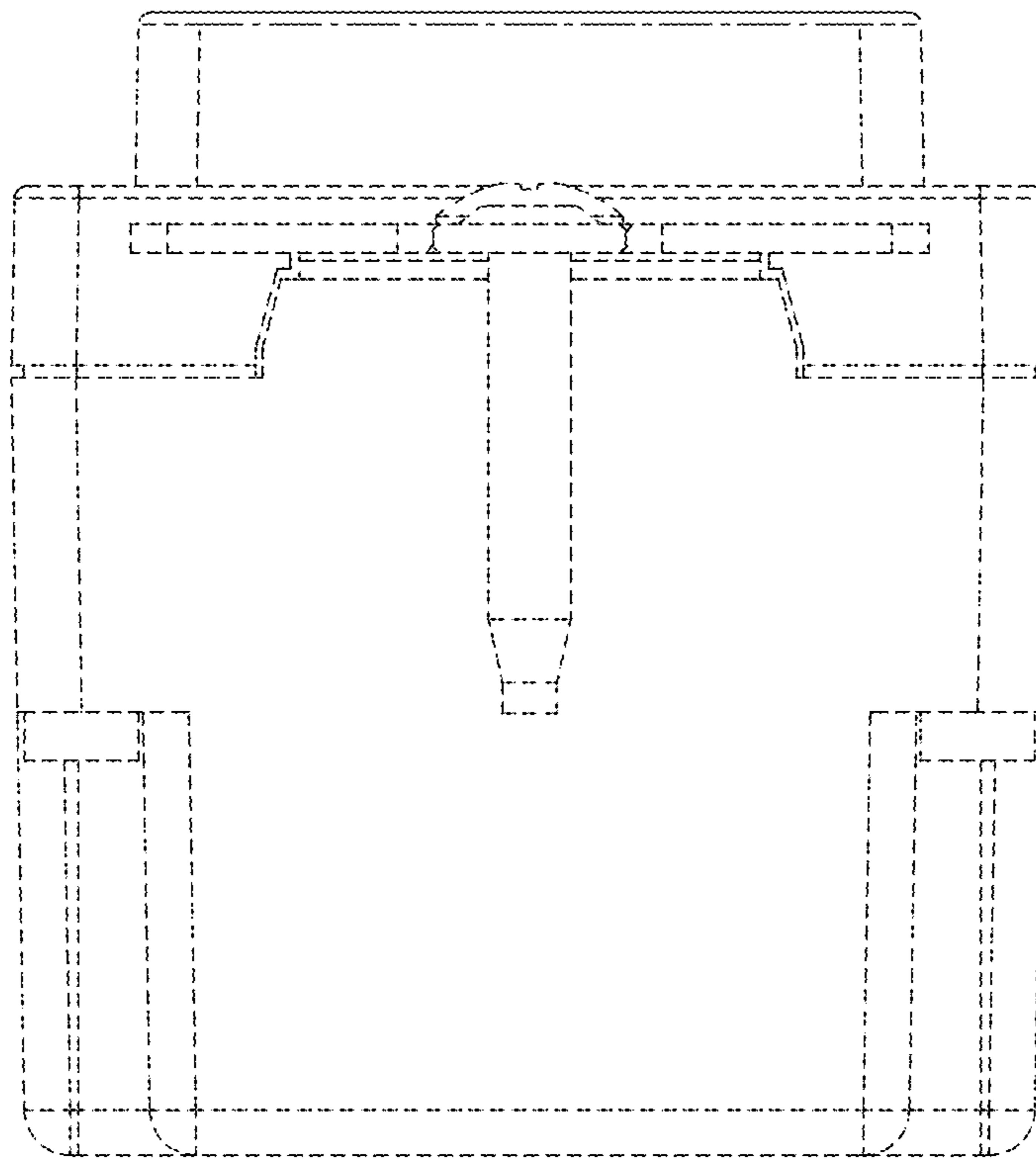


FIG. 11