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(12) **United States Design Patent** (10) **Patent No.:** **US D817,332 S**
Yaprak et al. (45) **Date of Patent:** **** May 8, 2018**

(54) **HOUSING FOR AN ELECTRONIC DEVICE**

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(**) Term: **15 Years**

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(21) Appl. No.: **29/575,186**

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(51) **LOC (11) Cl.** **14-02**

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(52) **U.S. Cl.**
USPC **D14/432**

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(58) **Field of Classification Search**

(74) *Attorney, Agent, or Firm* — Trentice V. Bolar, Esq.

USPC D13/102–106, 110, 118, 119, 184;
D14/341, 346, 498, 507, 509, 299, 432;
D25/186, 127; D10/70
CPC Y02E 60/12; Y02E 60/122; Y02E 60/124;
Y02E 60/50; H01M 2/02; H01M 2/022;
H01M 2/0202; H01M 2/0207; H01M
2/0212; H01M 2/1061; H01M 2/1022;
H01M 2/1055; H01M 2/1066; H01M
2/105; H01M 2/204; H01M 10/4257;
H01M 10/0426; H01M 10/48

See application file for complete search history.

(57) **CLAIM**

The ornamental design for a housing for an electronic device, as shown.

DESCRIPTION

FIG. 1 is a perspective view of a housing for an electronic device, showing our new design.

FIG. 2 is a front view thereof;

FIG. 3 is a side view thereof; and,

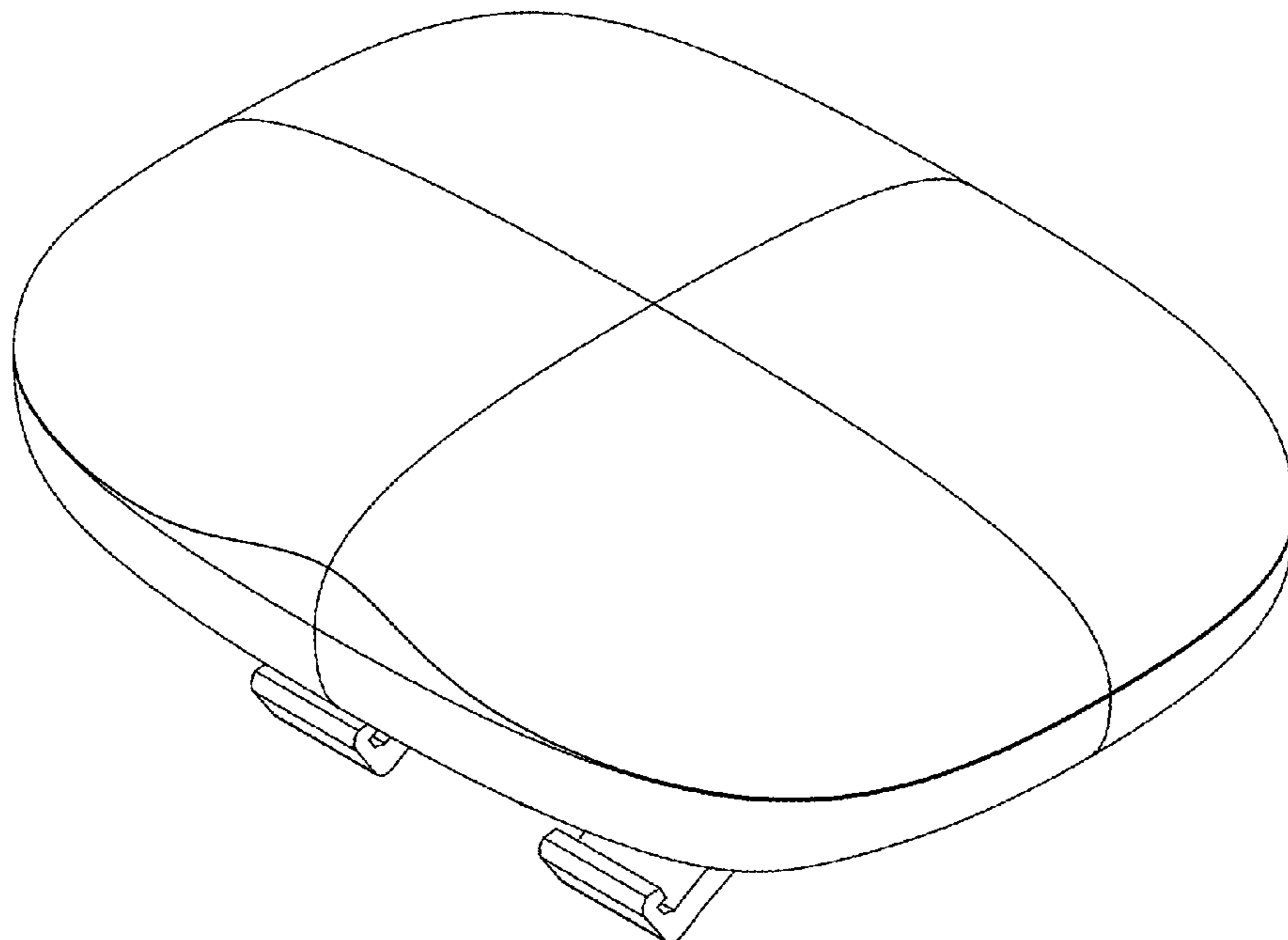
FIG. 4 is a back view thereof.

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1 Claim, 4 Drawing Sheets



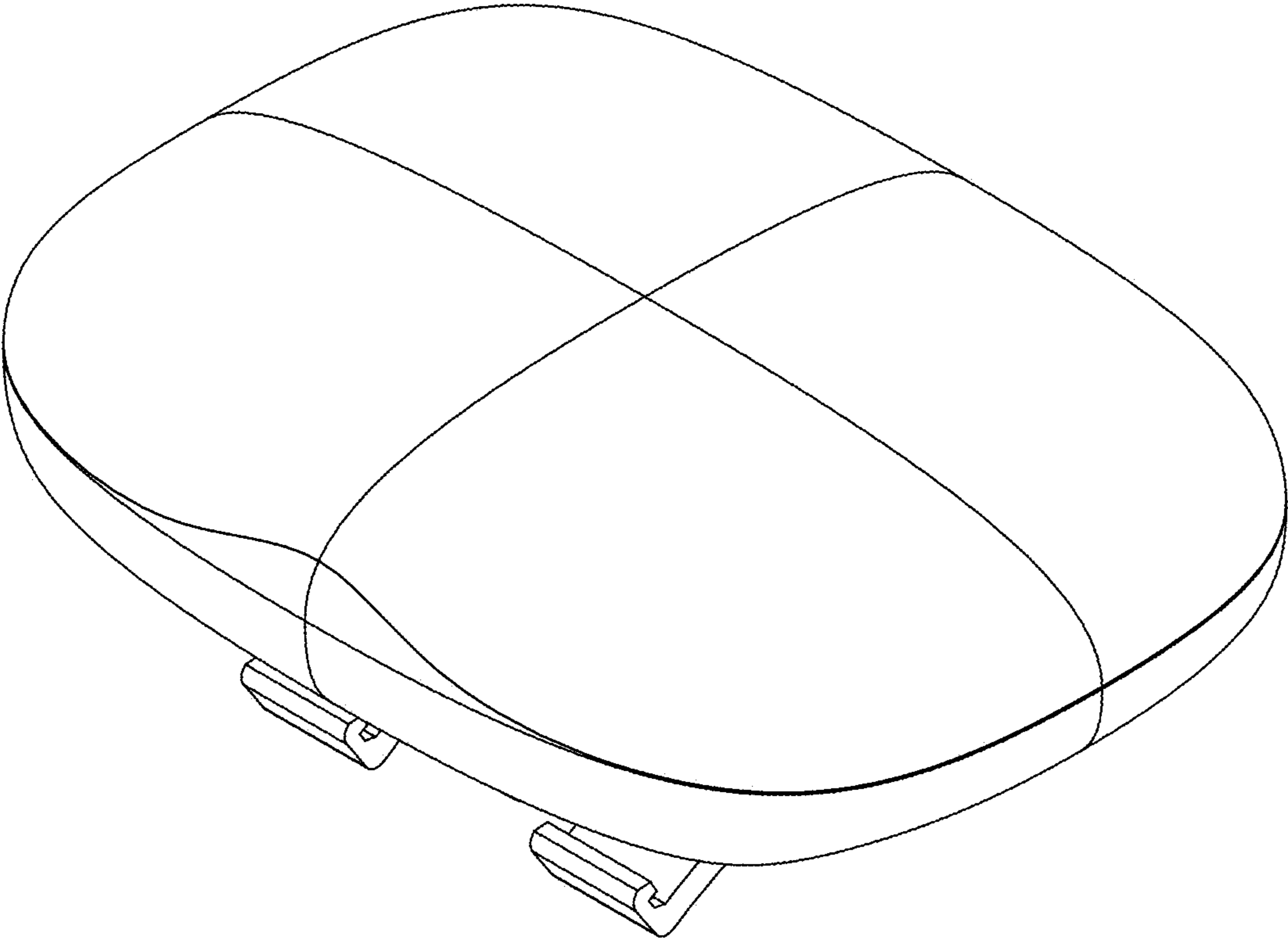


FIG. 1

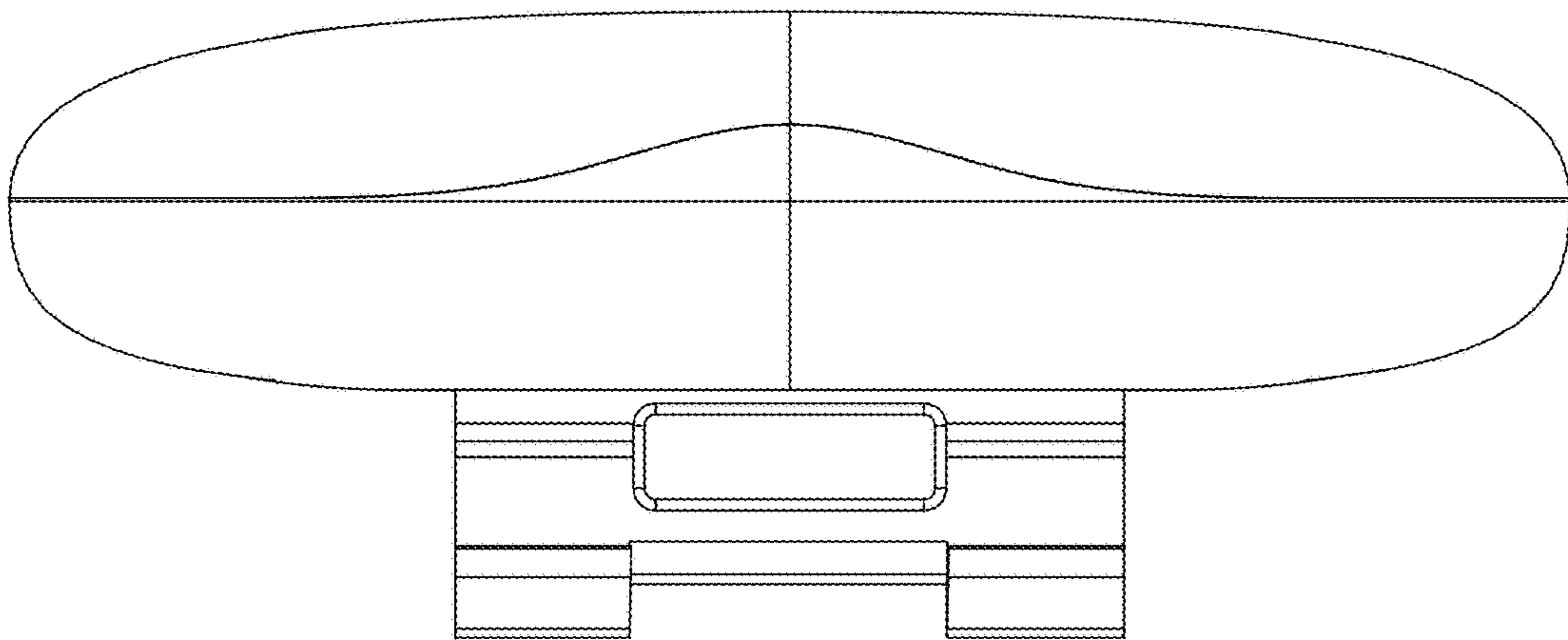


FIG. 2

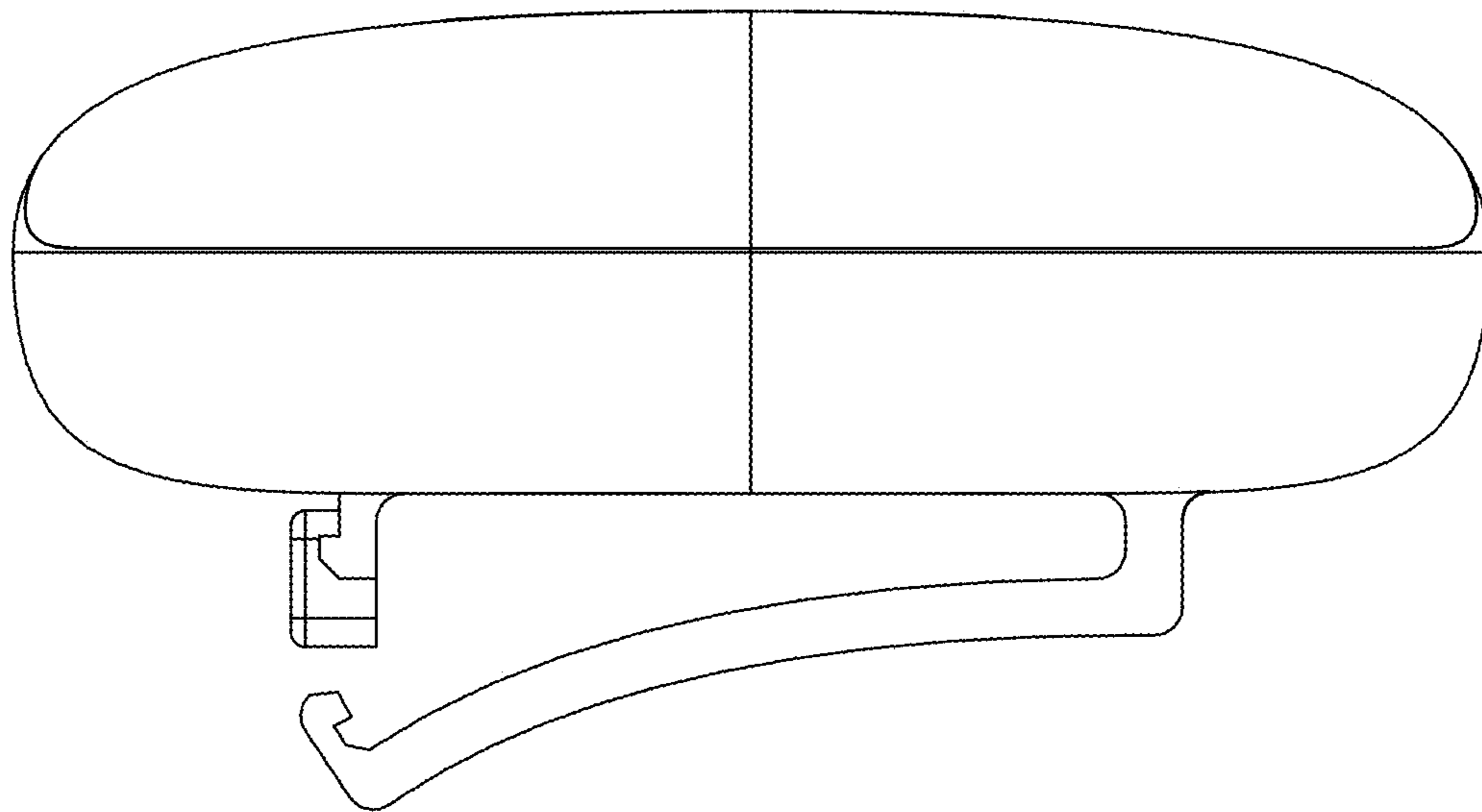


FIG. 3

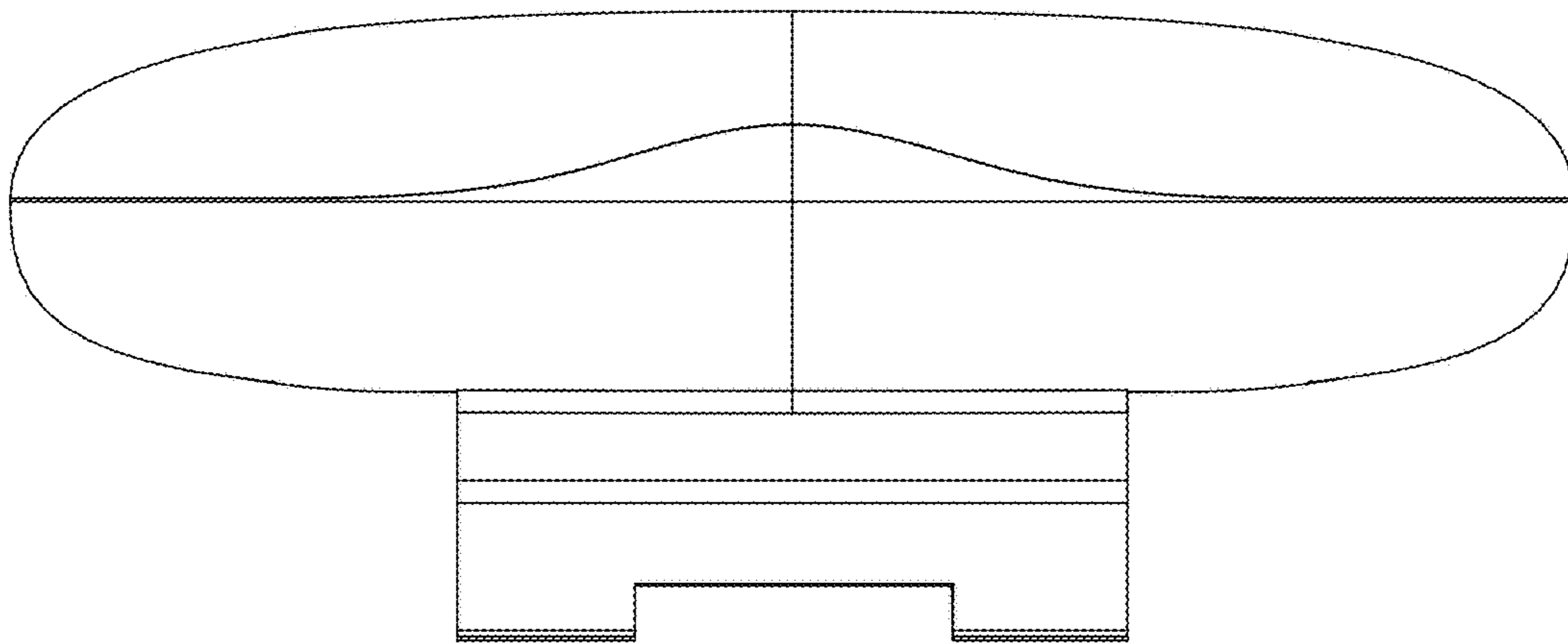


FIG. 4