



US00D816341S

(12) **United States Design Patent** (10) **Patent No.:** **US D816,341 S**  
**Schiller et al.** (45) **Date of Patent:** **\*\* May 1, 2018**

(54) **TAG-LOOP CARRIER ASSEMBLY**  
(71) Applicant: **Bedford Industries, Inc.**, Worthington, MN (US)  
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(73) Assignee: **Bedford Industries, Inc.**, Worthington, MN (US)

(\*\*) Term: **15 Years**

(21) Appl. No.: **29/569,353**

(22) Filed: **Jun. 27, 2016**

**Related U.S. Application Data**

(60) Division of application No. 29/542,306, filed on Oct. 13, 2015, now Pat. No. Des. 762,985, which is a continuation of application No. 29/496,818, filed on Jul. 17, 2014, now abandoned, which is a division of application No. 29/437,913, filed on Nov. 21, 2012, now Pat. No. Des. 712,154.

(51) **LOC (11) Cl.** ..... **03-01**

(52) **U.S. Cl.**  
USPC ..... **D3/328; D3/318**

(58) **Field of Classification Search**  
USPC ..... D3/318, 328  
CPC ..... G09F 3/18; B65D 69/00  
See application file for complete search history.

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(57) **CLAIM**

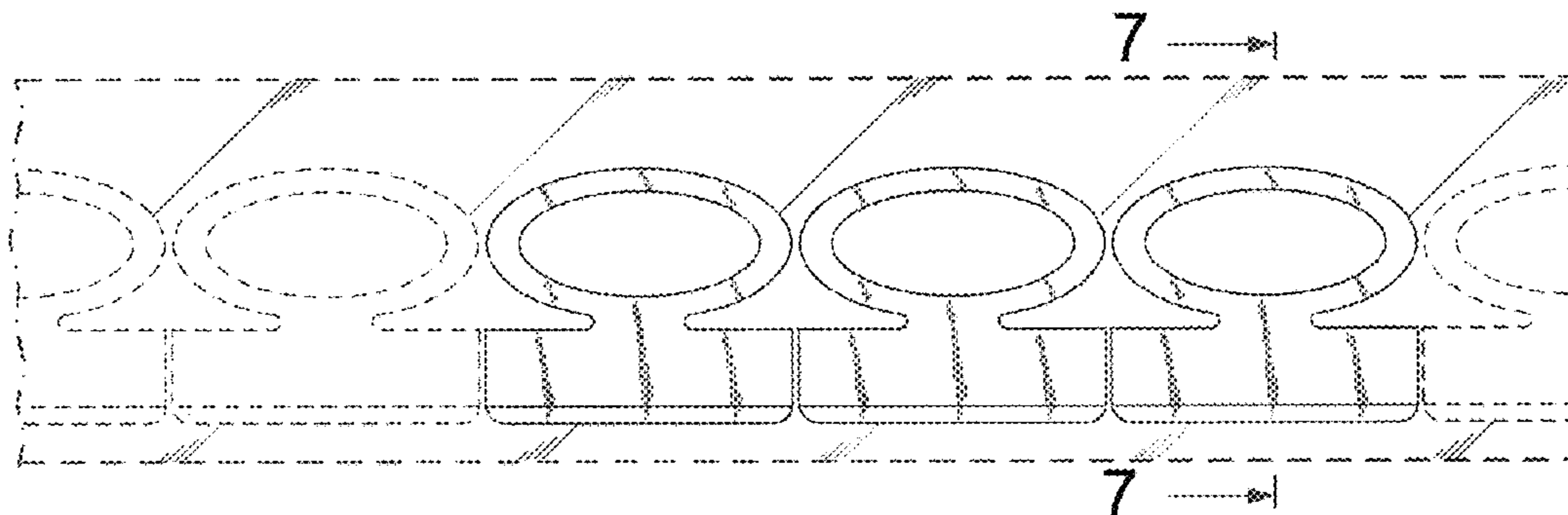
The ornamental design for a tag-loop carrier assembly, as shown and described.

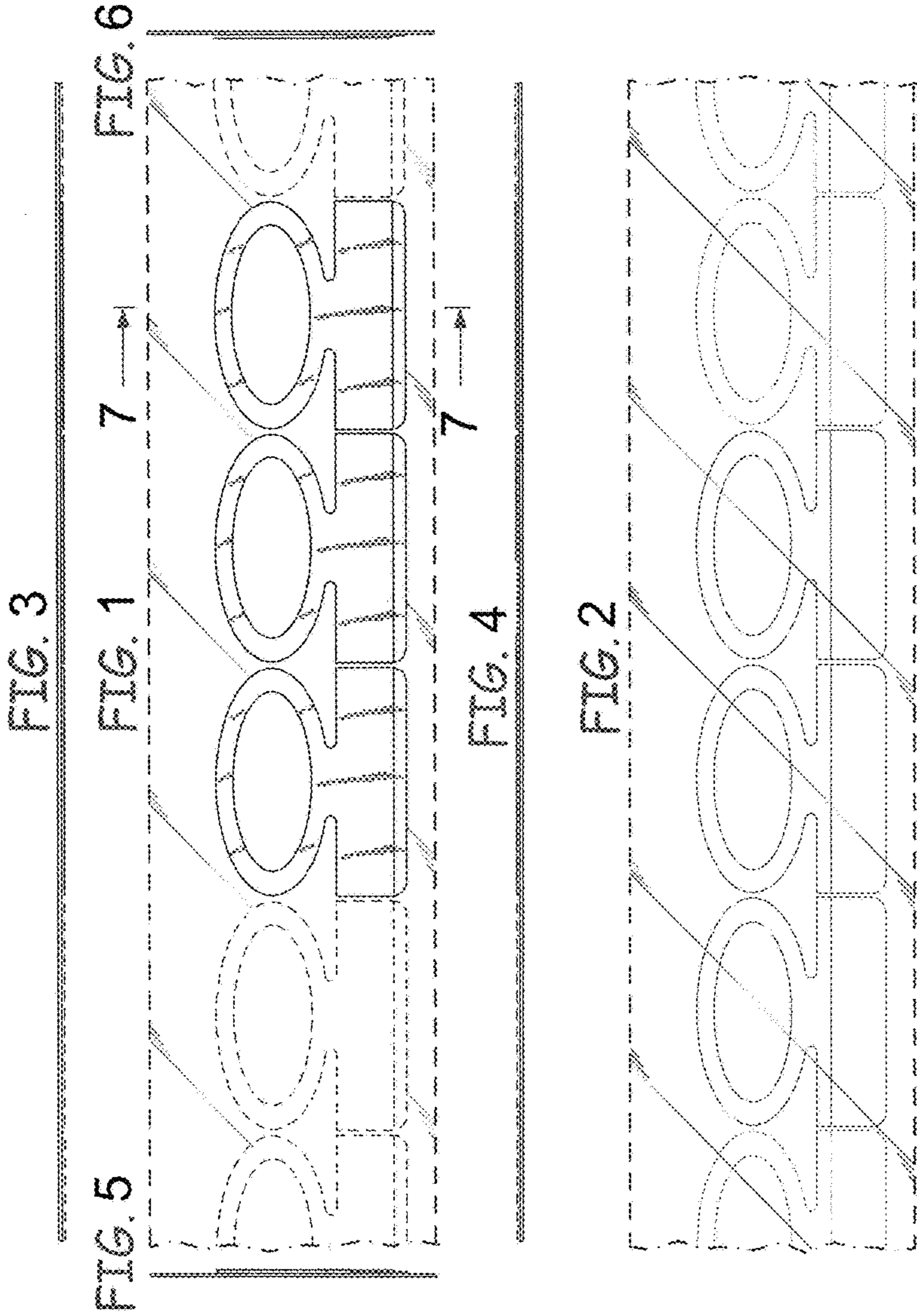
**DESCRIPTION**

FIG. 1 is a front view of a tag-loop carrier assembly;  
FIG. 2 is a back view thereof;  
FIG. 3 is a top view thereof;  
FIG. 4 is a bottom view thereof;  
FIG. 5 is a side view thereof, as taken from the left side of FIG. 1;  
FIG. 6 is a side view thereof; as taken from the right side of FIG. 1; and,  
FIG. 7 is a sectional view as taken along lines 7-7 in FIG. 1.

The evenly spaced broken lines are for the purpose of illustrating environment and form no part of the claimed design. The dot-dash-dot broken away lines in the drawings indicate that any portion of the article beyond what is shown forms no part of the claimed design.

**1 Claim, 2 Drawing Sheets**





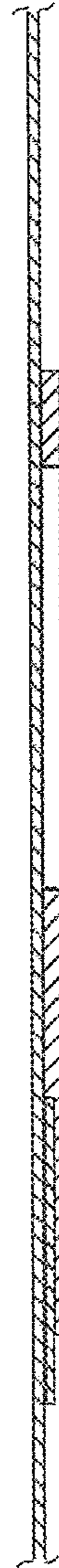


FIG. 7