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(12) **United States Design Patent**  
**Kirkland**

(10) **Patent No.:** **US D815,385 S**  
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- (54) **WAFER SUPPORT RING**
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- (73) Assignee: **Entegris, Inc.**, Billerica, MA (US)
- (\*\*) Term: **15 Years**
- (21) Appl. No.: **29/580,872**
- (22) Filed: **Oct. 13, 2016**
- (51) **LOC (11) Cl.** ..... **12-05**
- (52) **U.S. Cl.**  
USPC ..... **D34/29**
- (58) **Field of Classification Search**  
USPC ..... D34/28, 29; 198/851, 853, 825, 890.1,  
198/370.1, 370.04, 782, 722, 771, 866,  
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414/744.5, 800; D23/269  
CPC ..... H01L 21/67259; H01L 21/67742; H01L  
21/67288; H02K 5/128  
See application file for complete search history.

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*Primary Examiner* — Cynthia Ramirez

(57) **CLAIM**

I claim the ornamental design for a wafer support ring, as shown and described.

**DESCRIPTION**

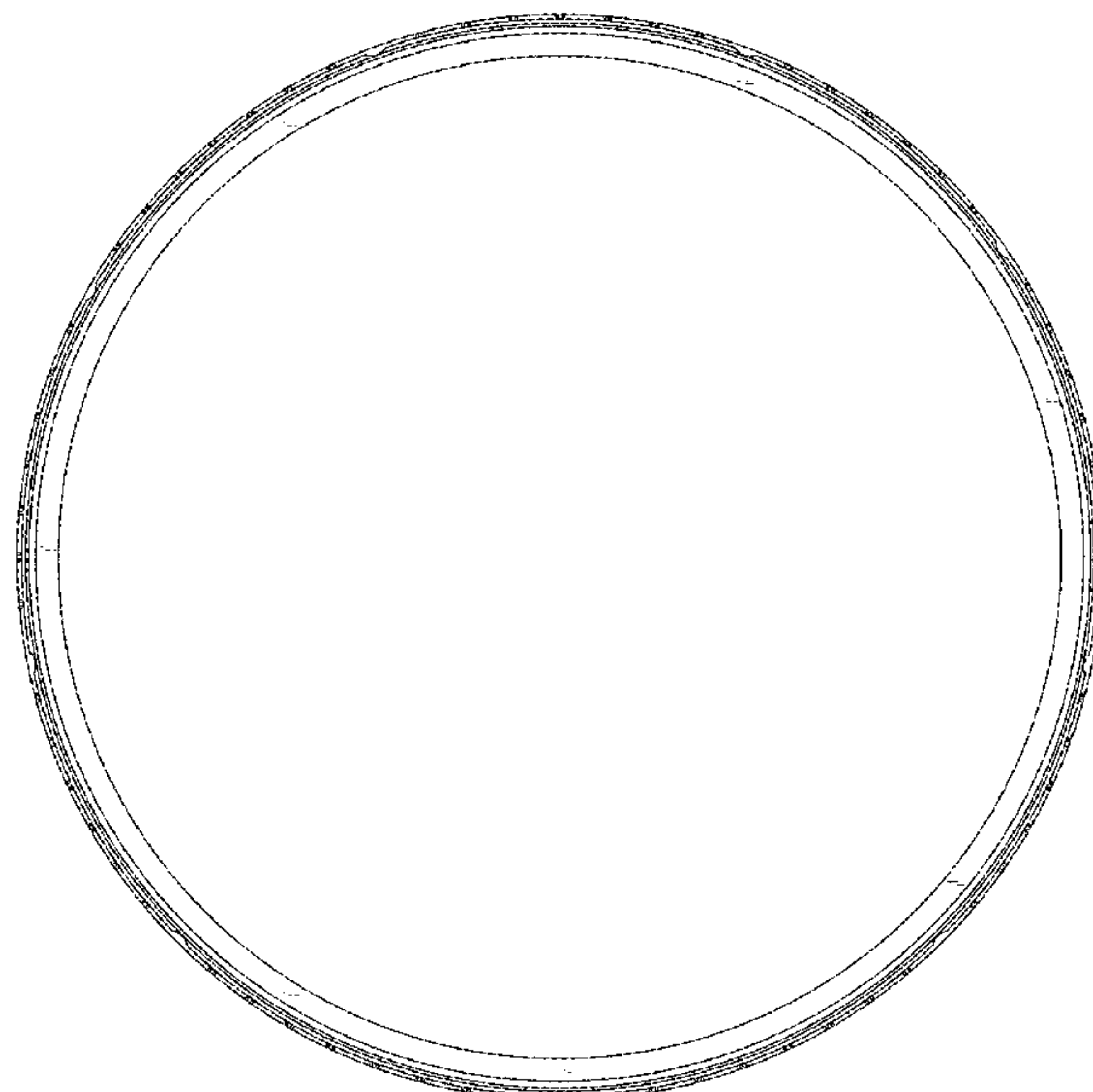
FIG. 1 is a top plan view of a wafer support ring according to an embodiment of the invention.  
 FIG. 2 is a top perspective view of the wafer support ring depicted in FIG. 1.  
 FIG. 3 is a cross-sectional view of the wafer support ring taken at line 3-3 of FIG. 1.  
 FIG. 4 is a cross-sectional view of the wafer support ring taken at line 4-4 of FIG. 1.  
 FIG. 5 is a cross-sectional view of the wafer support ring taken at line 5-5 of FIG. 1.  
 FIG. 6 is a left elevational view of the wafer support ring.  
 FIG. 7 is a front plan view of the wafer support ring.  
 FIG. 8 is a bottom perspective view of the wafer support ring; and,  
 FIG. 9 is a bottom plan view of the wafer support ring.

**1 Claim, 9 Drawing Sheets**

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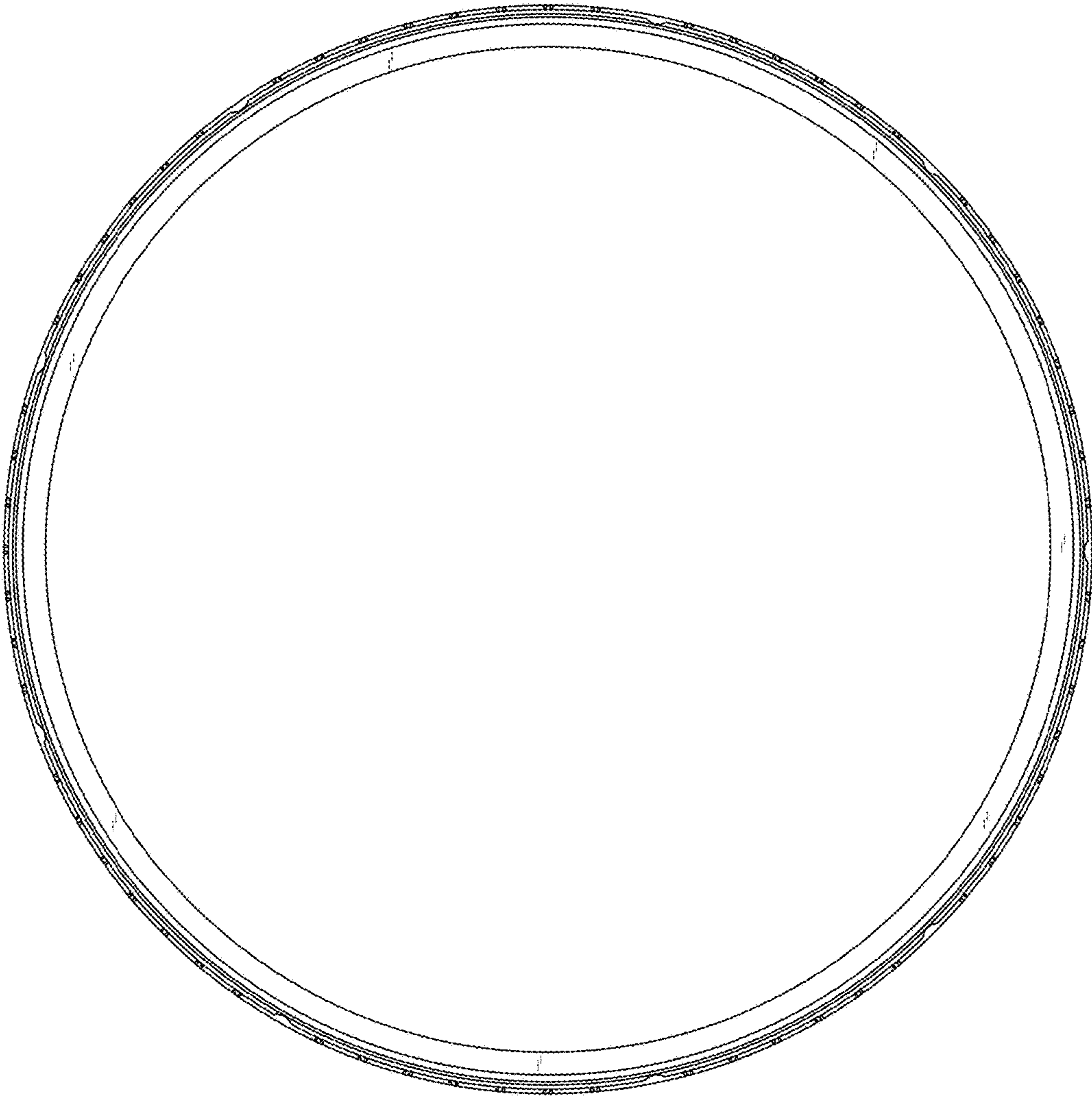


Fig. 1

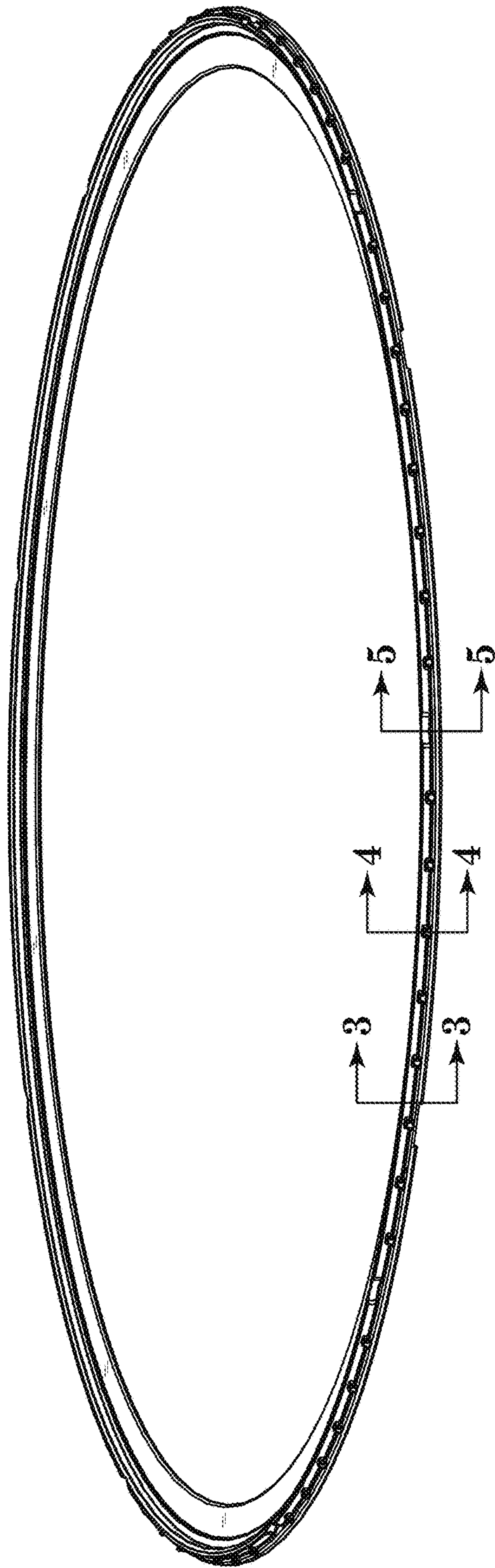


Fig. 2

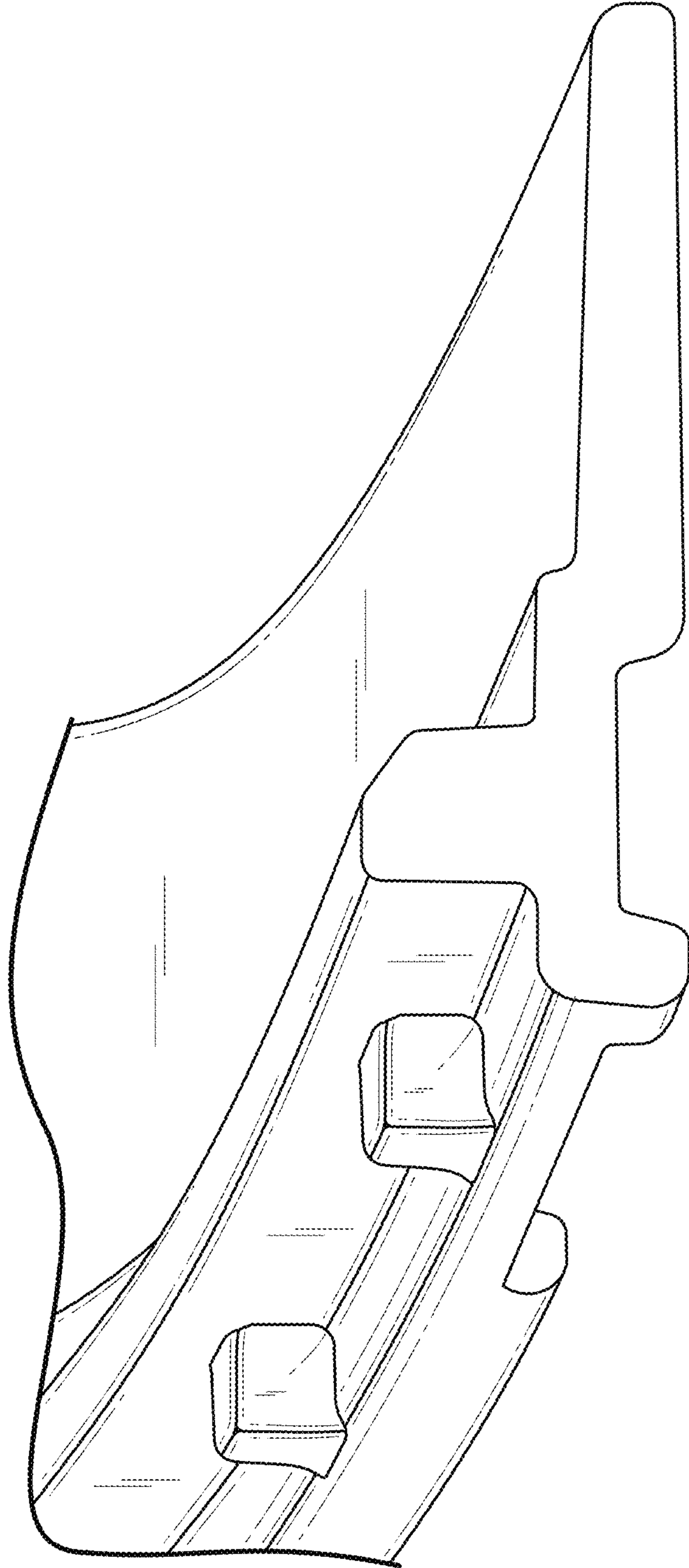


Fig. 3





Fig. 4

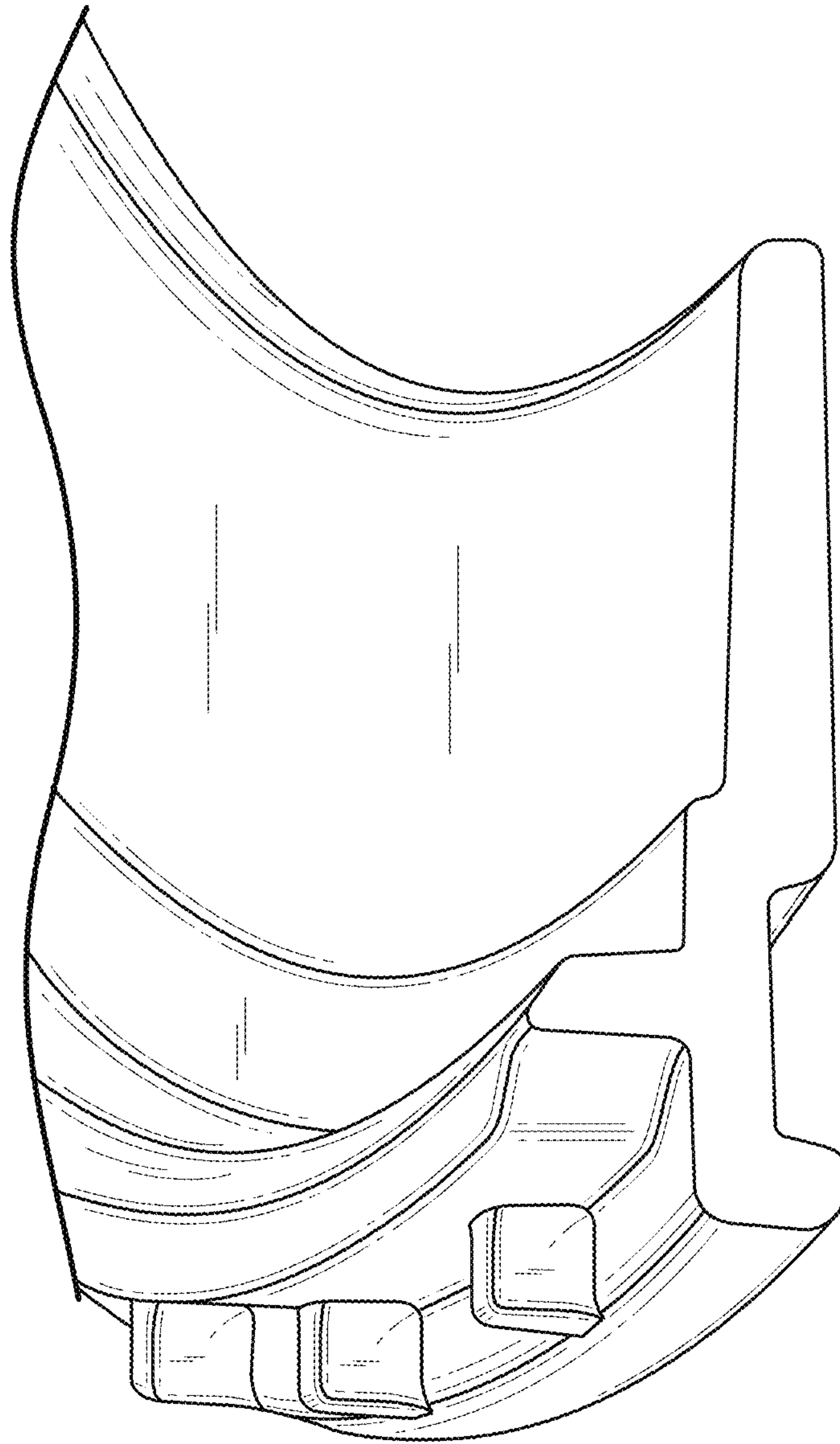


Fig. 5

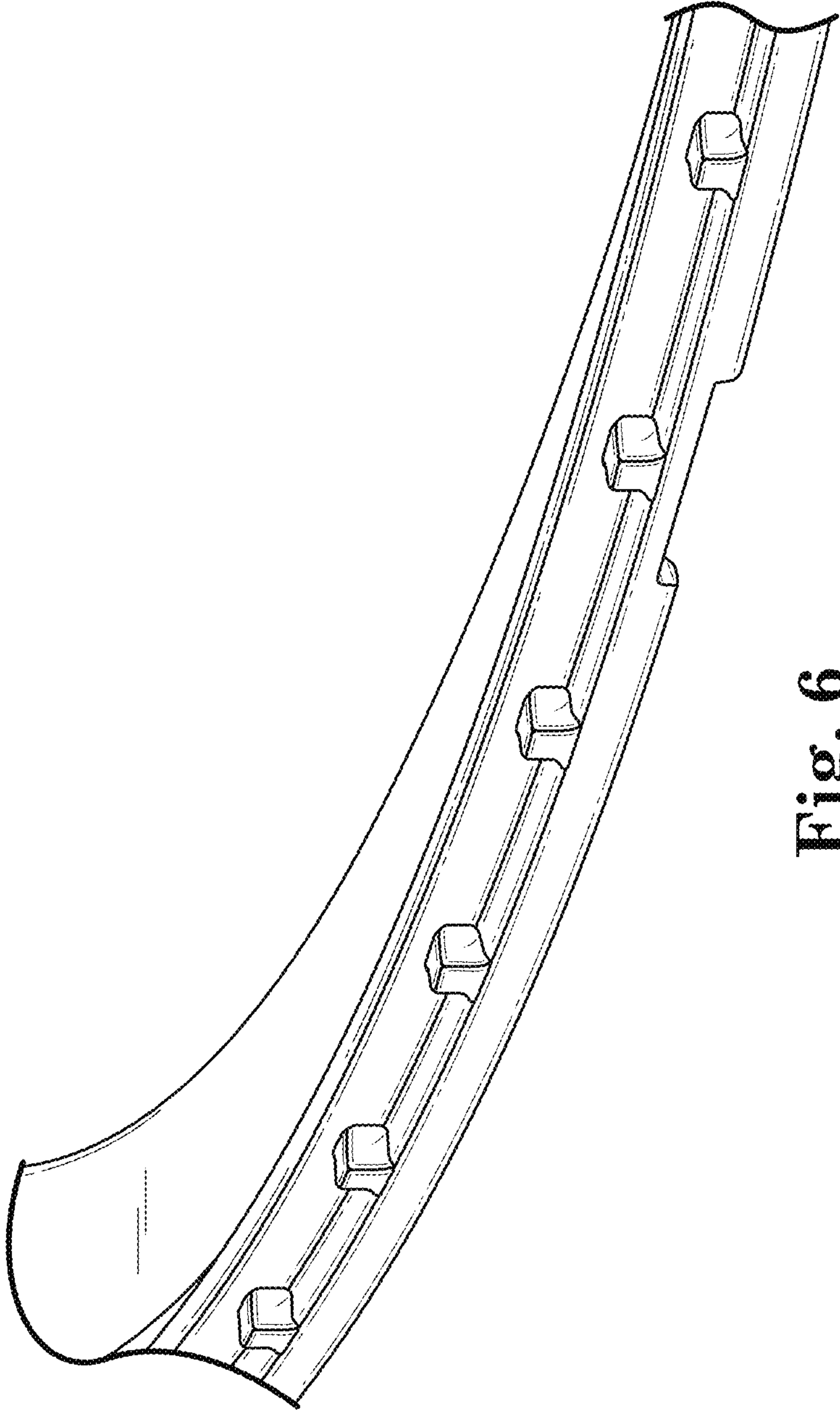


Fig. 6

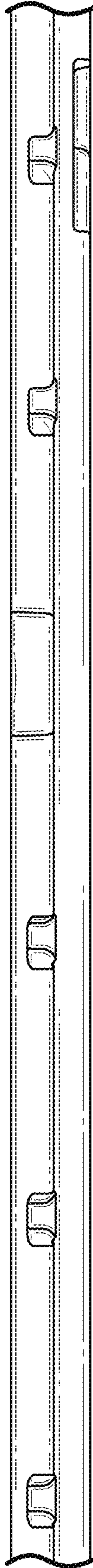


Fig. 7



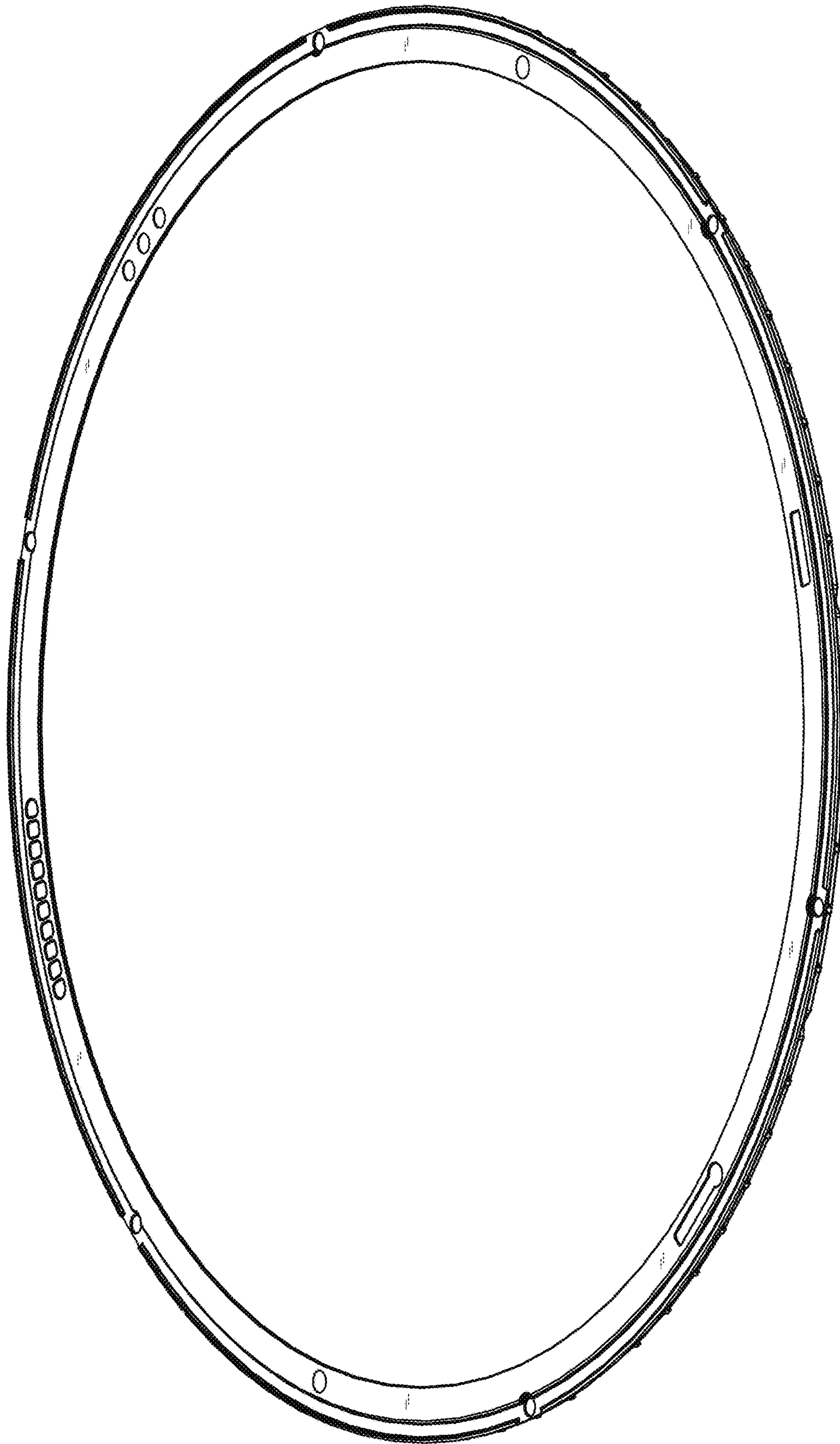


Fig. 8

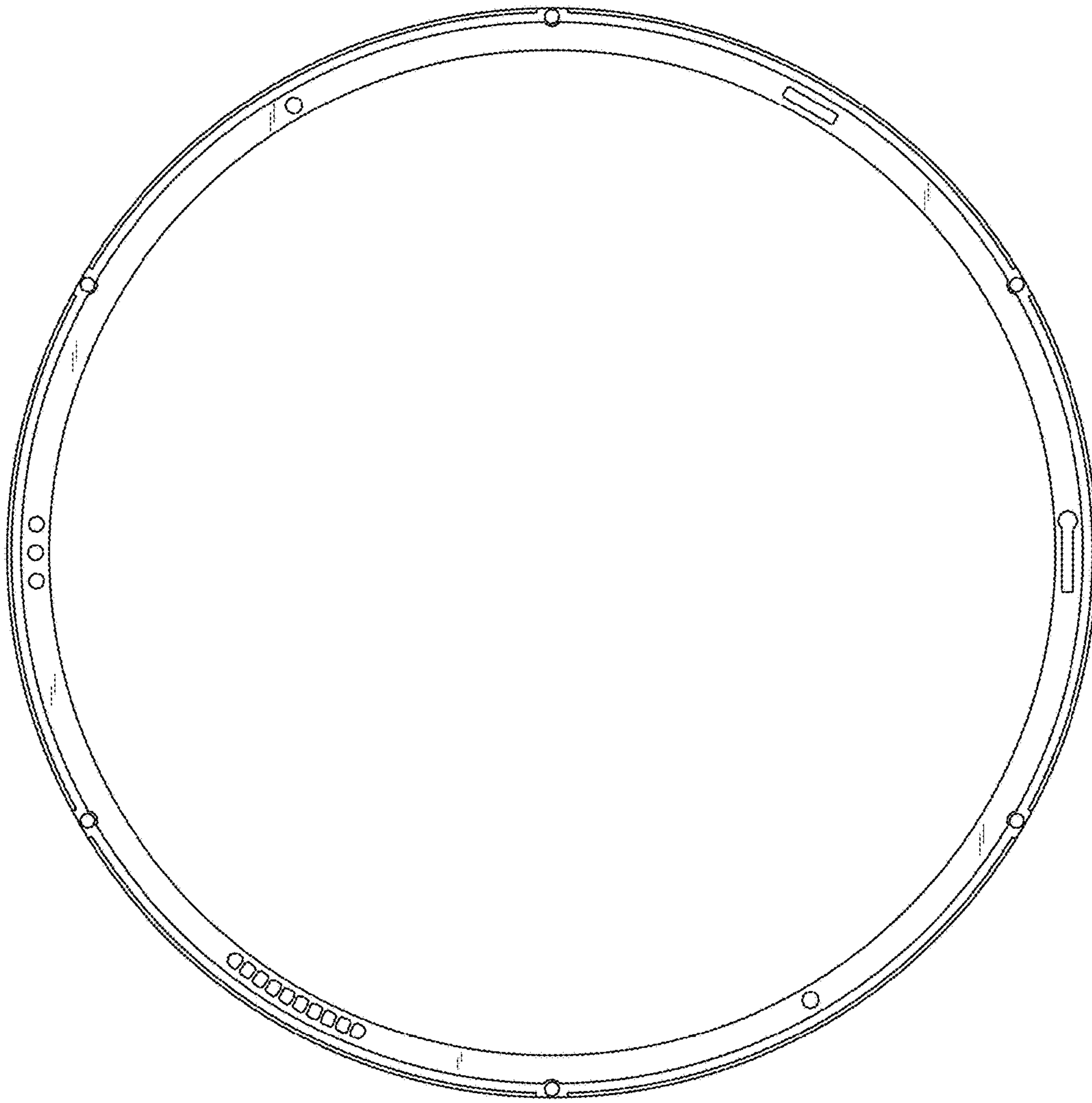


Fig. 9