



US00D815097S

(12) **United States Design Patent**  
**Magi**

(10) **Patent No.:** **US D815,097 S**  
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- (54) **COMPUTING DEVICE DOCK**
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- (73) Assignee: **Intel Corporation**, Santa Clara, CA (US)
- (\*\*) Term: **15 Years**
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- (51) **LOC (11) Cl.** ..... **14-02**
- (52) **U.S. Cl.**  
USPC ..... **D14/434**
- (58) **Field of Classification Search**  
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D3/201, 215, 218  
CPC .... G06F 1/1616; G06F 1/1626; G06F 1/1632;  
H01M 10/44; F16M 11/10; H02J 7/0042;  
H02J 7/00; H02J 7/0027; H02J 7/0044;  
H02J 7/0045; H01R 31/065  
See application file for complete search history.

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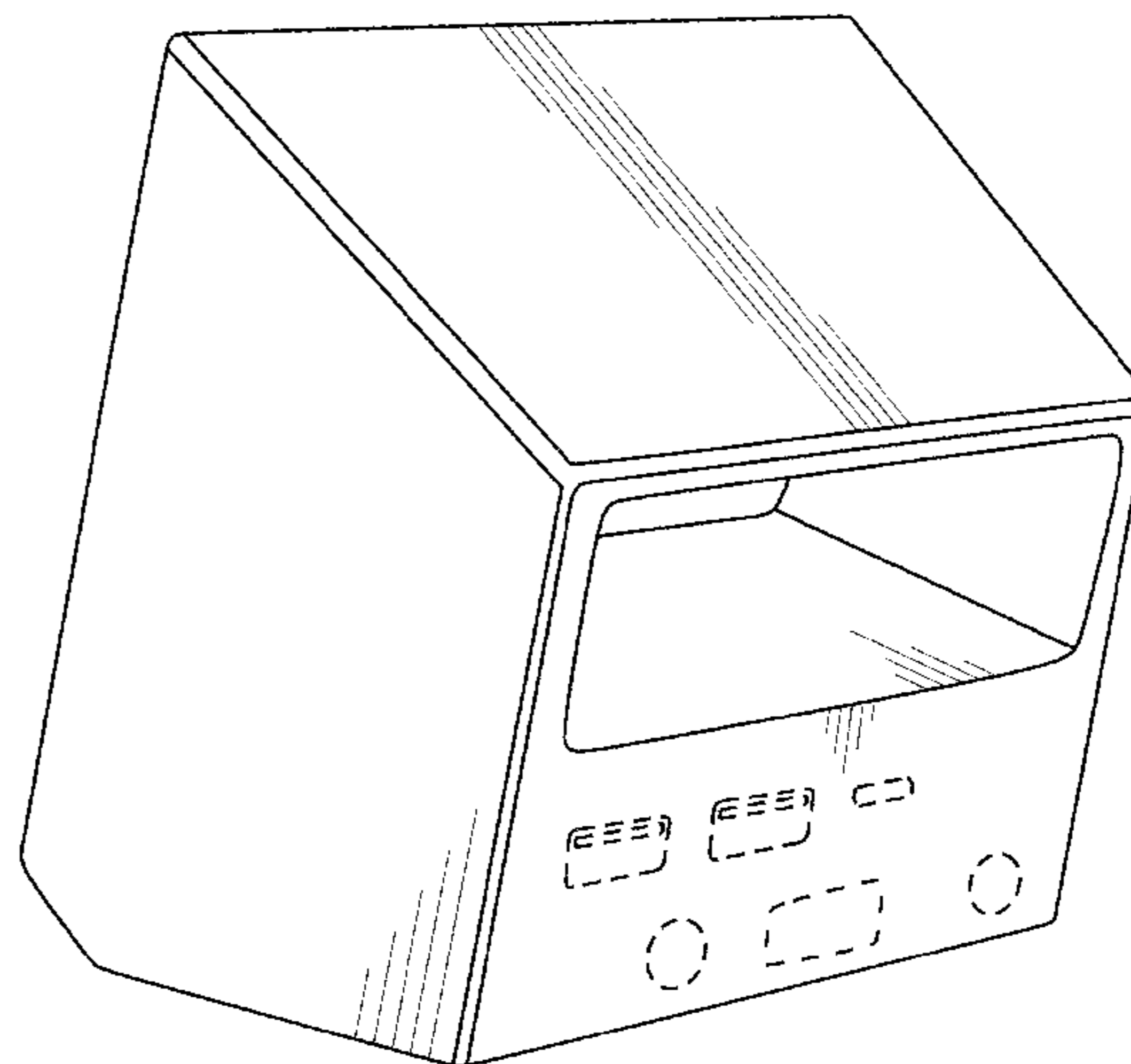
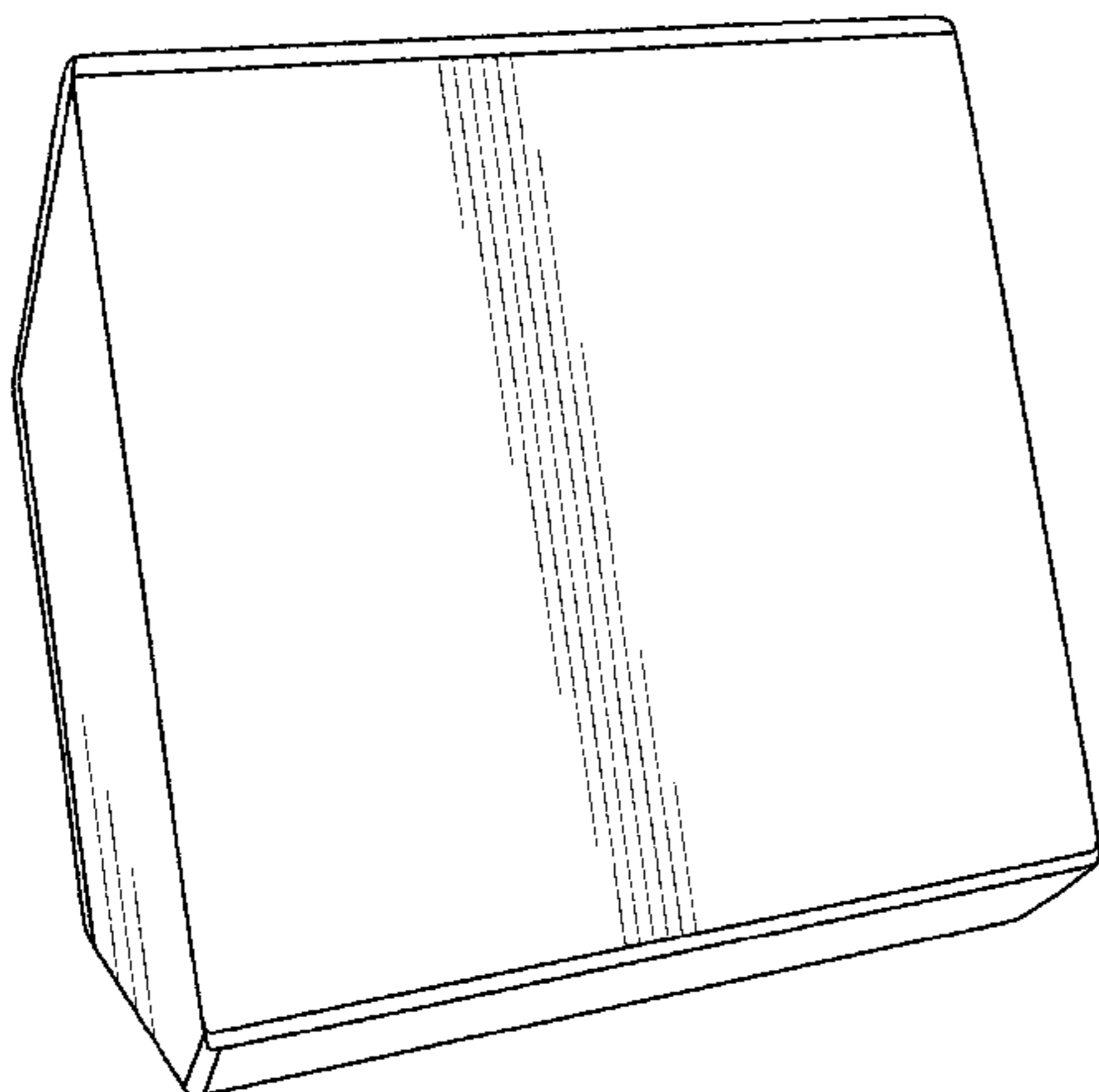
(57) **CLAIM**

What is claimed is the ornamental design for a computing device dock, as shown and described.

**DESCRIPTION**

FIG. 1 is a front view of a computing device dock; FIG. 2 is a back view of the computing device dock from FIG. 1; FIG. 3 is a top view of the computing device dock from FIG. 1; FIG. 4 is a bottom view of the computing device dock from FIG. 1; FIG. 5 is a right view of the computing device dock from FIG. 1; FIG. 6 is a left view of the computing device dock from FIG. 1; and FIG. 7 is a top left perspective view of the computing device dock from FIG. 1. FIG. 8 is a rear perspective view of a computing device dock; and, FIG. 9 is another front left perspective view thereof, shown in a use coupled with a computing device. The broken lines in FIGS. 2, 4, and 8 depict portions of the computing device dock that form no part of the claimed design. The broken lines in FIG. 9 depict environment, these broken lines form no part of the claimed design. The line shading is meant to show contour of represented surfaces and not surface ornamentation.

**1 Claim, 9 Drawing Sheets**



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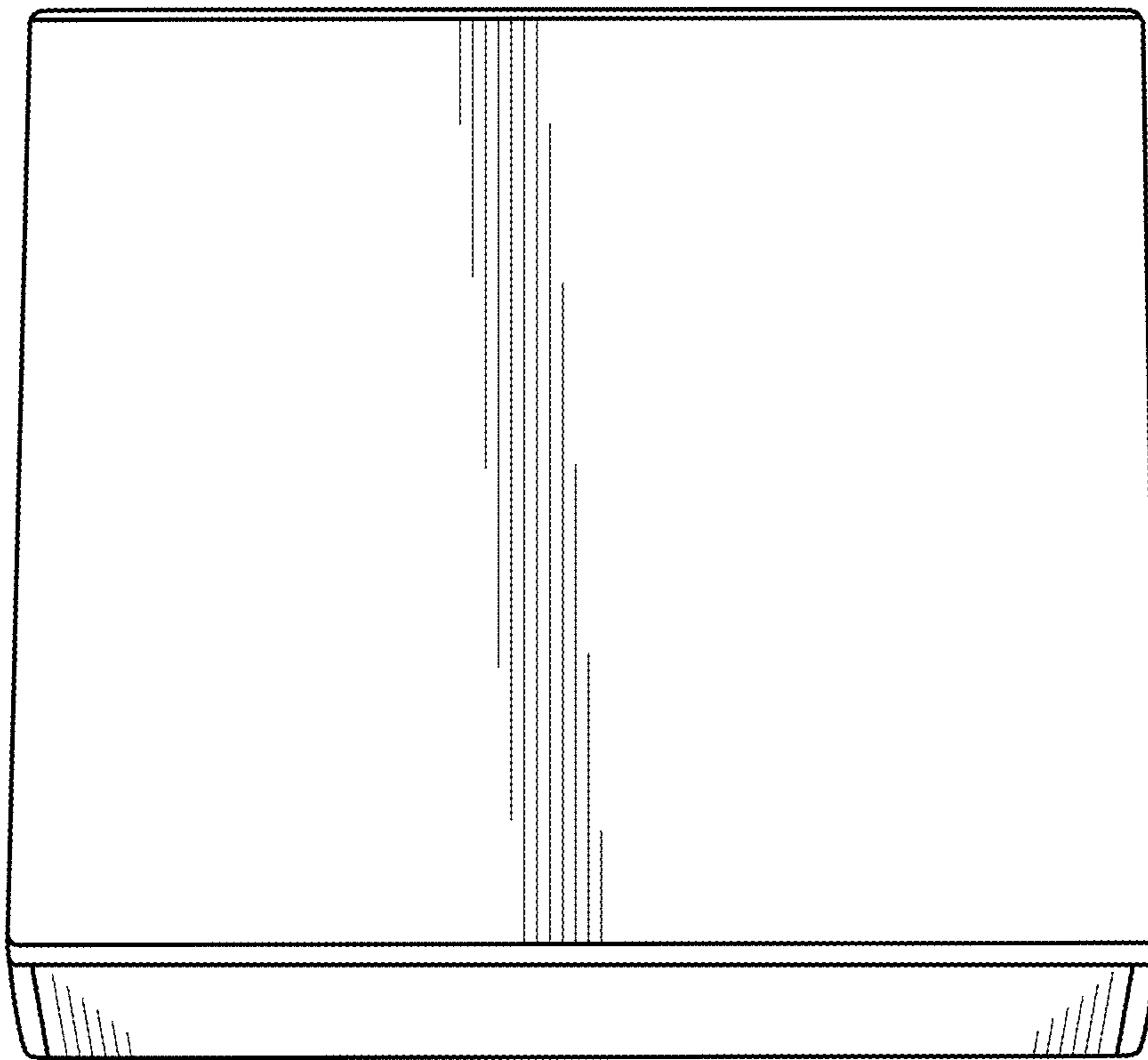


FIG. 1

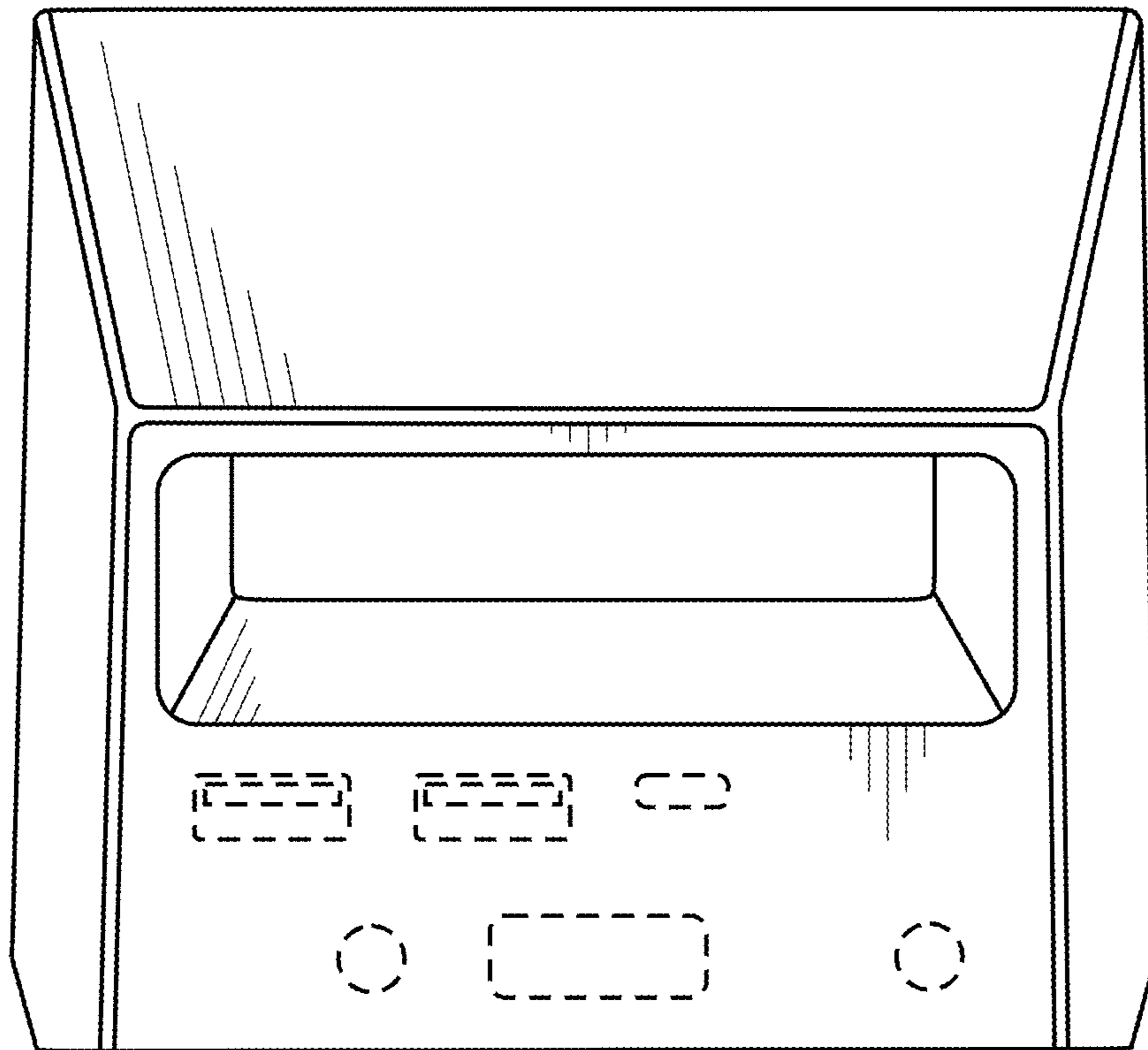


FIG. 2

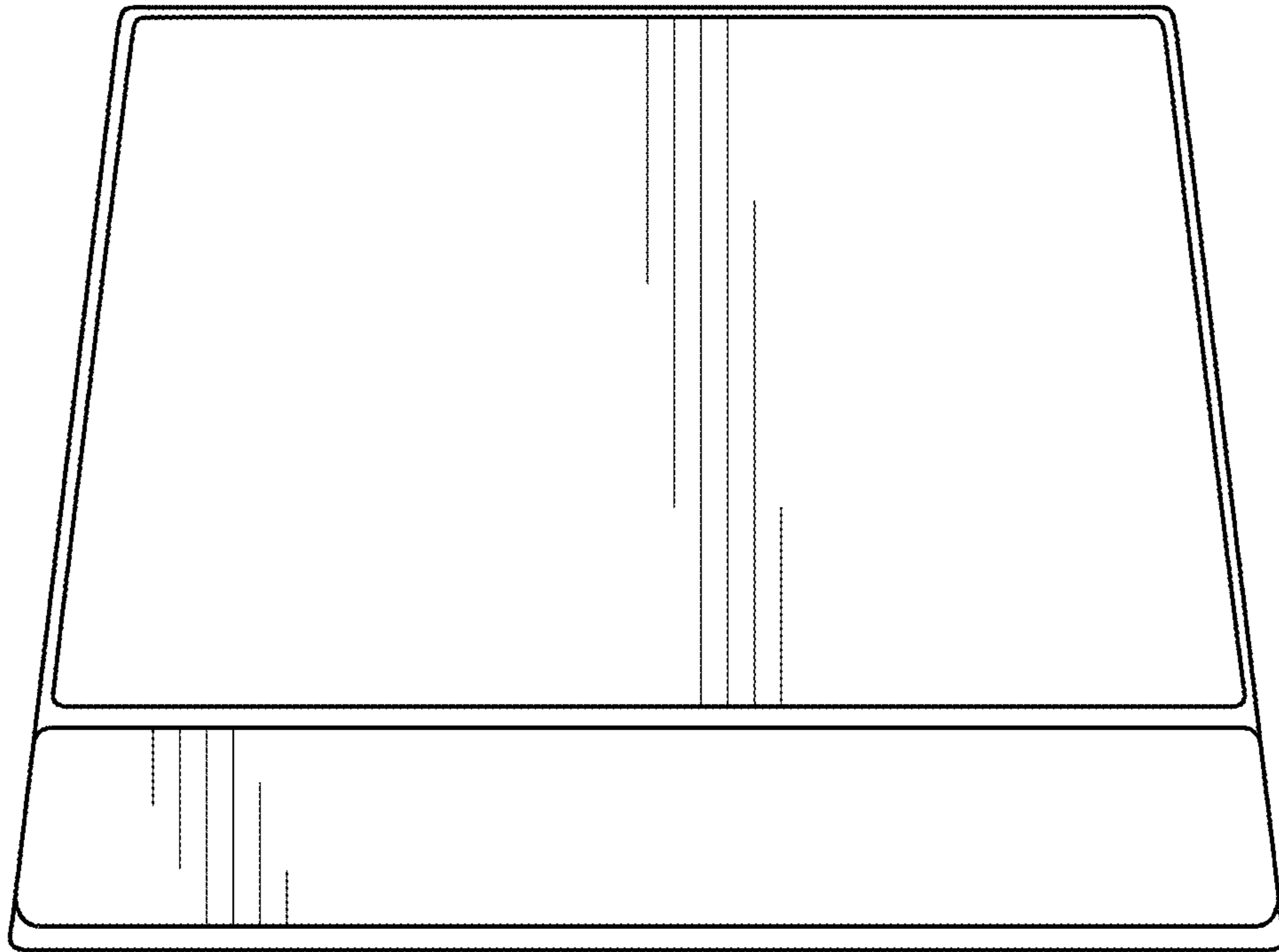


FIG. 3

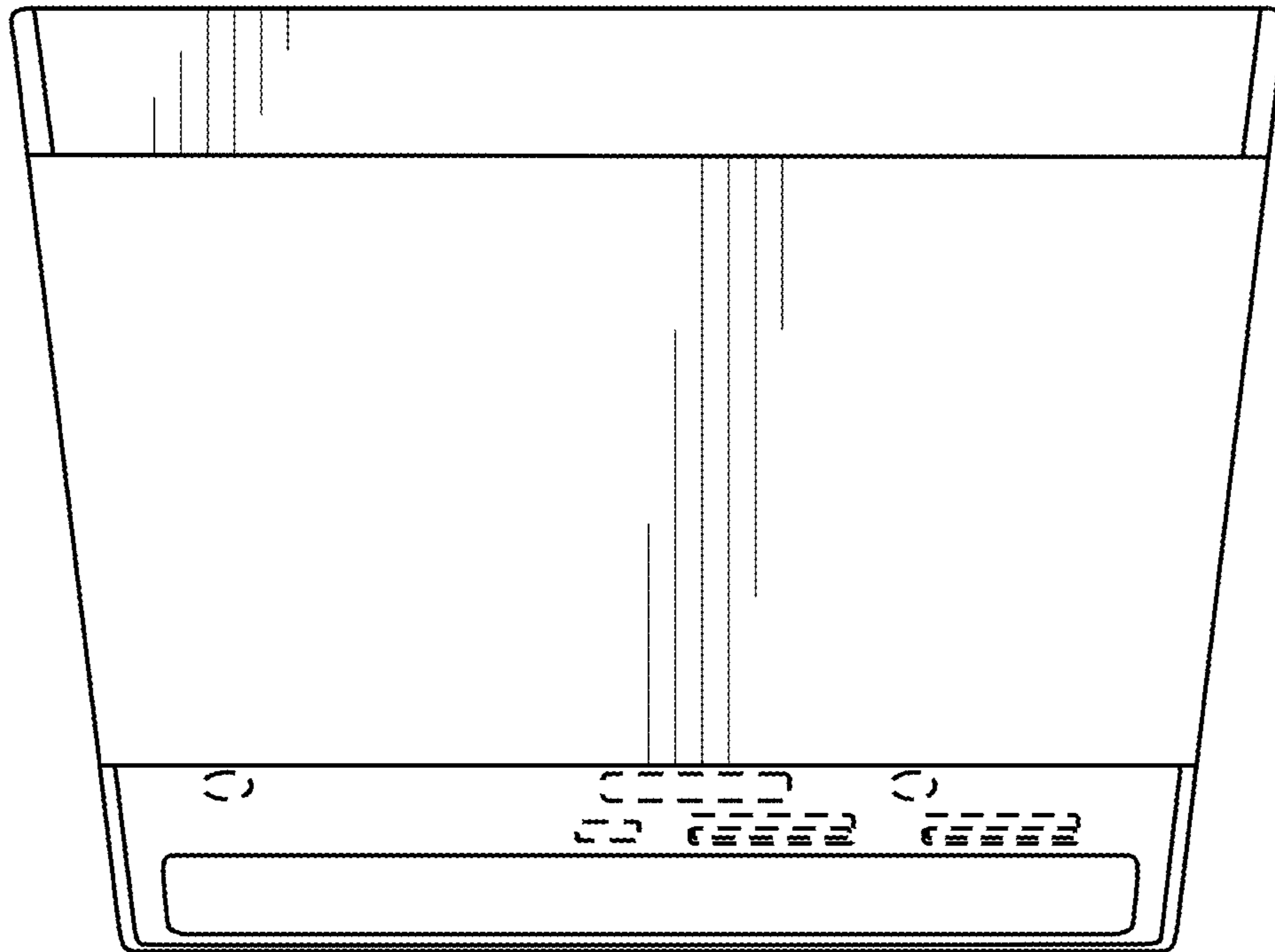


FIG. 4

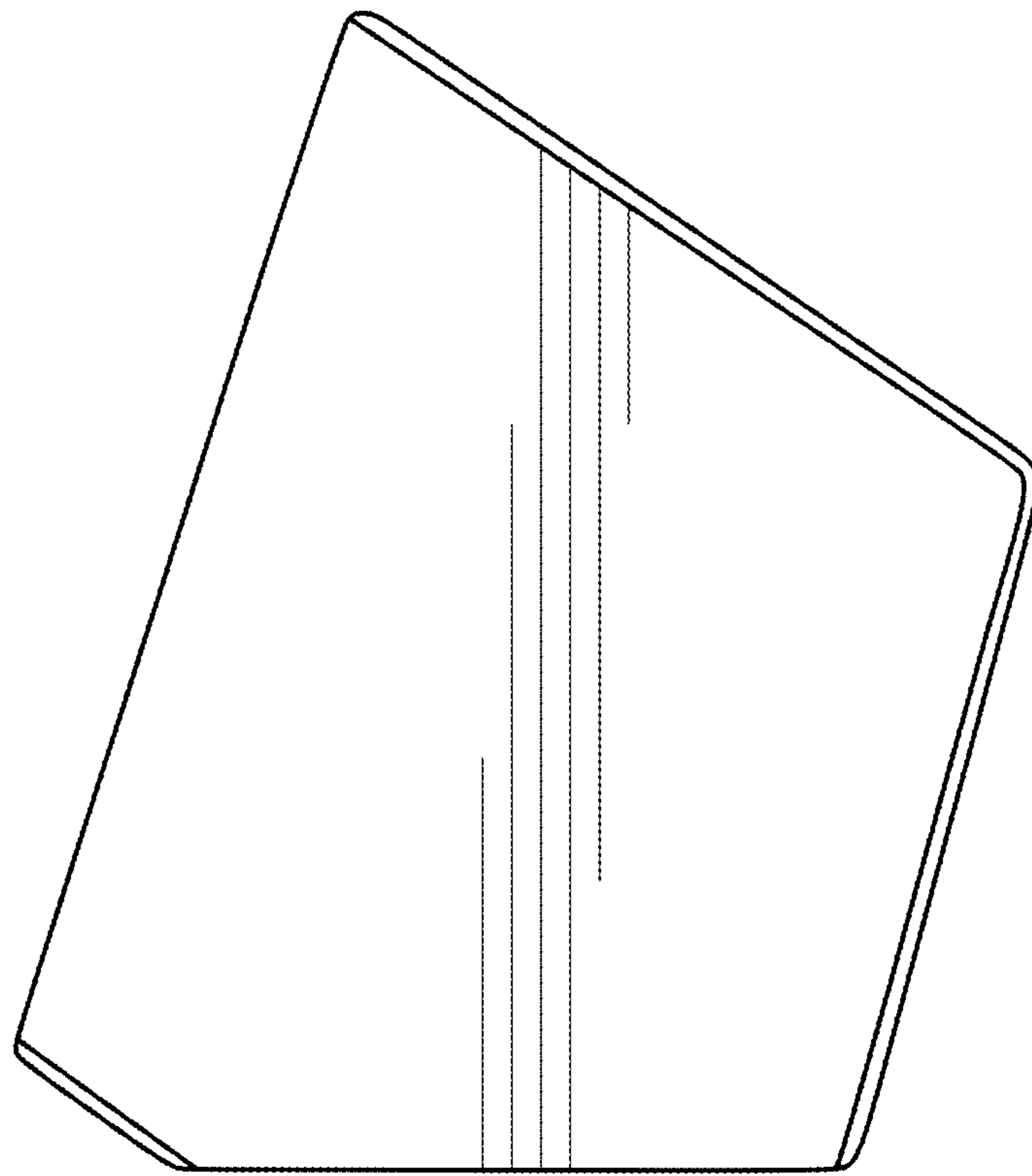


FIG. 5

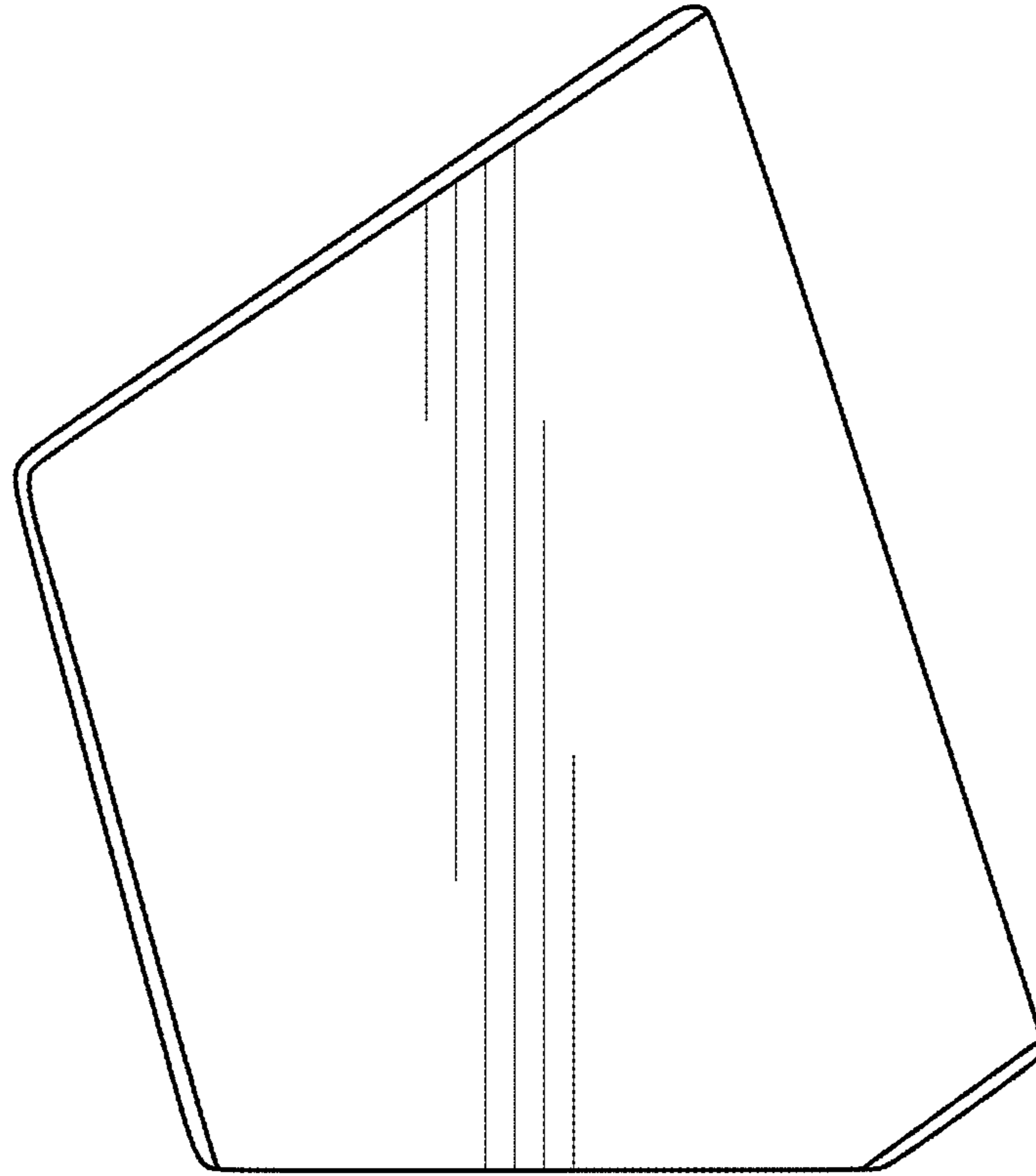


FIG. 6



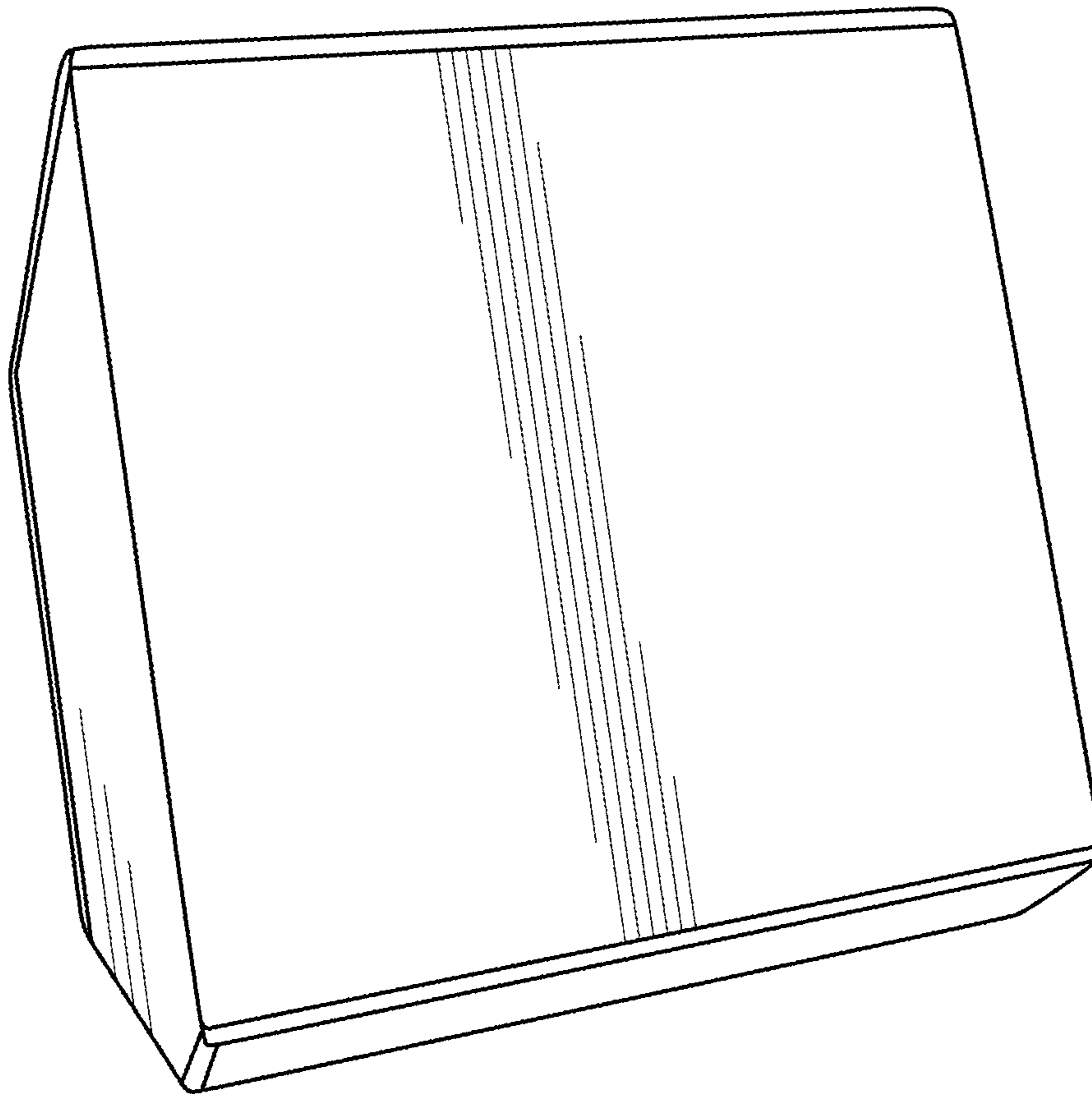


FIG. 7

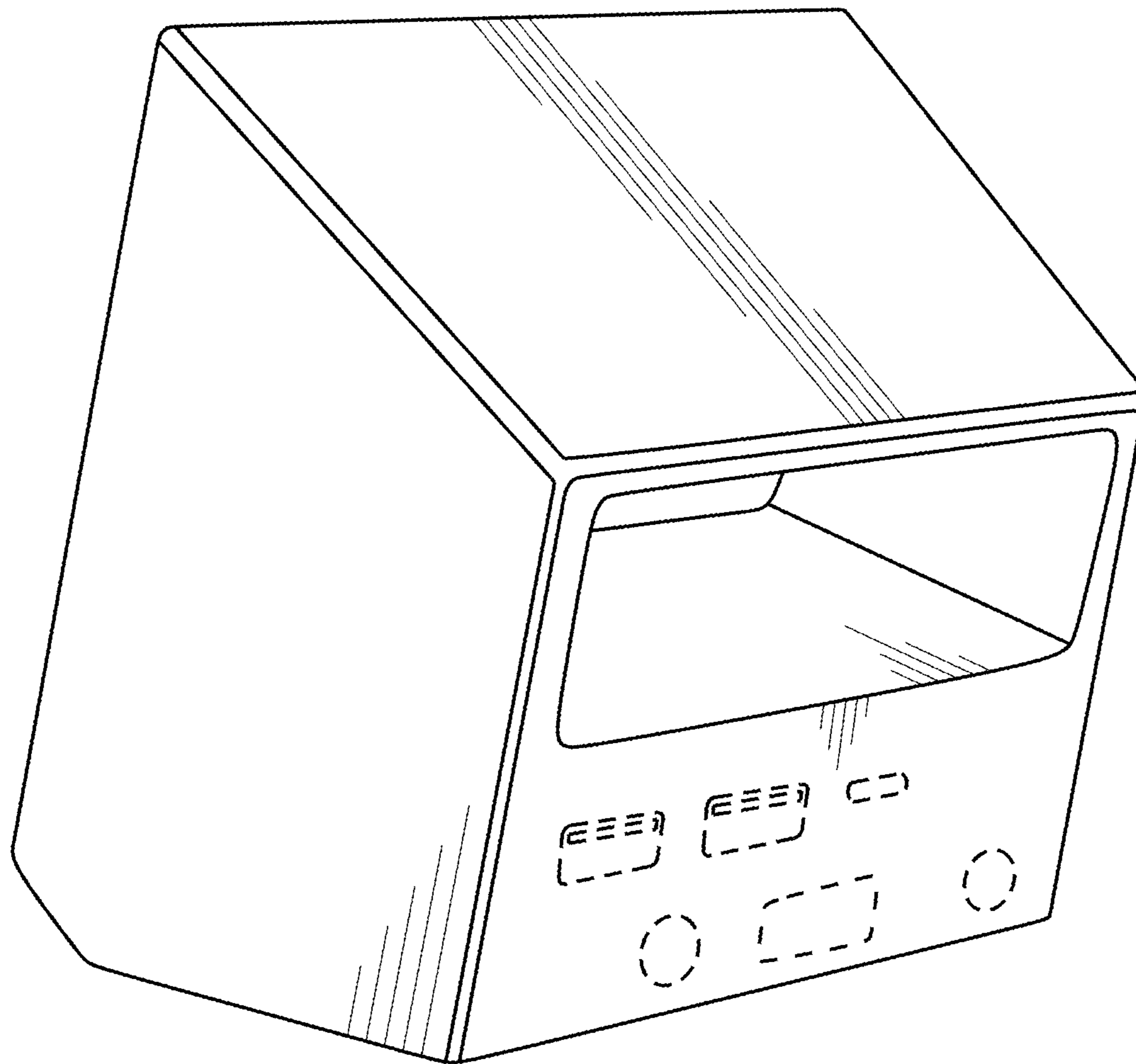


FIG. 8

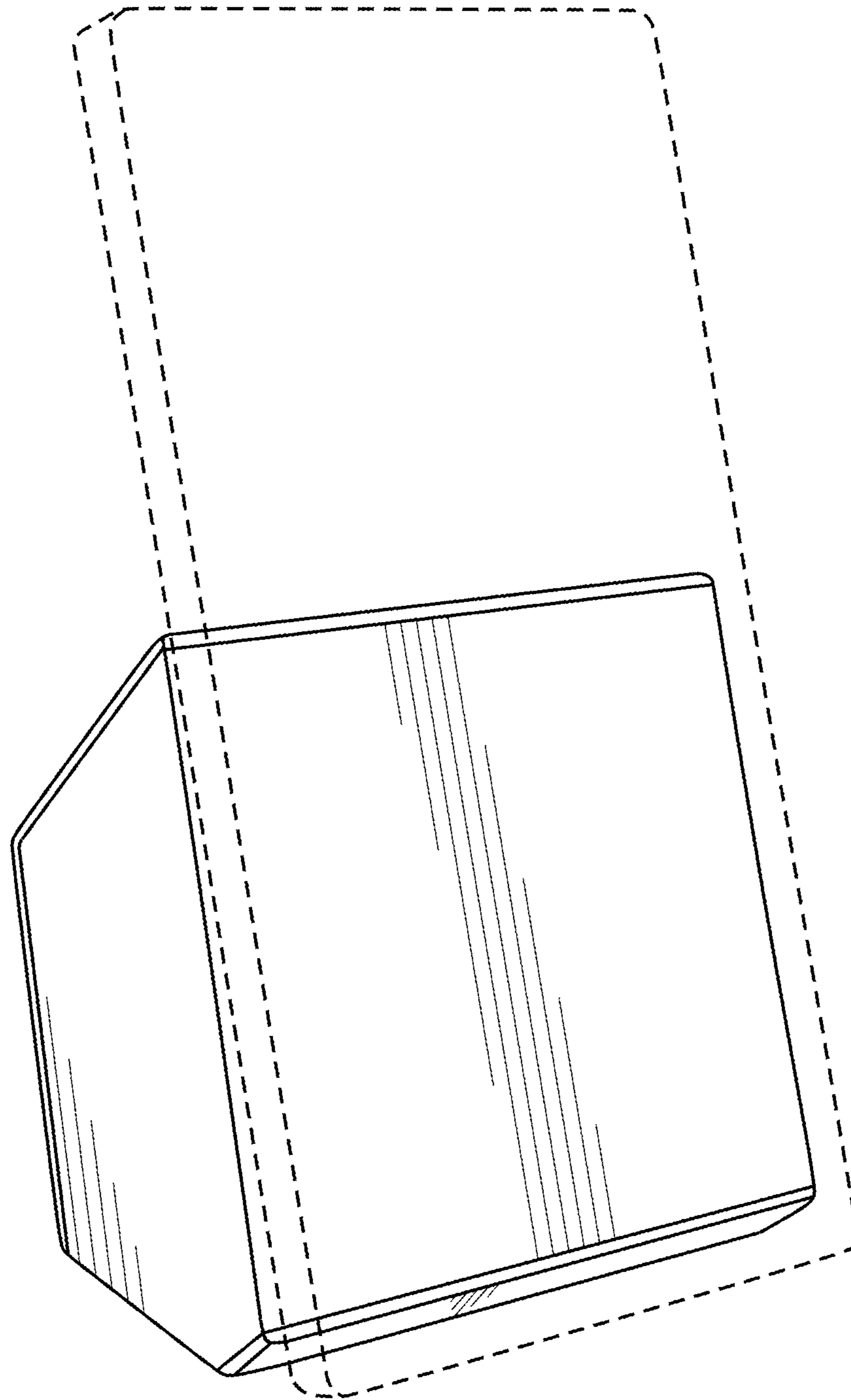


FIG. 9