

US00D811249S

(12) **United States Design Patent** (10) **Patent No.:** **US D811,249 S**
Li (45) **Date of Patent:** **** Feb. 27, 2018**

(54) **USB VOLTAGE AND CURRENT TESTER**

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(72) Inventor: **Wailin Li**, Guangzhou (CN)

(**) Term: **15 Years**

(21) Appl. No.: **29/564,923**

(22) Filed: **May 17, 2016**

(30) **Foreign Application Priority Data**

Apr. 18, 2016 (CN) 2016 3 0129992

(51) **LOC (11) Cl.** **10-04**

(52) **U.S. Cl.**
USPC **D10/78**

(58) **Field of Classification Search**
USPC D10/78
CPC G01R 1/04; G01R 1/0408; G01R 1/0416;
G01R 1/0425; G01R 1/0433; G01R
1/0441; G01R 1/045; G01R 1/0458;
G01R 1/0466; G01R 1/0475; G01R
1/0483; G01R 1/0491; G01R 1/06777;
G01R 1/06788; G01R 15/16; G01R
15/165; G01R 19/14

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D698,674 S * 2/2014 Mattsson D10/78

* cited by examiner

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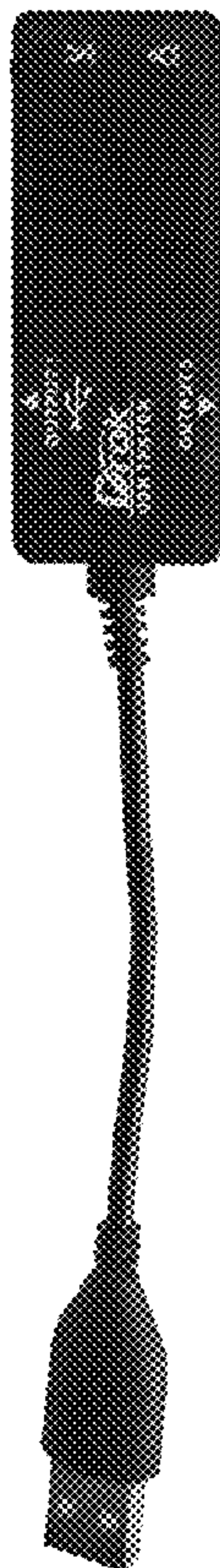
(57) **CLAIM**

The ornamental design for a USB voltage and current tester,
as shown and described.

DESCRIPTION

FIG. 1 is a front view of a USB voltage and current tester showing my new design in accordance with an embodiment; FIG. 2 is a back view thereof; FIG. 3 is a left side view thereof; FIG. 4 is a right side view thereof; FIG. 5 is a top view thereof; FIG. 6 is a front view of the USB voltage and current tester of FIG. 1 showing a first state of use thereof; FIG. 7 is a front view of the USB voltage and current tester of FIG. 1 showing a second state of use thereof; and, FIG. 8 is a front view of the USB voltage and current tester of FIG. 1 showing a third state of use thereof.

1 Claim, 8 Drawing Sheets



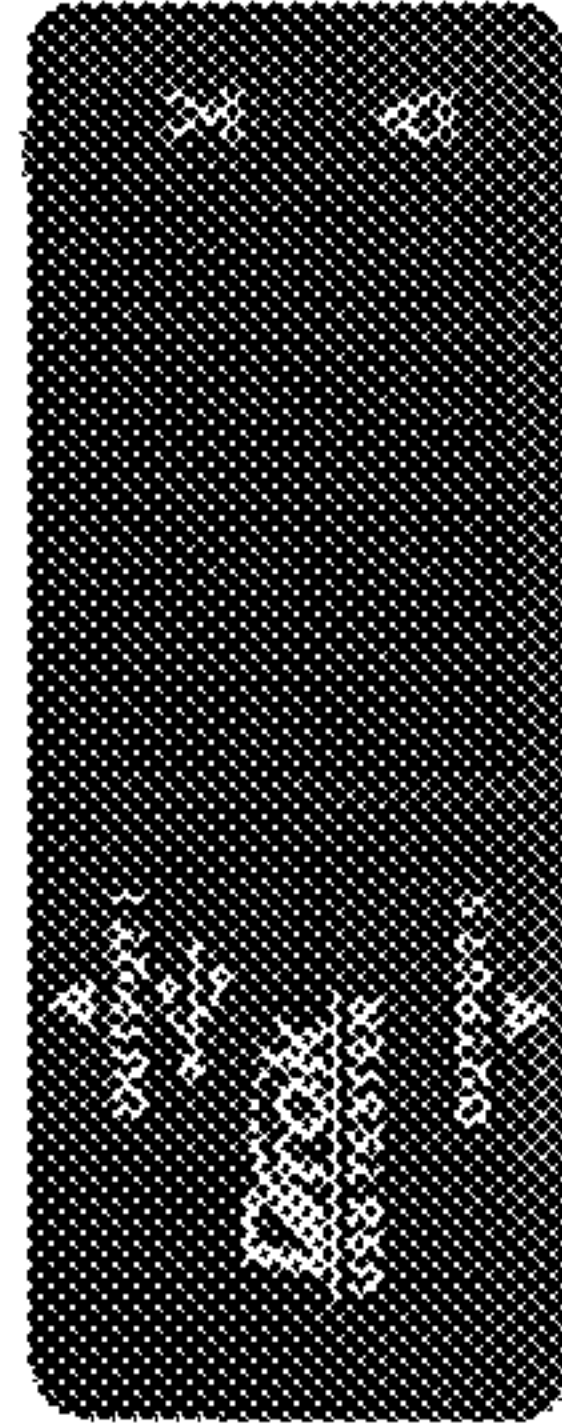


FIG. 1

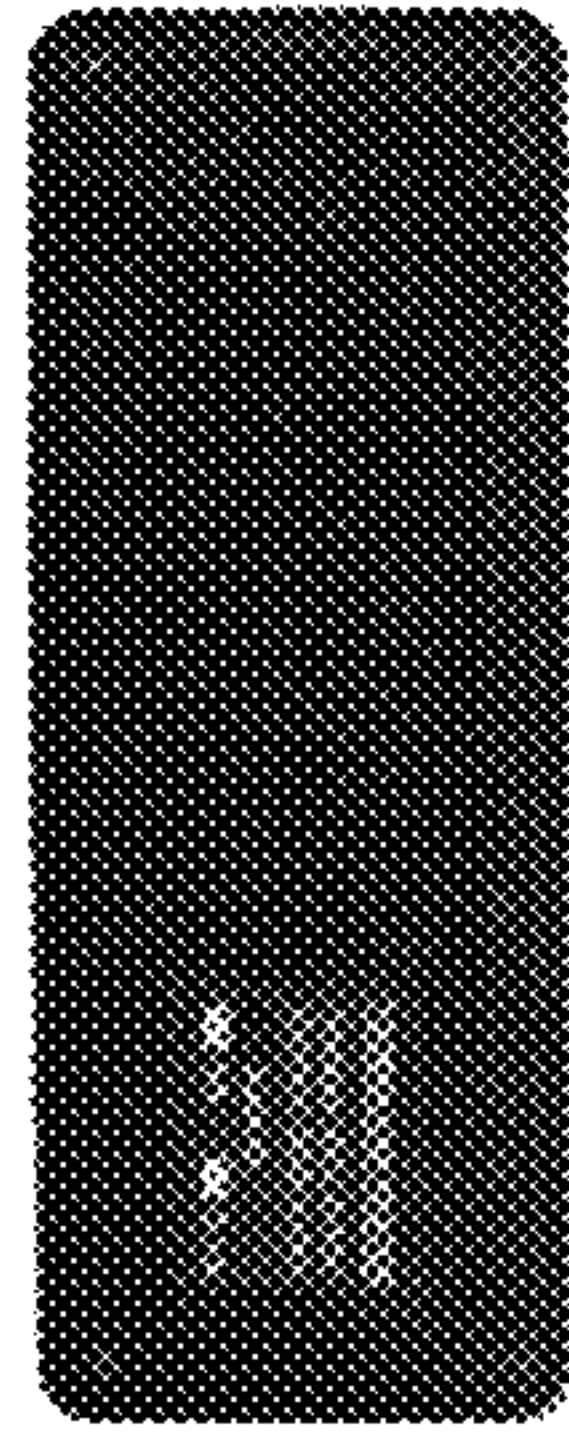


FIG. 2

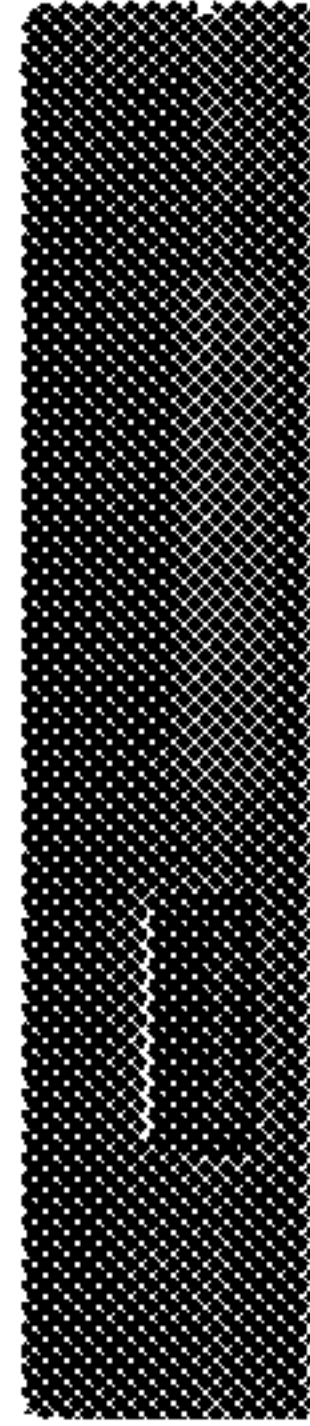


FIG. 3

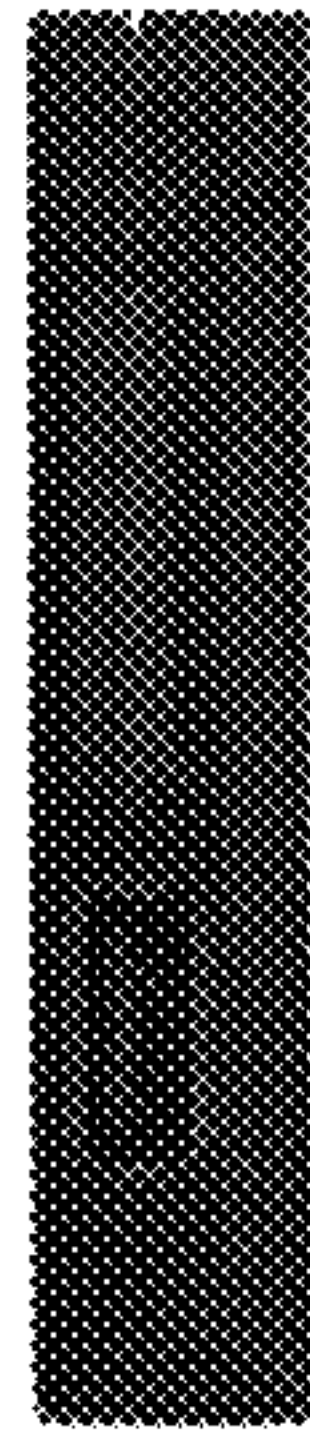


FIG. 4

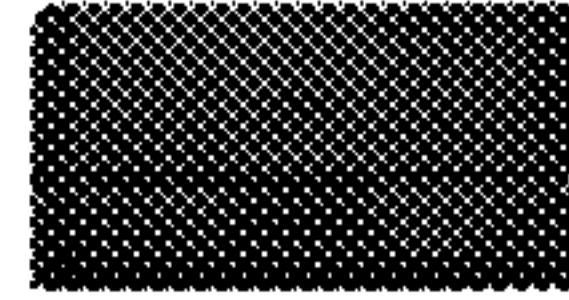


FIG. 5

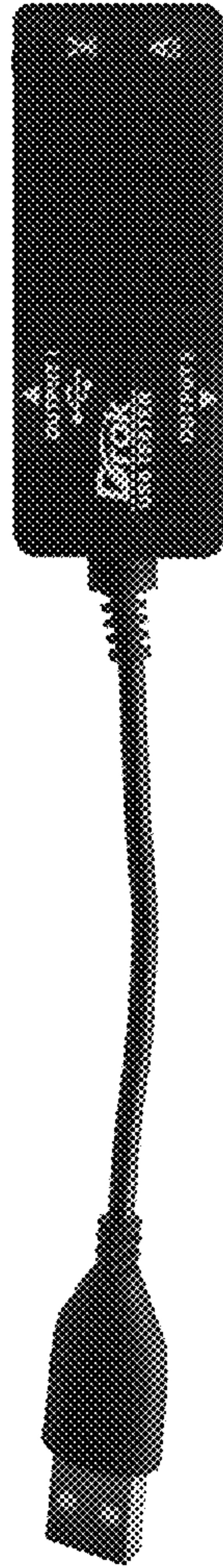


FIG. 6

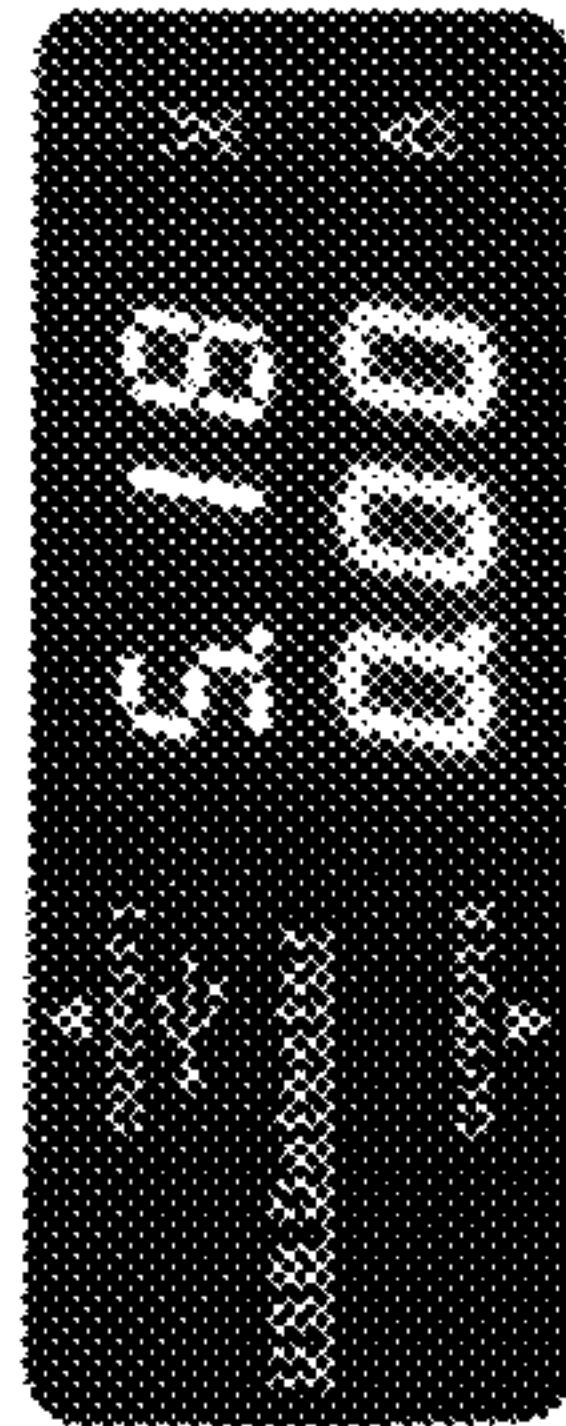


FIG. 7

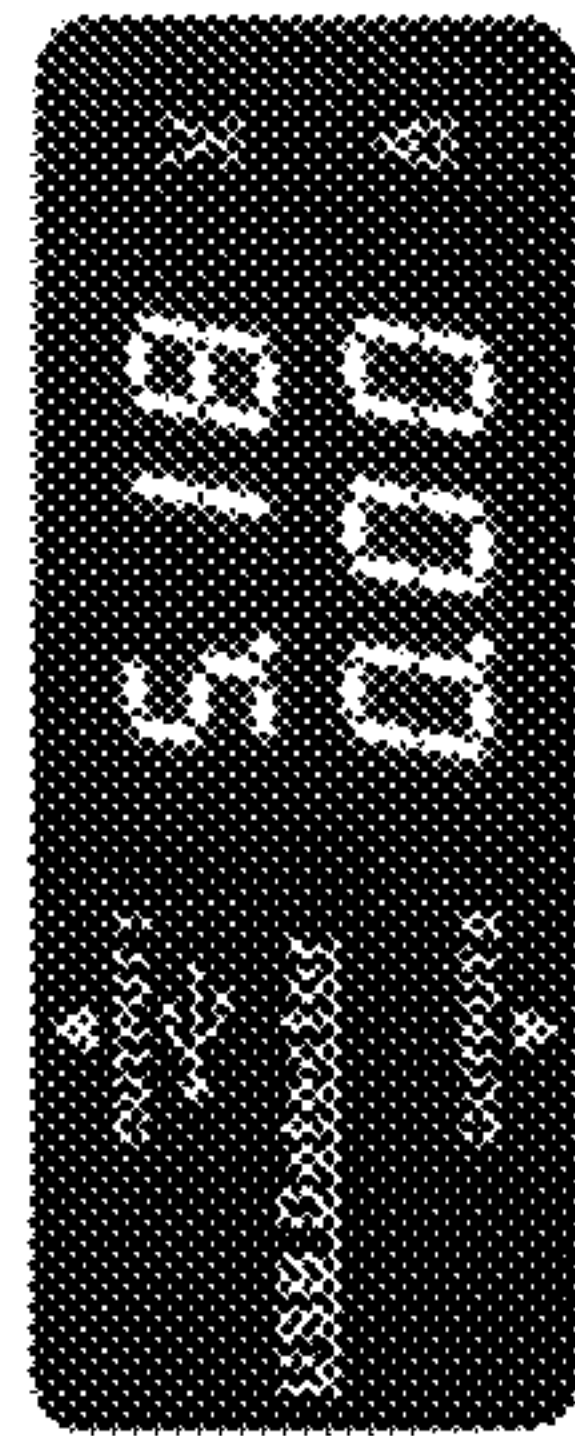


FIG. 8