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(12) **United States Design Patent**  
**Wengreen**

(10) **Patent No.:** **US D808,367 S**  
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- (54) **MOUNT FOR ELECTRONIC DEVICES**
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- (72) Inventor: **Eric John Wengreen**, Sammamish, WA (US)
- (73) Assignee: **Innovelis, Inc.**, Sammamish, WA (US)
- (\*\*) Term: **15 Years**
- (21) Appl. No.: **29/593,760**
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**Related U.S. Application Data**

- (63) Continuation of application No. 15/424,837, filed on Feb. 4, 2017.
- (51) **LOC (11) Cl.** ..... **14-03**
- (52) **U.S. Cl.**  
USPC ..... **D14/238.1**
- (58) **Field of Classification Search**  
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D14/140, 142, 496, 203.1-203.8;  
D13/103, 107, 108, 118, 184; D21/324,  
D21/333; 273/148 B; 463/1, 29-35, 46,  
463/47; 345/156-164  
CPC ..... H02J 7/00; G06F 16/00; G06F 1/1616;  
G06F 1/1828; G96F 1/266  
See application file for complete search history.

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*Primary Examiner* — Prabhakar G Deshmukh

(57) **CLAIM**

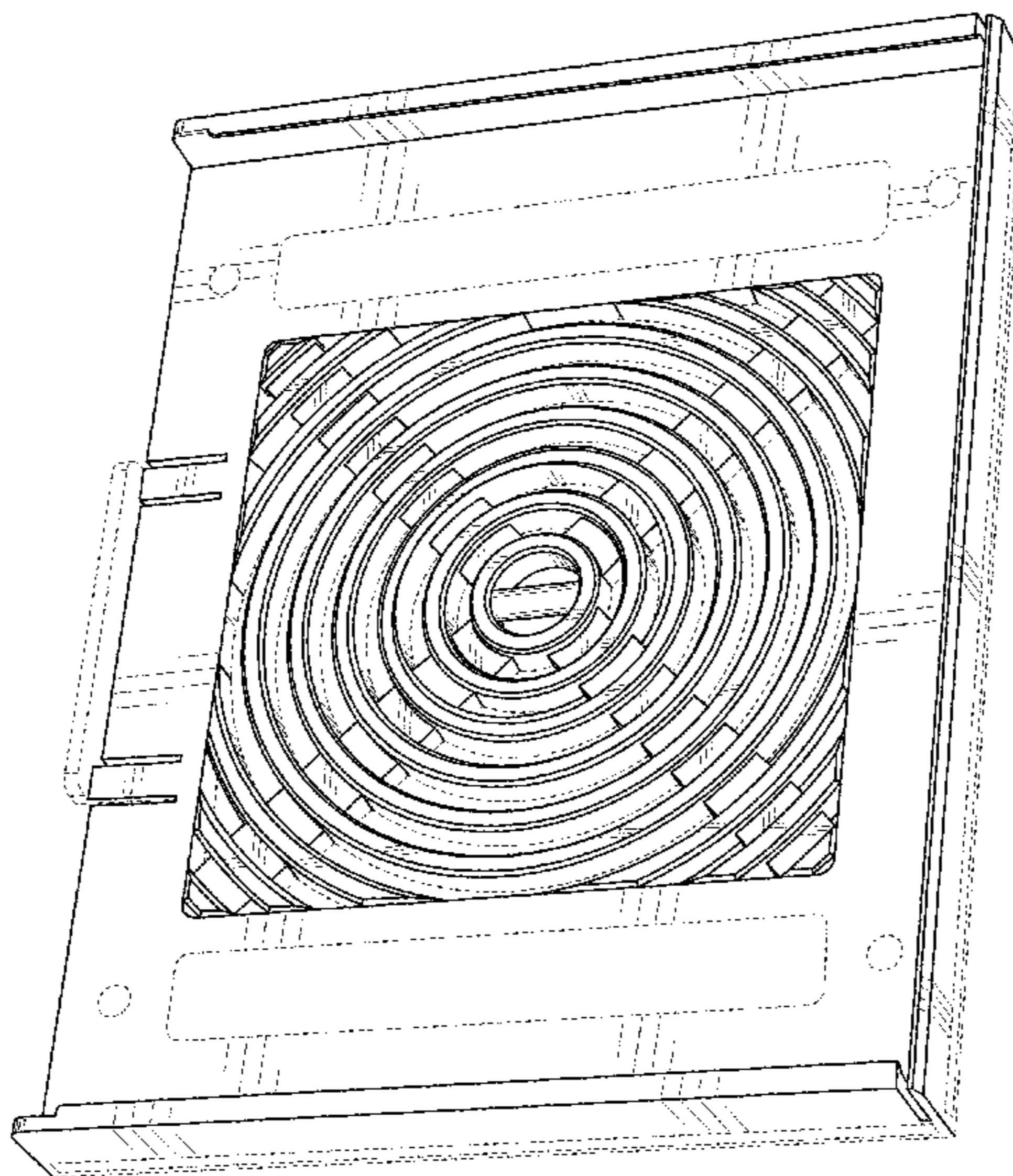
The ornamental design for a mount for electronic devices, as shown and described.

**DESCRIPTION**

FIG. 1 is a front, first side, and bottom perspective view of a mount for electronic devices embodying the new design; FIG. 2 is a back, second side, and top perspective view thereof; FIG. 3 is a bottom view thereof; FIG. 4 is a top view thereof; FIG. 5 is a second side view thereof; FIG. 6 is a first side view thereof; FIG. 7 is a front view thereof; and, FIG. 8 is a back view thereof.

The uniform repeating broken lines depict portions of a mount for electronic devices in which the claimed design is embodied and form no part of the claimed design.

**1 Claim, 8 Drawing Sheets**





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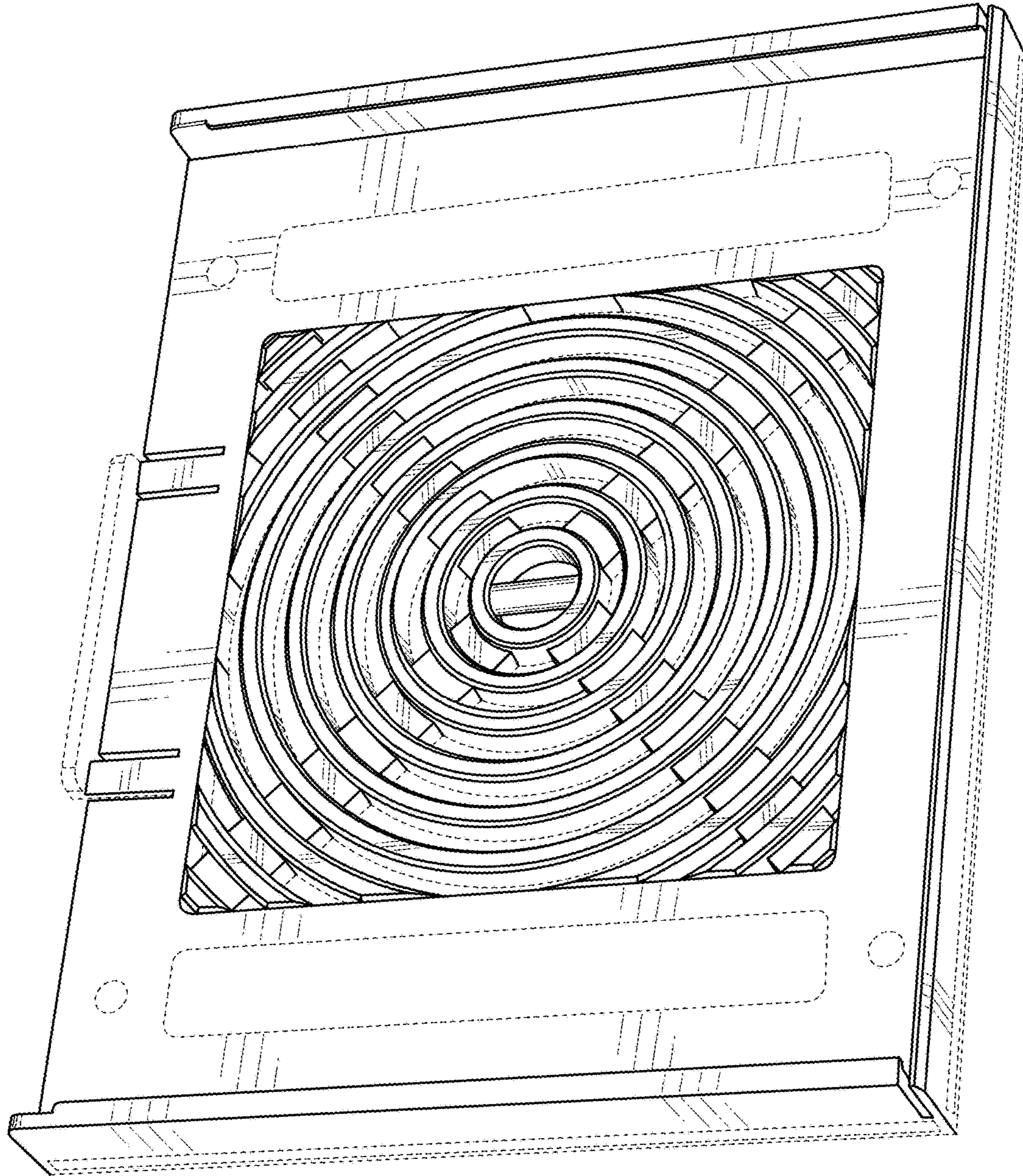


FIG. 1

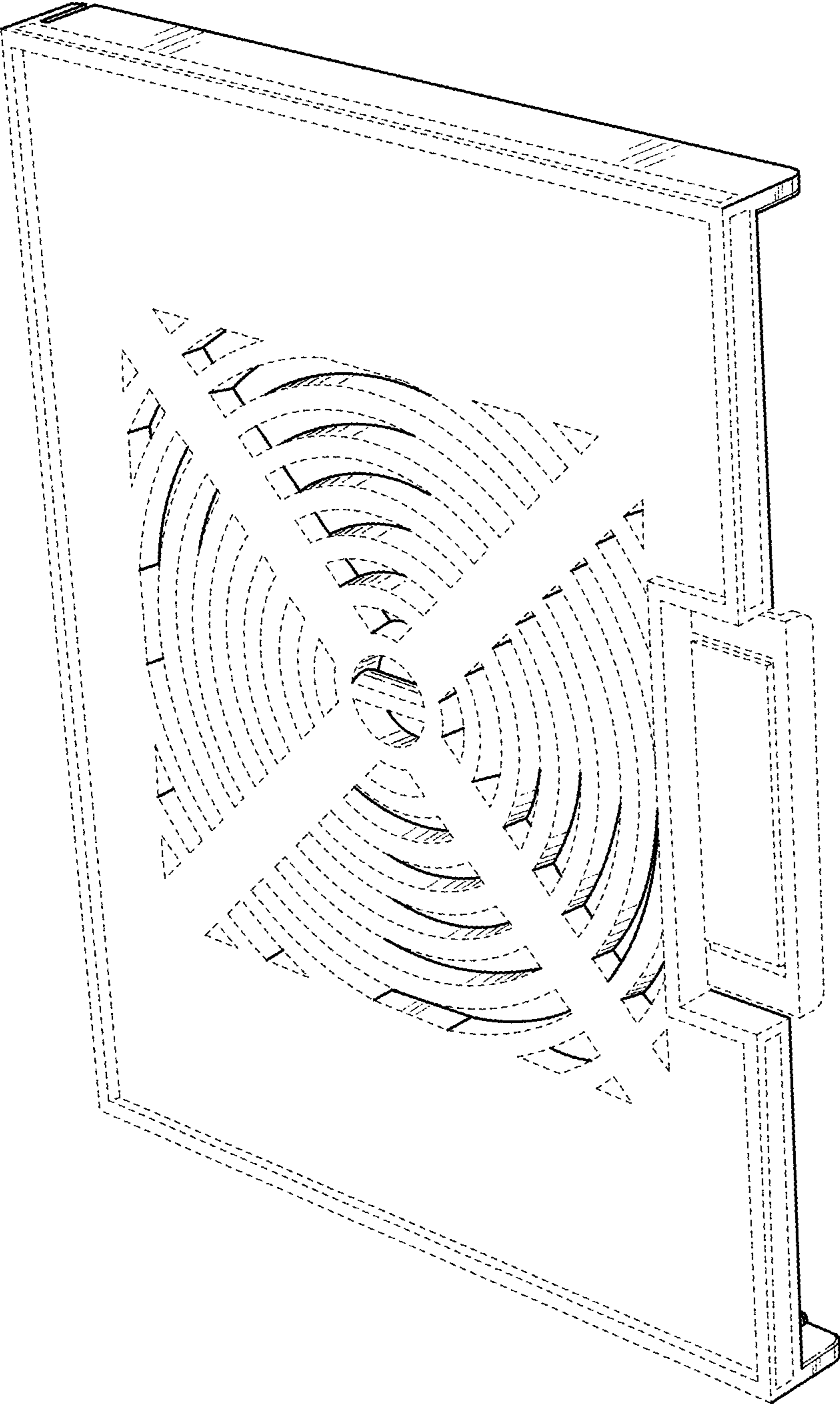


FIG. 2

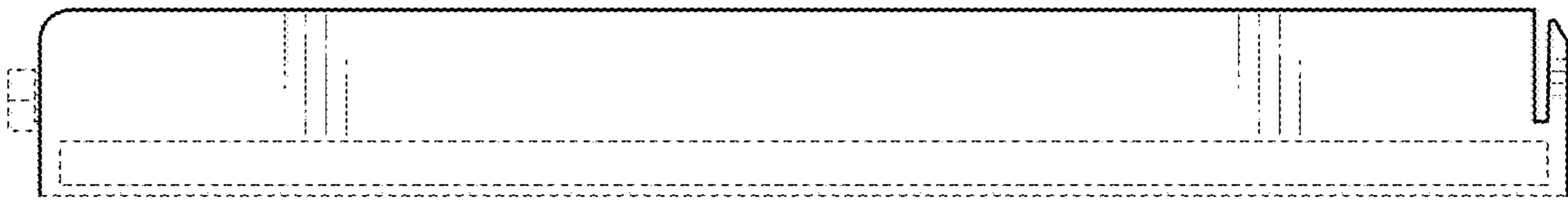


FIG. 3

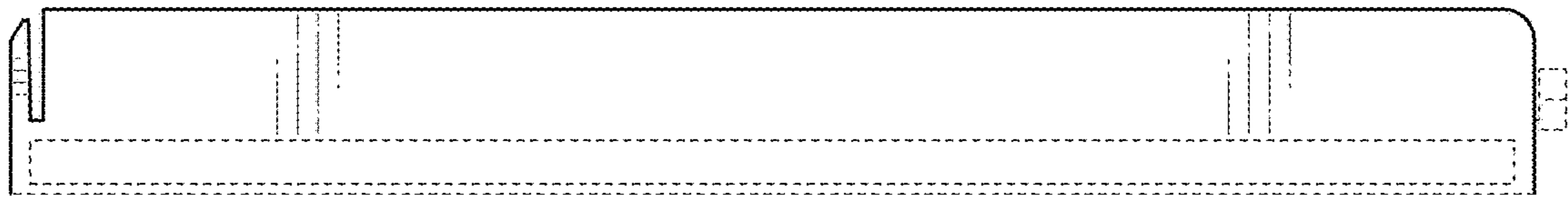


FIG. 4

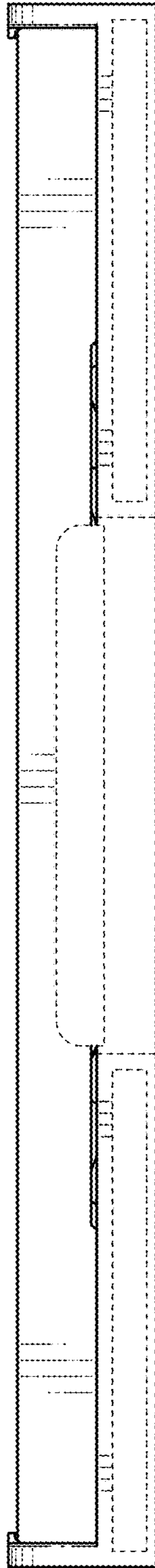


FIG. 5



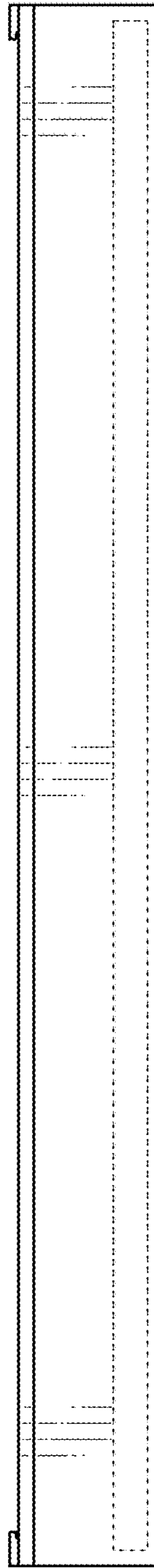


FIG. 6

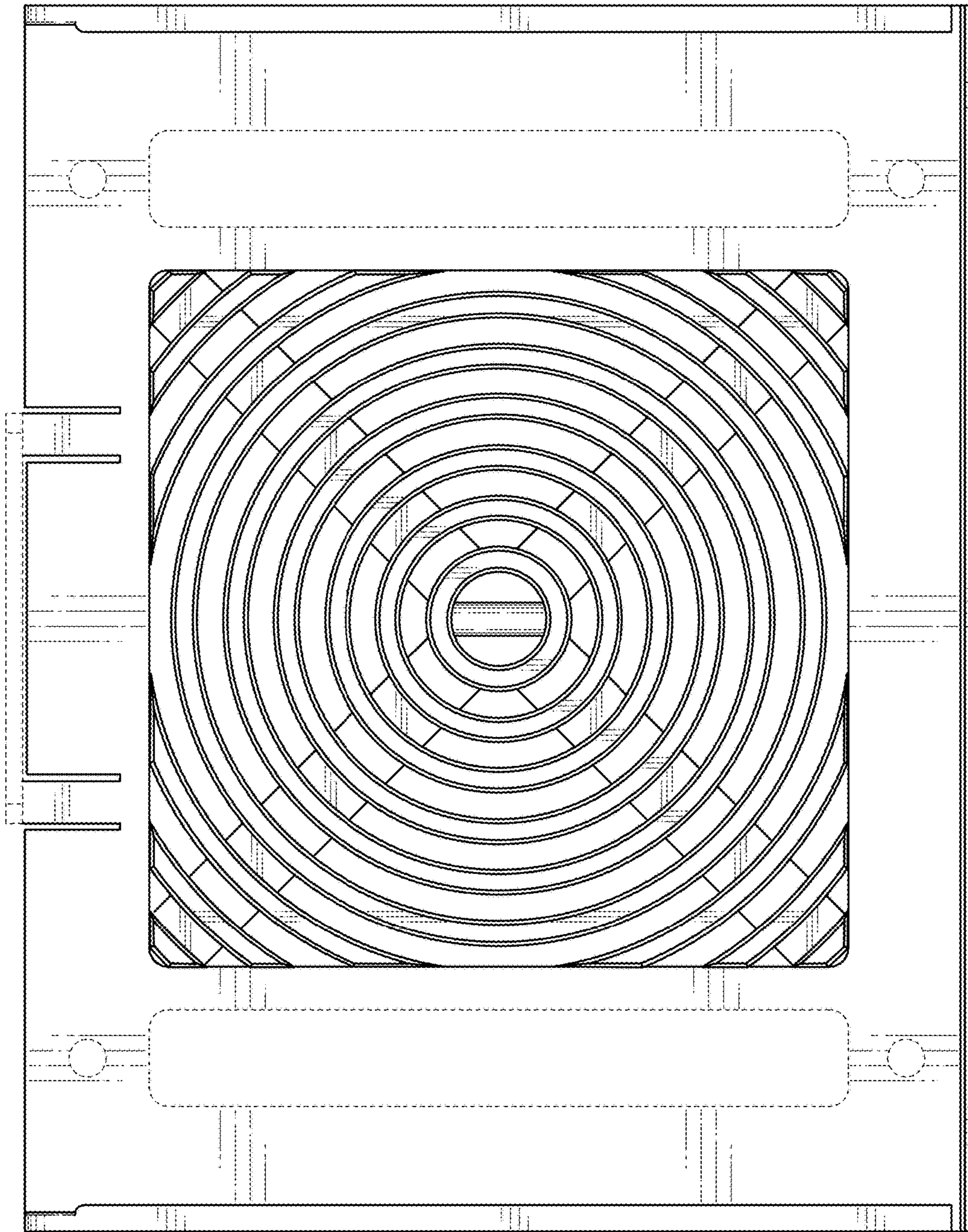


FIG. 7

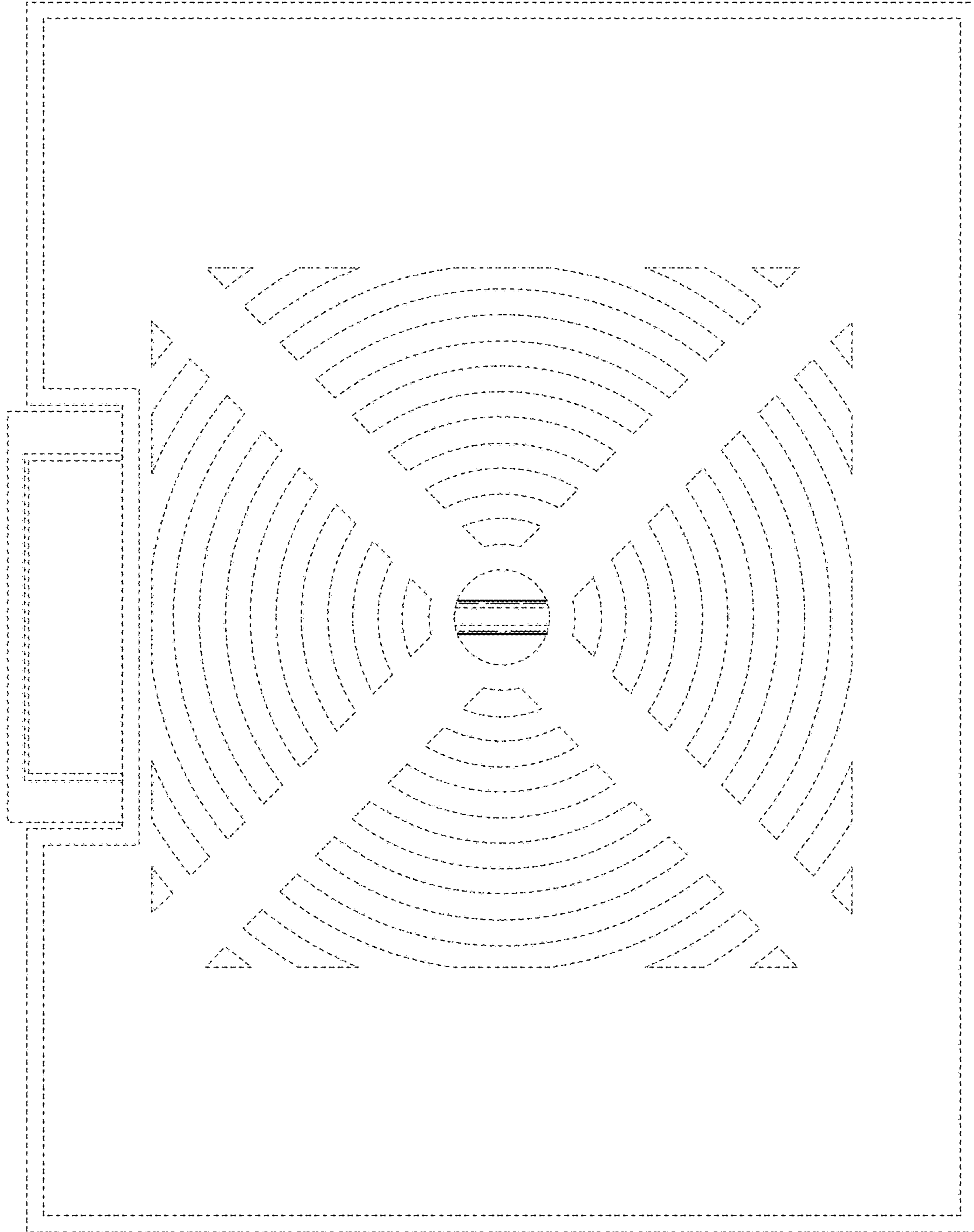


FIG. 8