



US00D804466S

(12) **United States Design Patent** (10) **Patent No.:** **US D804,466 S**
Okuley et al. (45) **Date of Patent:** **** Dec. 5, 2017**

(54) **ELECTRONIC DEVICE**

(56) **References Cited**

(71) Applicant: **Intel Corporation**, Santa Clara, CA (US)

U.S. PATENT DOCUMENTS

(72) Inventors: **James Okuley**, Portland, OR (US);
Markus Wierzoch, Seattle, WA (US);
Tim B. Hulford, Portland, OR (US);
Ben Collette, Seattle, WA (US)

6,067,224 A	5/2000	Nobuchi	
6,317,315 B1 *	11/2001	Lee	G06F 1/1601 16/254
6,519,139 B2	2/2003	Kambayashi	
D488,807 S	4/2004	Ogasawara	
D516,552 S	3/2006	Iseki	
D591,283 S	4/2009	Luminosu et al.	
8,208,245 B2 *	6/2012	Staats	G06F 1/162 361/679.02
8,699,215 B2 *	4/2014	Whitt, III	G06F 1/1618 345/169
D705,774 S *	5/2014	Hung	D14/316

(Continued)

(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/554,617**

(22) Filed: **Feb. 12, 2016**

OTHER PUBLICATIONS

USPTO Restriction Requirement in U.S. Appl. No. 29/473,872 dated Mar. 19, 2015.

(Continued)

Related U.S. Application Data

(62) Division of application No. 29/473,872, filed on Nov. 26, 2013, now Pat. No. Des. 749,561.

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/315**

(58) **Field of Classification Search**
USPC D14/315-327; D18/1, 2, 7, 11;
235/145 A, 145 R; 341/22, 23; 345/104,
345/156, 168, 169, 173; 361/679.08,
361/679.09, 679.11, 679.26, 679.27

CPC G06F 1/16; G06F 1/1616; G06F 1/1626;
G06F 1/1632; G06F 1/1613; G06F
1/1601; G06F 1/1618; G06F 1/162; G06F
1/166; G06F 1/1654; G06F 1/1681; G06F
1/1637; G06F 1/1688; G06F 1/1656;
G06F 1/1679; G06F 1/1683; G06F
1/1667; H01R 35/02; H03M 11/00; G09B
13/04; H03K 17/94; G09G 5/00; F16M
11/10; H05K 5/00; H04M 1/0237
See application file for complete search history.

Primary Examiner — Freda S Nunn

(74) *Attorney, Agent, or Firm* — Patent Capital Group

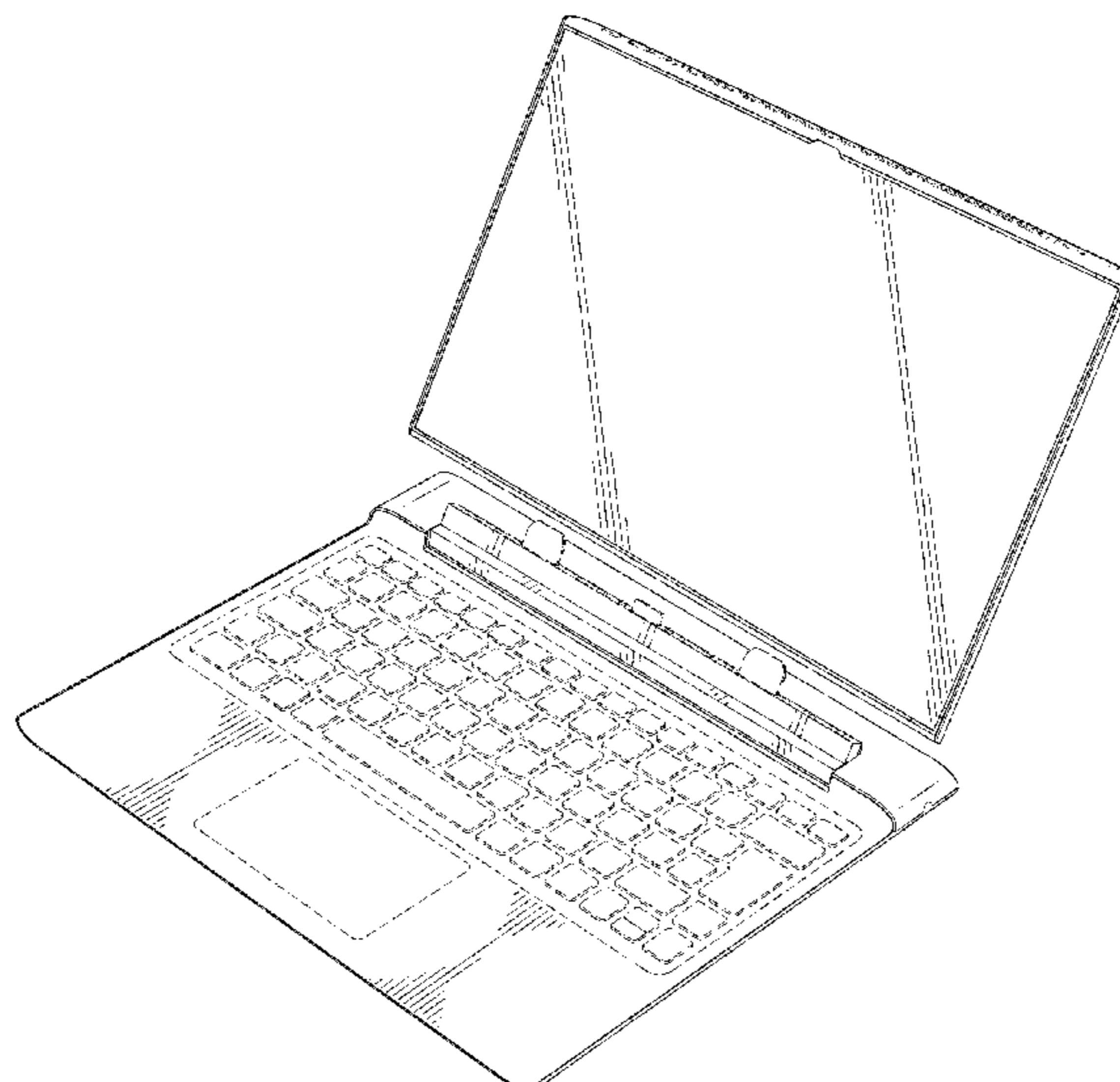
(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic device showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a back view thereof;
FIG. 4 is a side view thereof;
FIG. 5 is an opposite side view thereof;
FIG. 6 is a top view thereof; and,
FIG. 7 is a bottom view thereof.
The broken lines shown in the figures depict environment or structure only and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



(56)

References Cited

U.S. PATENT DOCUMENTS

D720,741	S *	1/2015	Leonard	D14/315
9,007,761	B2 *	4/2015	Wolff	G06F 1/1632 361/679.43
9,141,146	B2 *	9/2015	Ke	G06F 1/1679
9,256,256	B2 *	2/2016	Liang	G06F 1/1632
2002/0145846	A1 *	10/2002	Helot	G06F 1/1683 361/679.27
2004/0246666	A1 *	12/2004	Maskatia	G06F 1/1616 361/679.57
2011/0162987	A1 *	7/2011	Bekele	G06F 1/1616 206/320
2012/0212900	A1 *	8/2012	Hung	G06F 1/1632 361/679.41
2013/0188304	A1 *	7/2013	Lee	G06F 1/1667 361/679.12

OTHER PUBLICATIONS

USPTO Ex Parte Quayle Action in U.S. Appl. No. 29/473,872 dated Jun. 25, 2015.

USPTO Notice of Allowance in U.S. Appl. No. 29/473,872 dated Sep. 28, 2015.

* cited by examiner

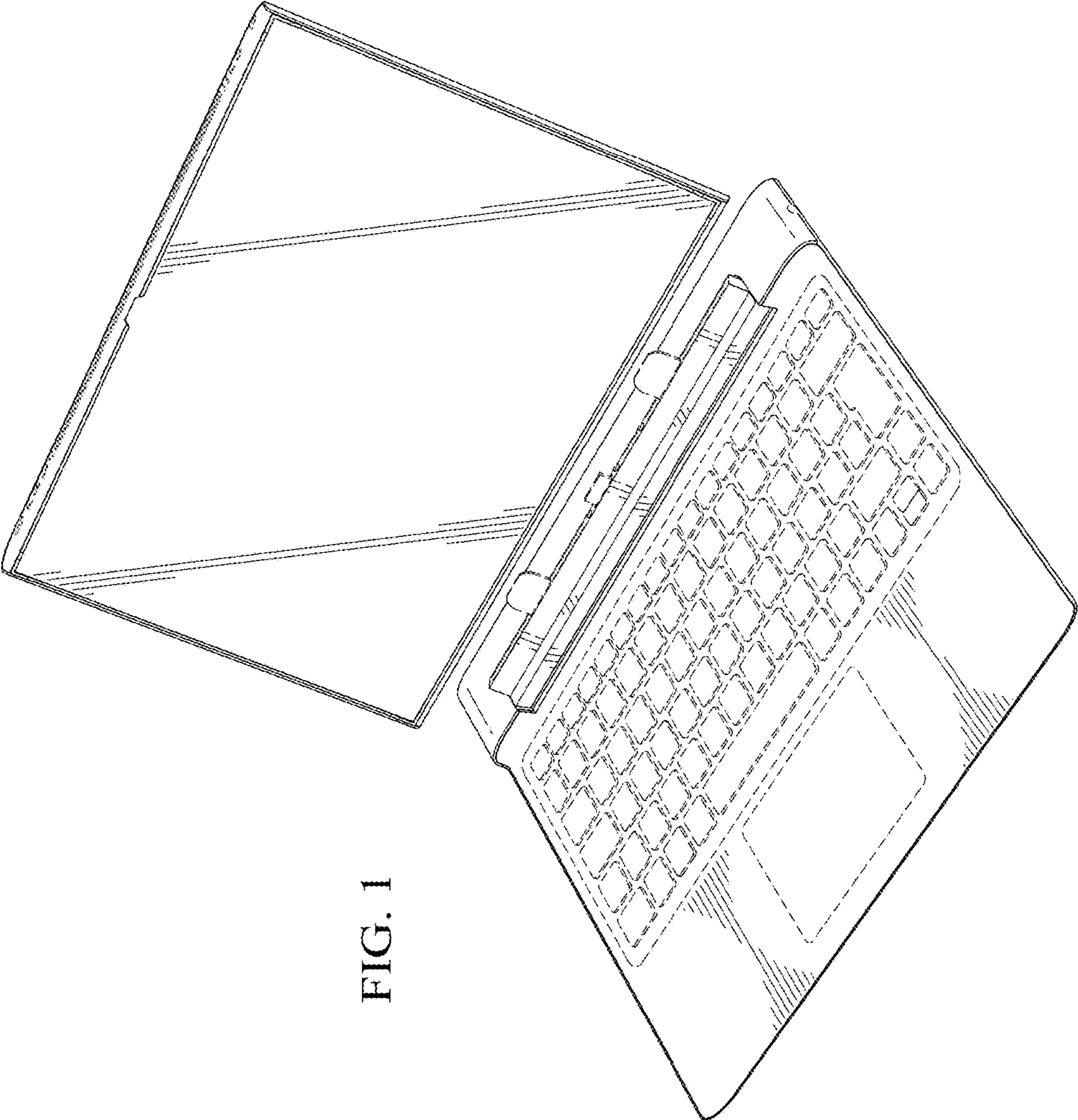


FIG. 1

FIG. 2

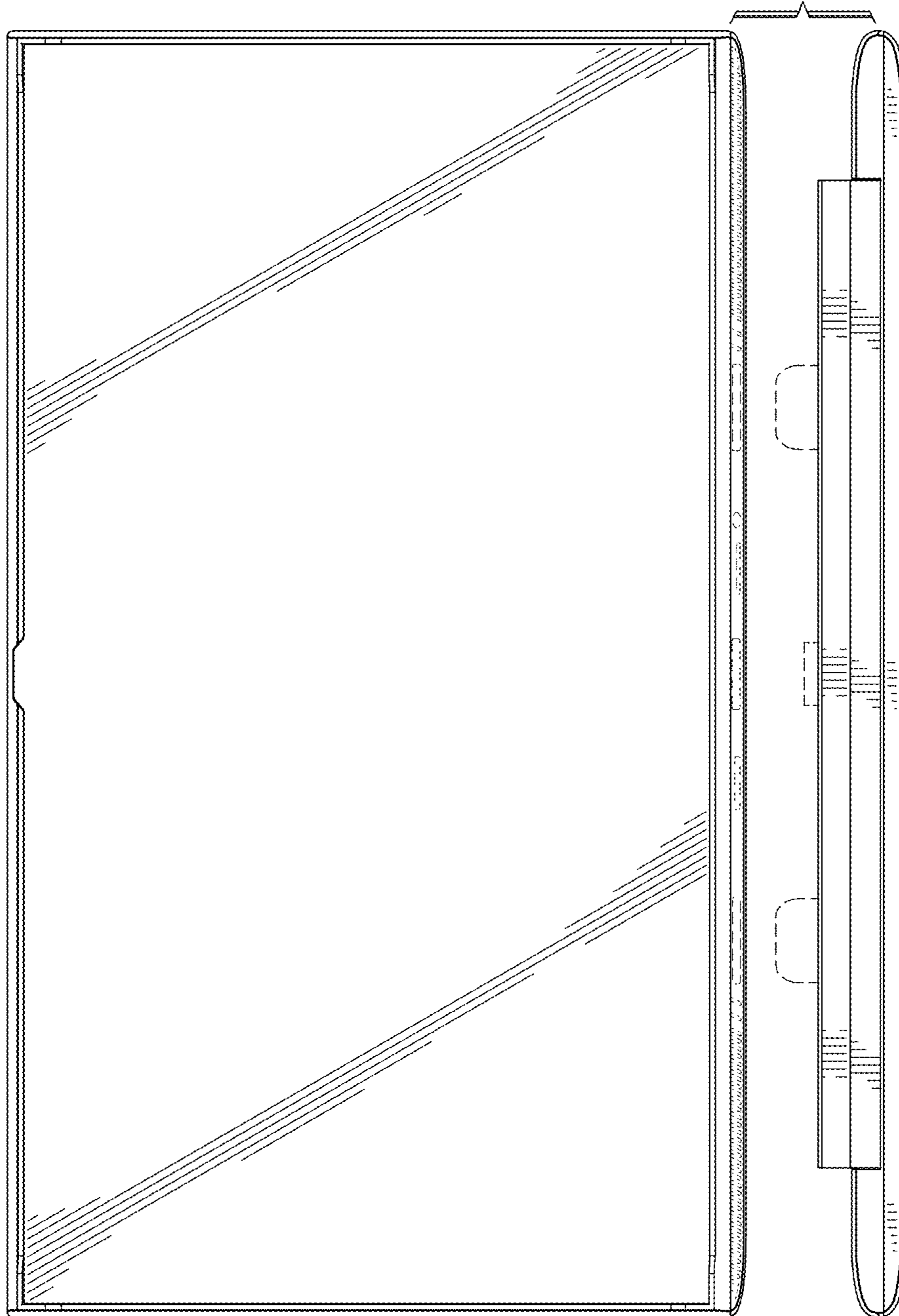
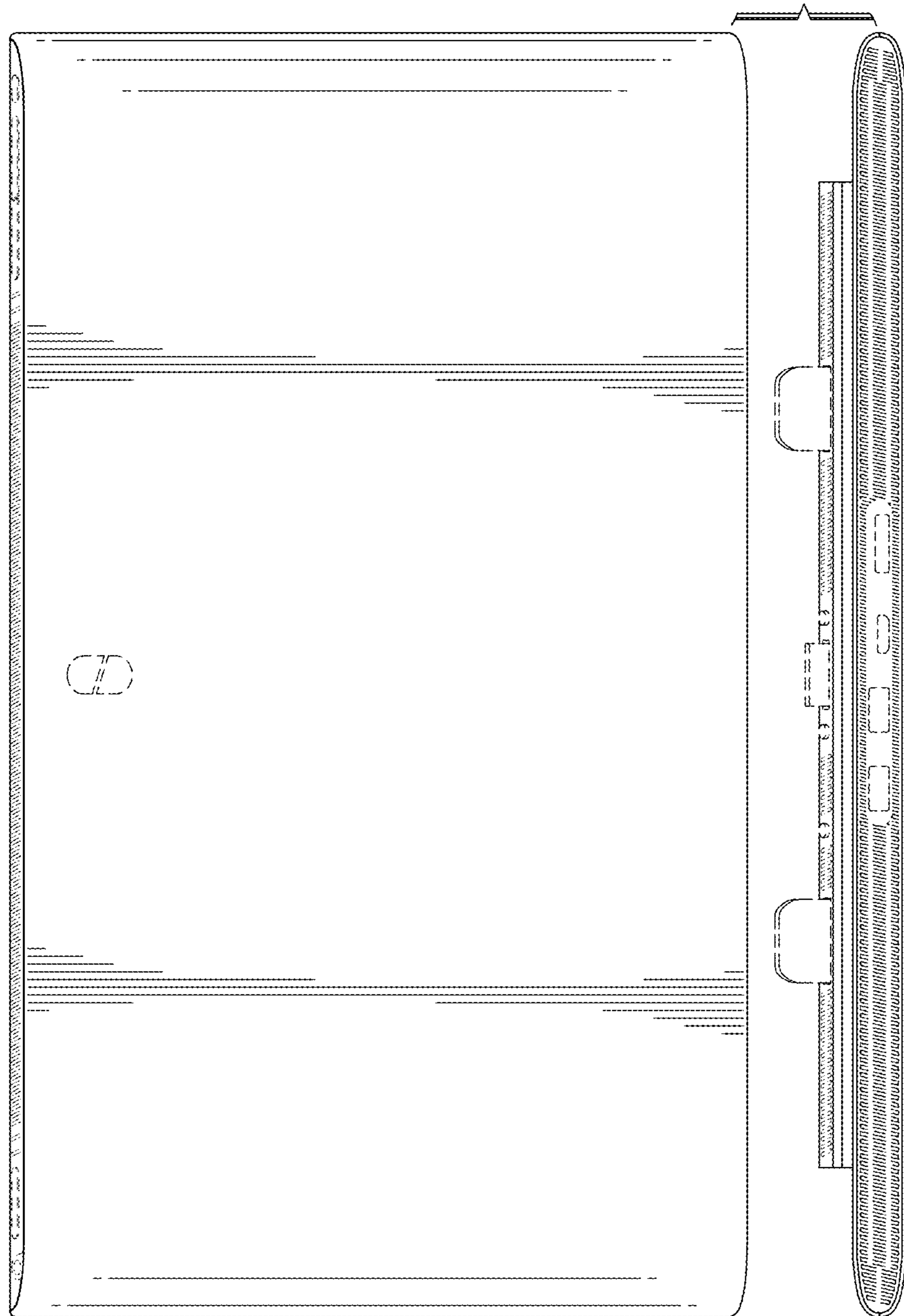


FIG. 3



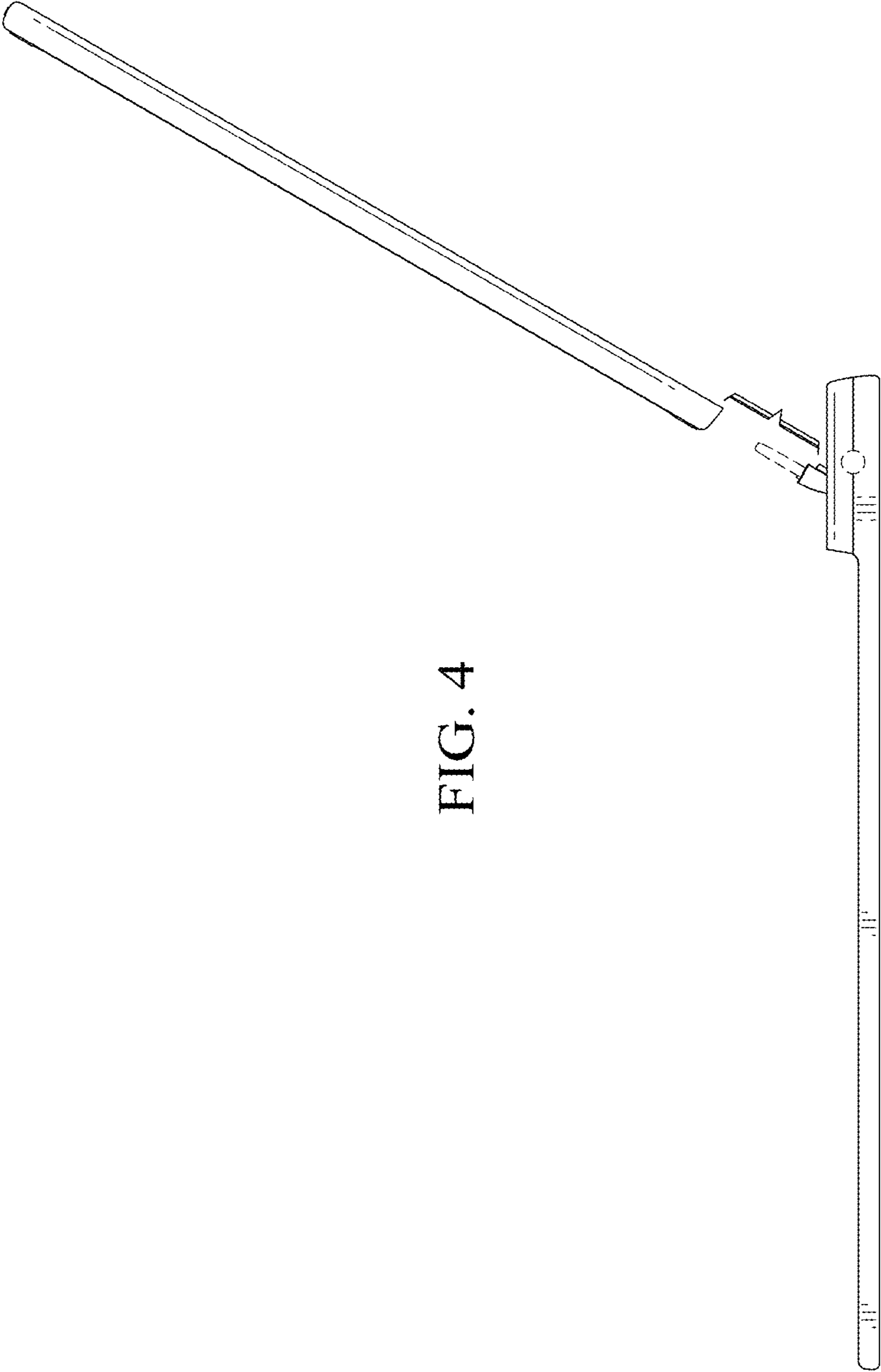


FIG. 4

FIG. 5

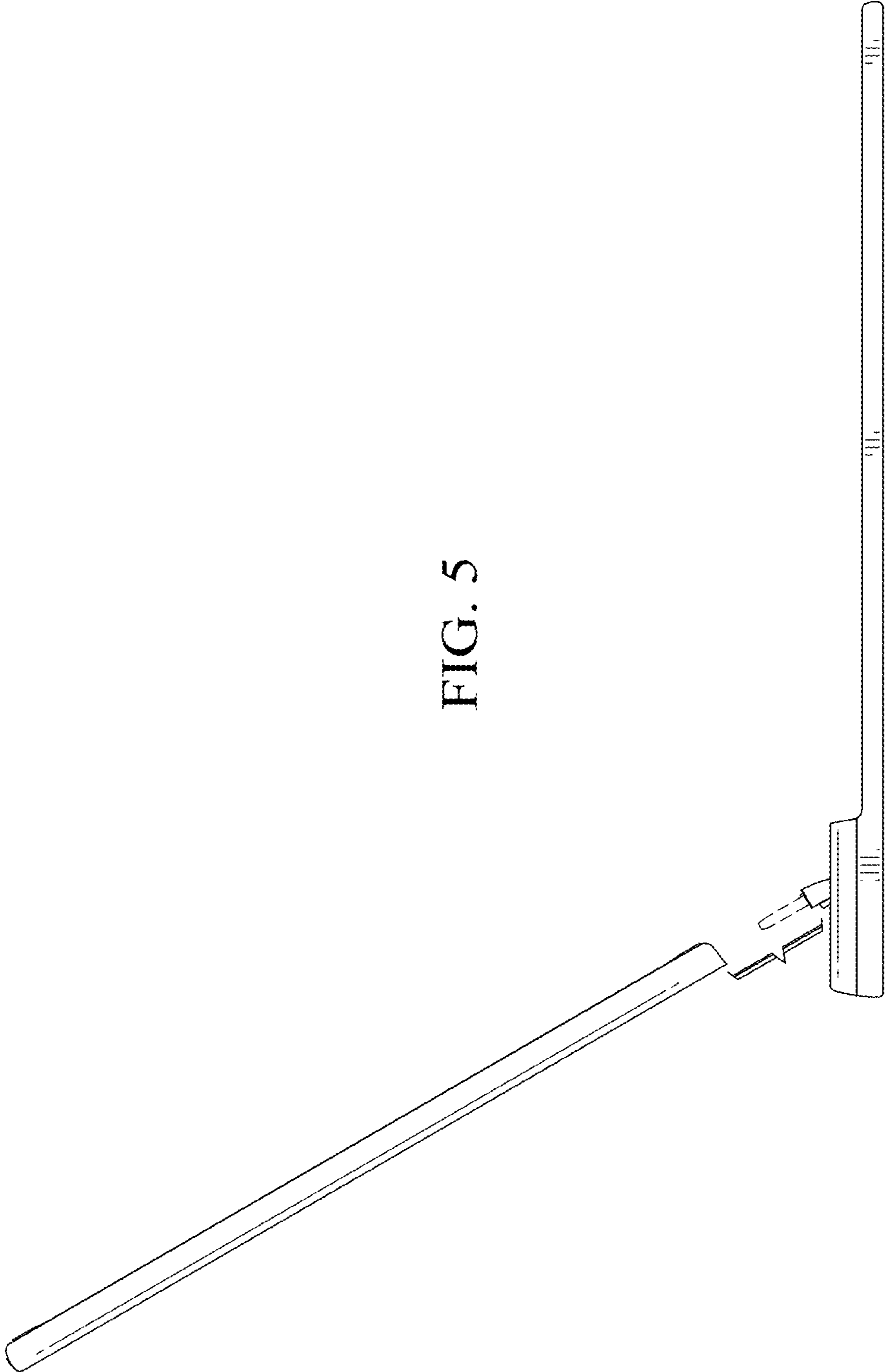


FIG. 6

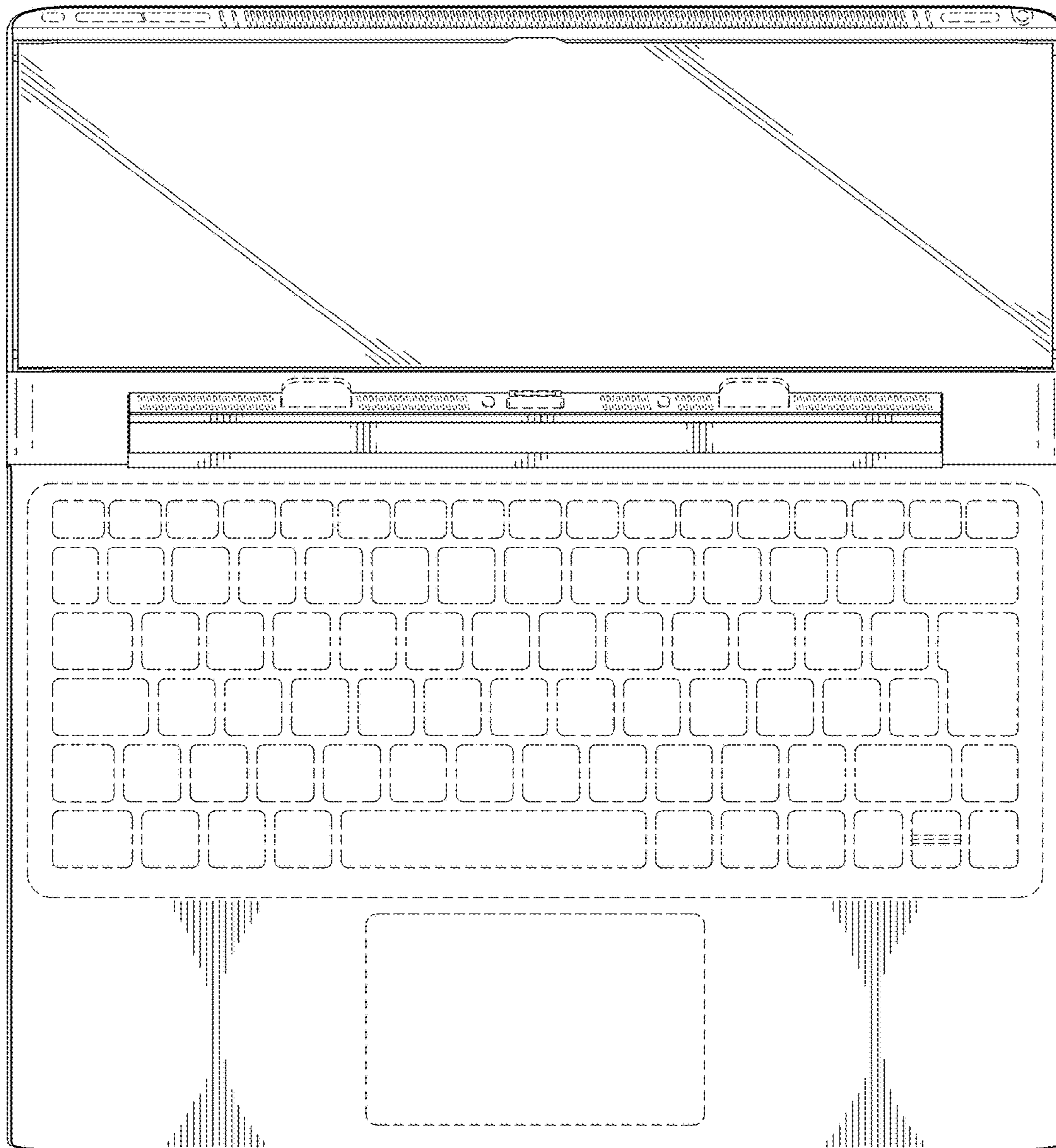


FIG. 7

