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(12) **United States Design Patent** (10) **Patent No.:** **US D803,707 S**
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(54) **ELECTRONICS ENCLOSURE FOR HOLSTER**

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(**) Term: **15 Years**

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(52) **U.S. Cl.**
USPC **D10/70; D3/222**

(58) **Field of Classification Search**
USPC D14/356-358, 426-428, 432, 440, 447, D14/496, 250, 251-253; D3/201, 215, D3/217, 218, 221-223, 228, 273; D10/46, 47, 65, 70, 75, 80, 104.1, 106.2, D10/106.3, 106.4, 106.6, 106.9, 114.9, D10/121; D22/108, 117, 199; D13/184, D13/199
CPC F41C 33/00602; F41C 33/0209; F41C 33/0263; F41C 33/029; F41C 33/04; F41C 33/041; F41C 33/06; F41A 17/06; F41A 17/063
See application file for complete search history.

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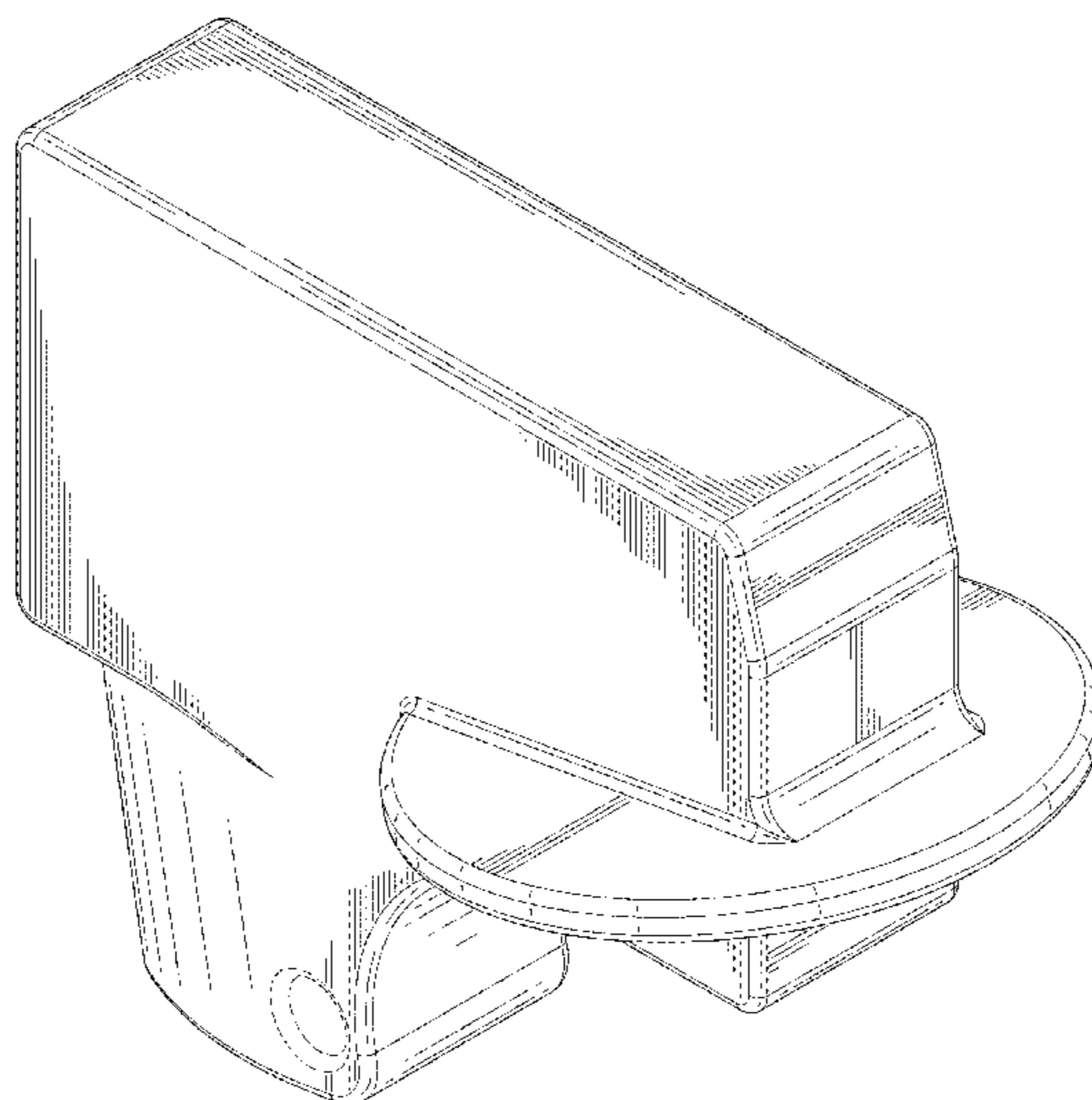
(57) **CLAIM**

The ornamental design for an electronics enclosure for holster, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of the electronics enclosure for holster of the present invention;
FIG. 2 is a front view of the electronics enclosure for holster of the present invention;
FIG. 3 is a rear view of the electronics enclosure for holster of the present invention;
FIG. 4 is a right side view of the electronics enclosure for holster of the present invention;
FIG. 5 is a left side view of the electronics enclosure for holster of the present invention;
FIG. 6 is a top view of the electronics enclosure for holster of the present invention;
FIG. 7 is a bottom view of the electronics enclosure for holster of the present invention; and,
FIG. 8 is a rear perspective view of an embodiment of the electronics enclosure for holster of the present invention. The broken lines depicting a data port in FIGS. 3 and 8, and screw holes in FIGS. 1, 4, 5, and 8, depict structural portions of the electronics enclosure for holster that form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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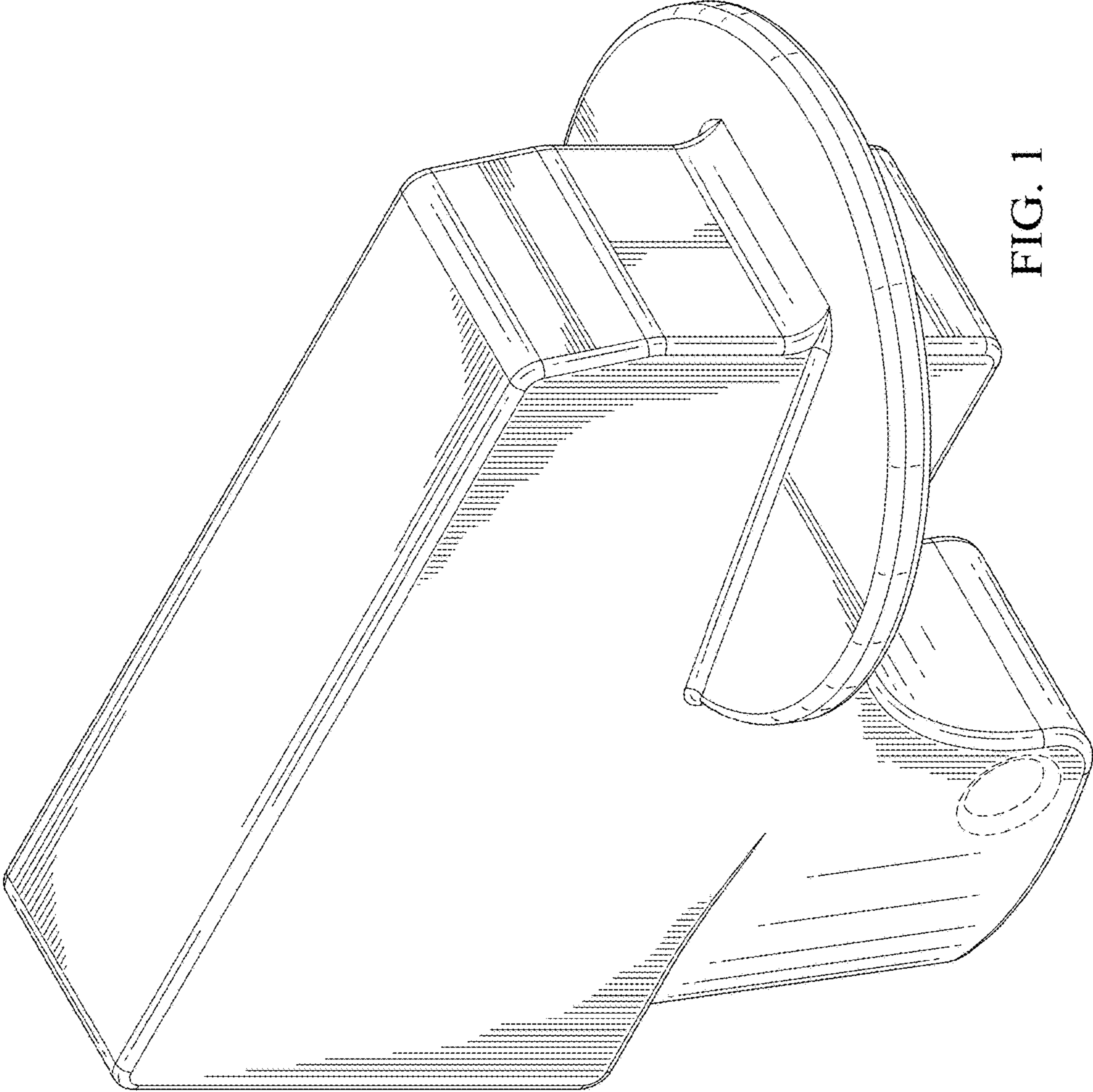


FIG. 1

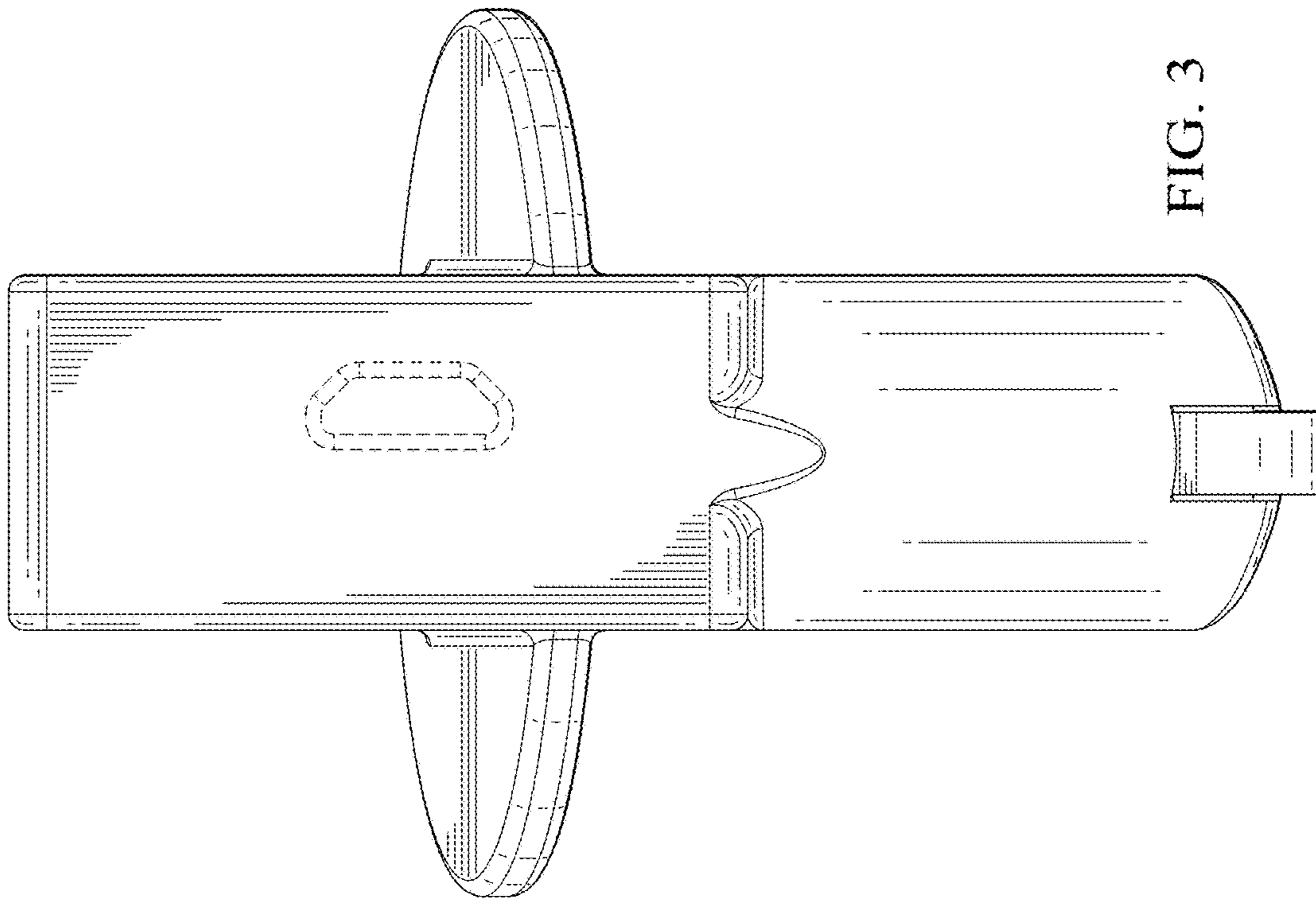


FIG. 3

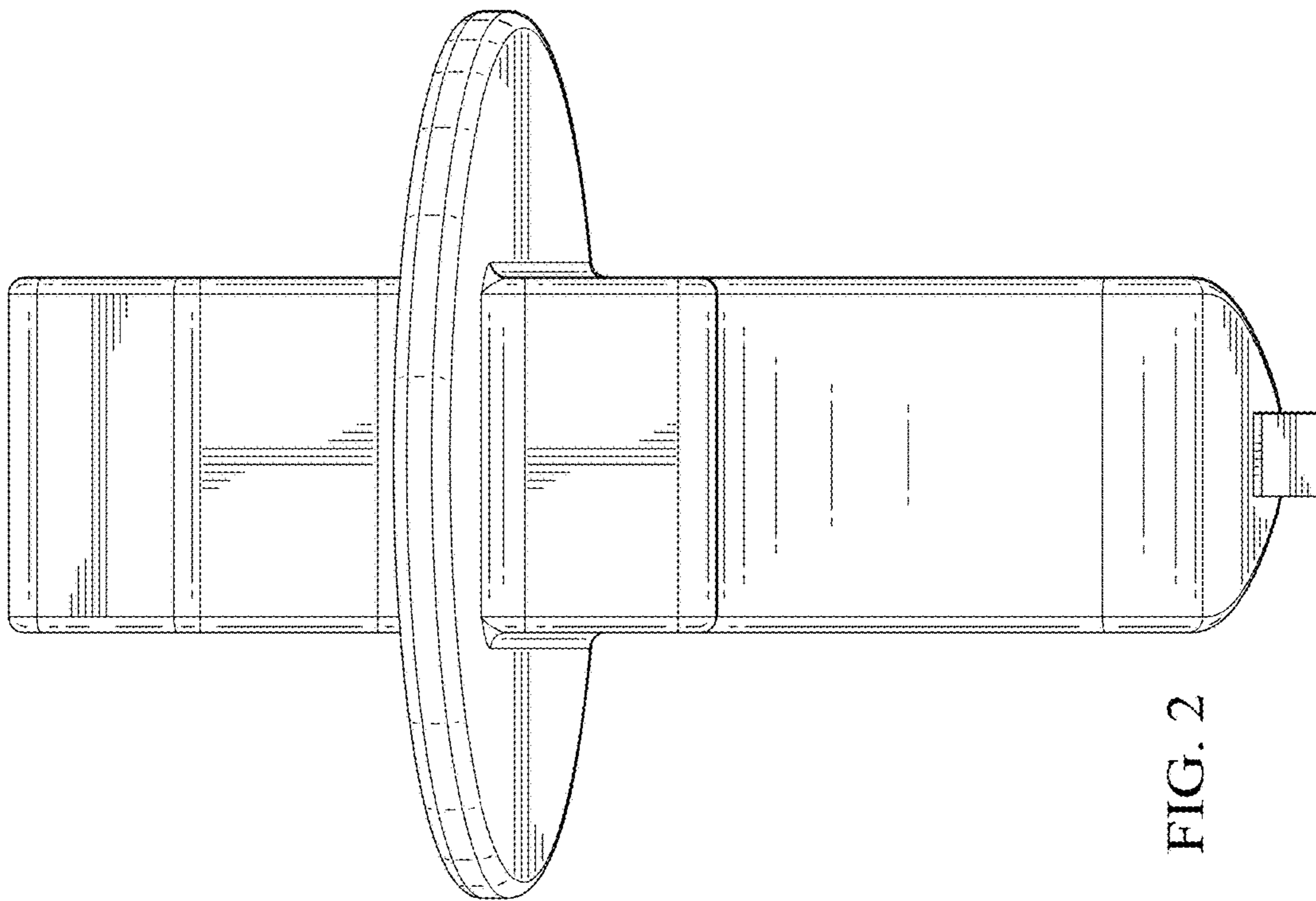


FIG. 2

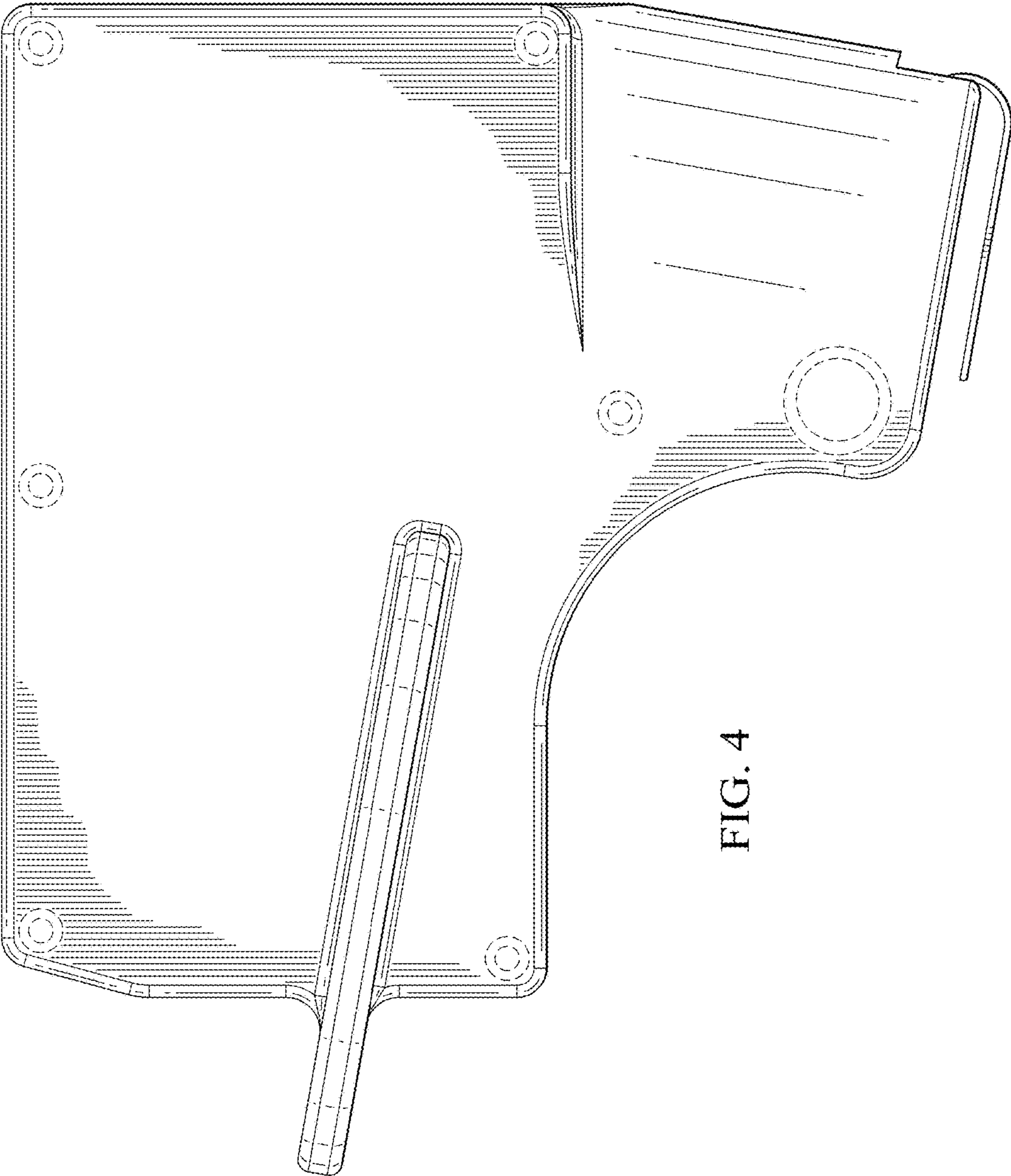


FIG. 4

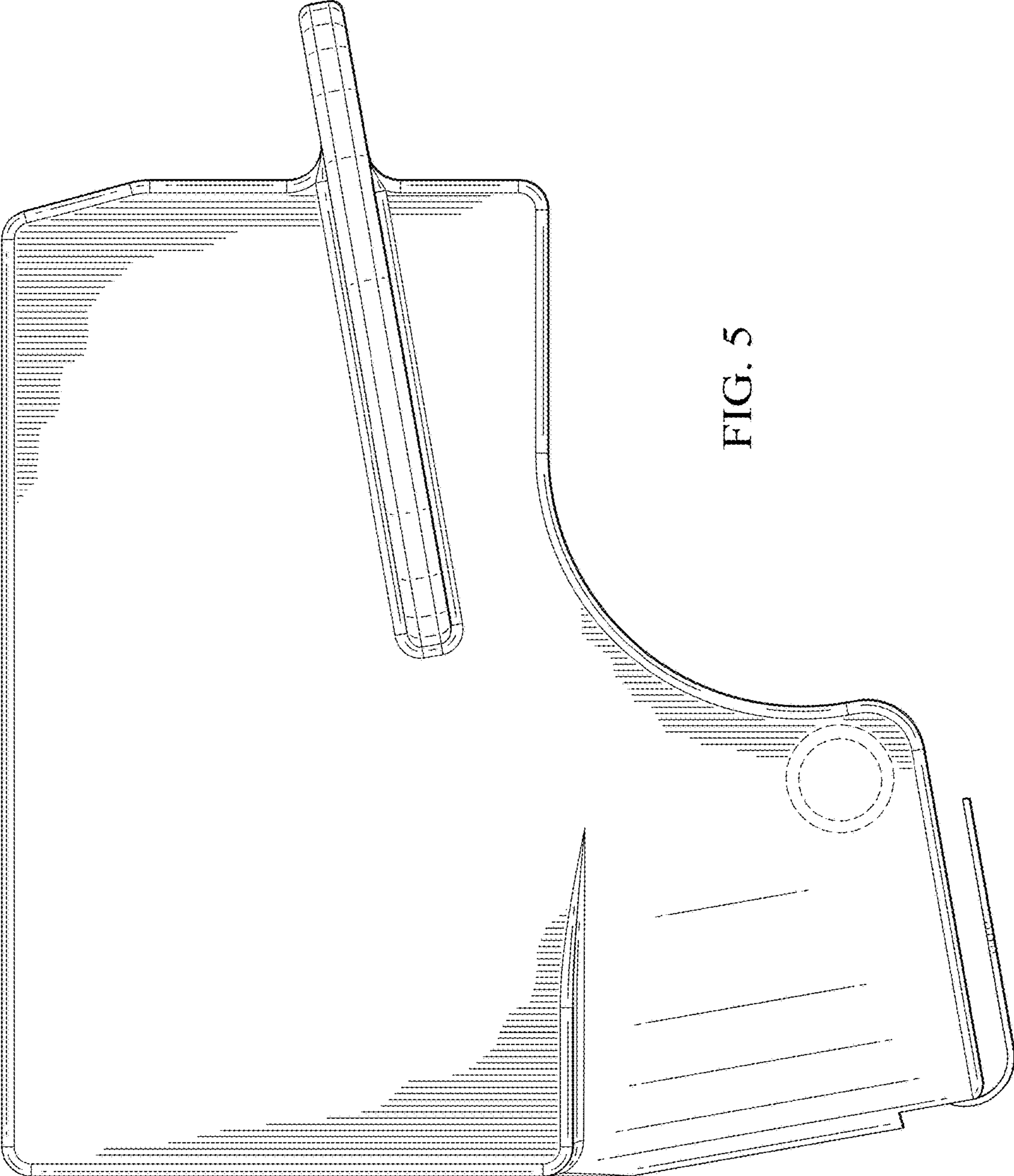


FIG. 5

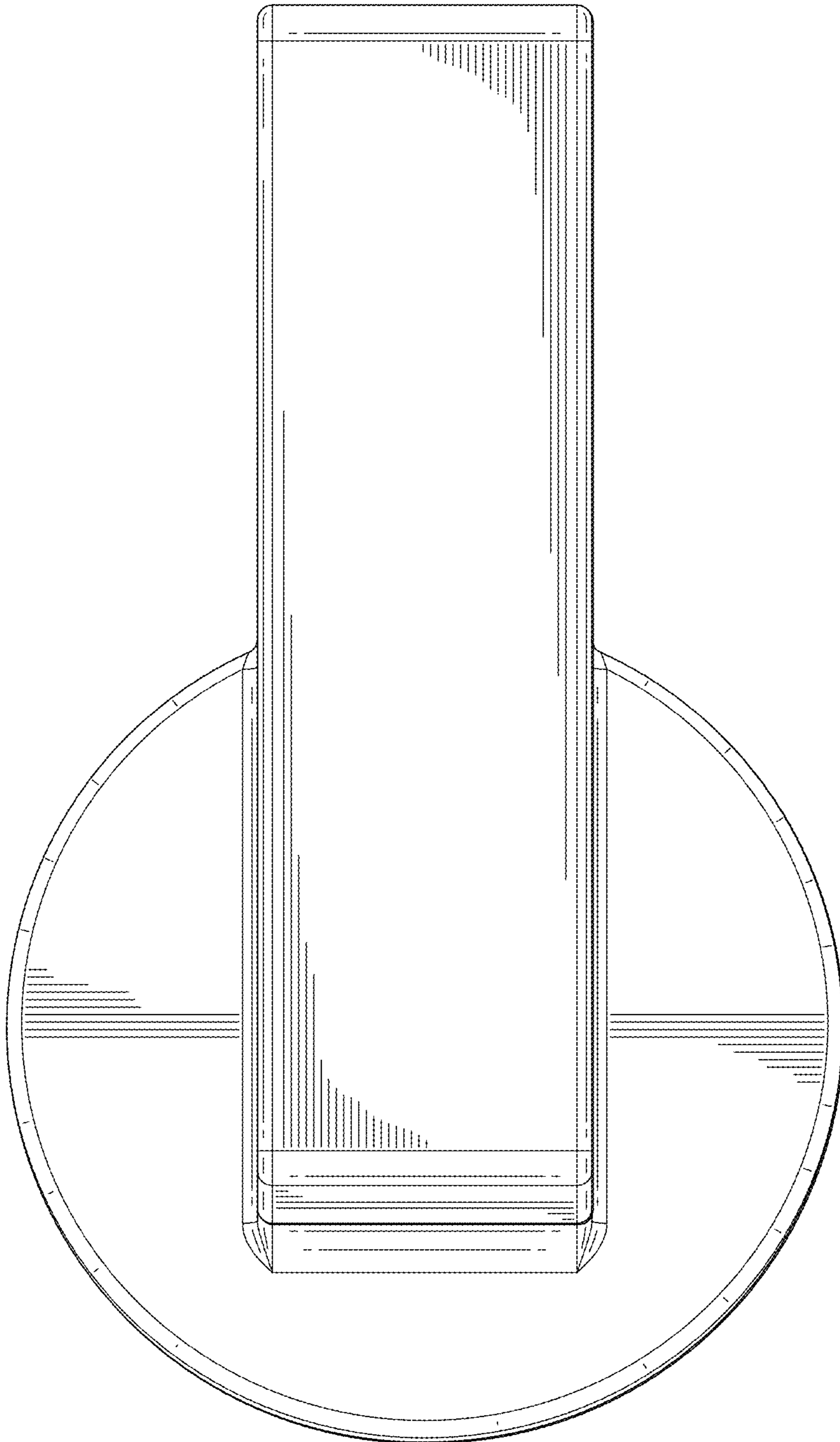


FIG. 6

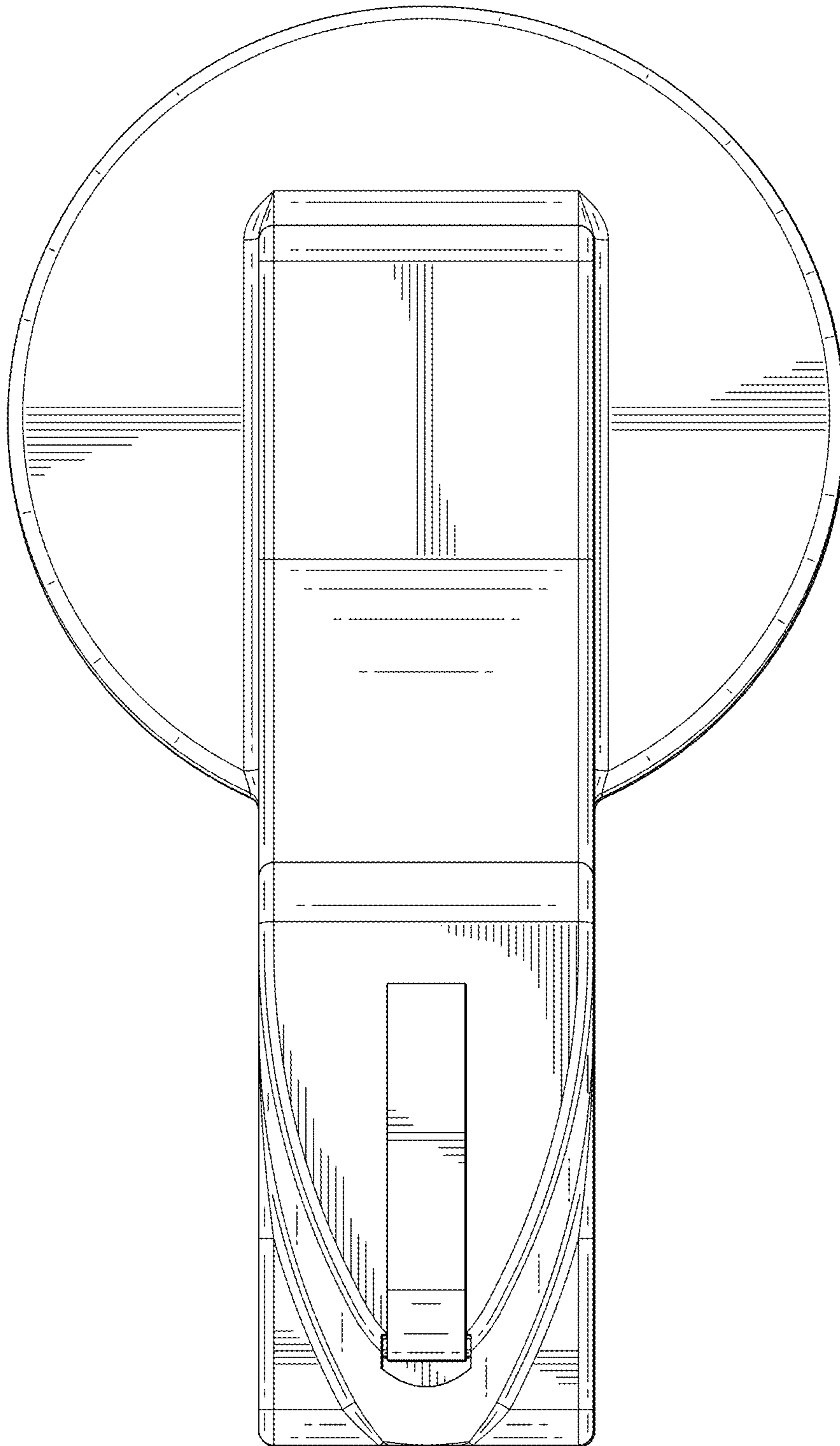


FIG. 7

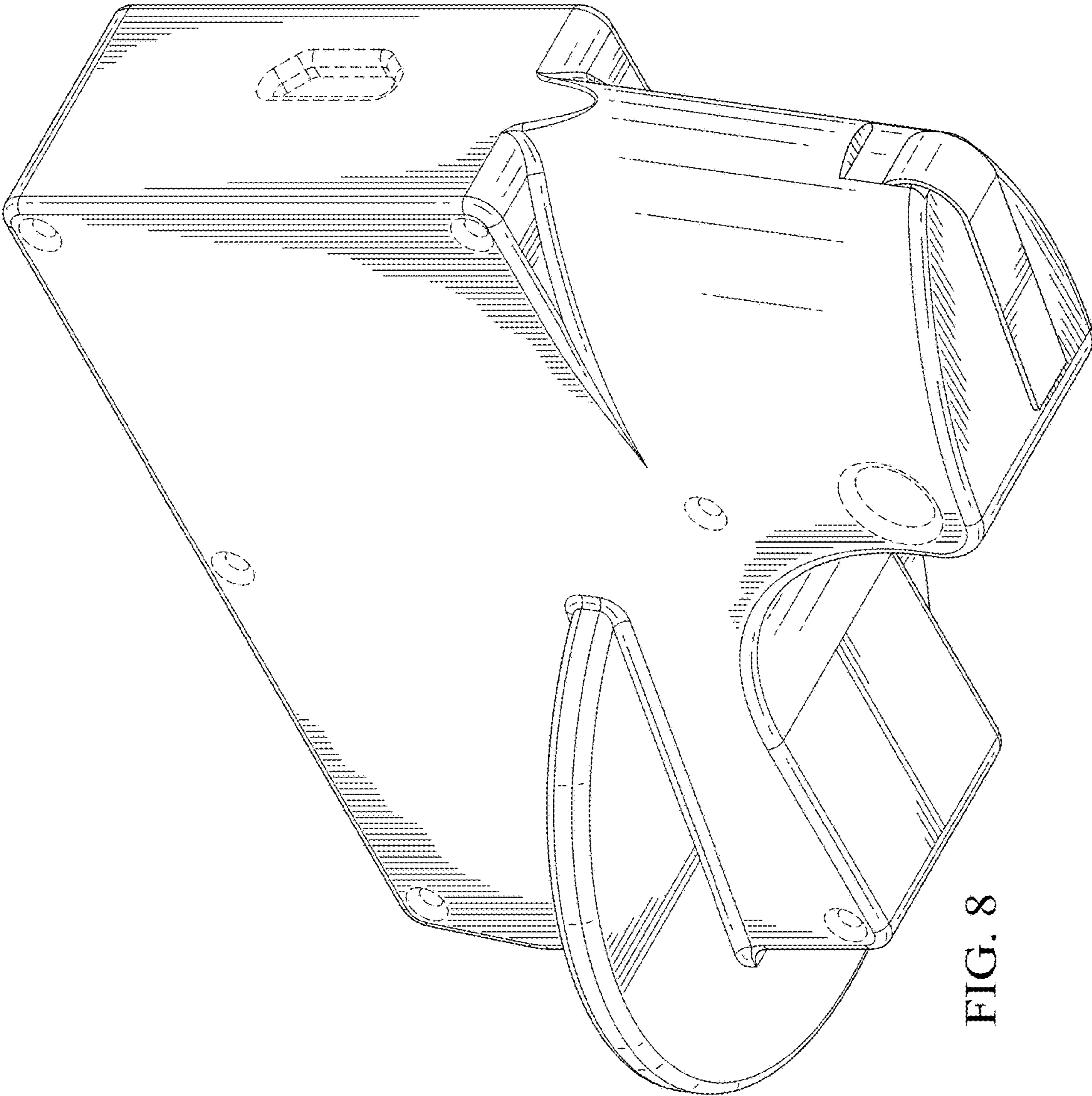


FIG. 8