



US00D801963S

(12) **United States Design Patent** (10) **Patent No.:** **US D801,963 S**
Probst et al. (45) **Date of Patent:** **** Nov. 7, 2017**

(54) **INPUT/OUTPUT DEVICE**

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 (73) Assignee: **Dell Products L.P.**, Round Rock, TX (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/575,450**

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(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/314; D14/356**

(58) **Field of Classification Search**
 USPC D14/240, 300, 301, 308, 313, 314,
 D14/348–356, 364–368, 402, 188;
 D13/123, 158, 162, 184, 199;
 312/223.1–223.3, 351.11;
 360/99.01–99.12; 369/34.01–36.01;
 361/679.31–679.33, 679.37, 679.39,
 361/679.46, 679.53, 679.4, 679.41, 727
 CPC . G06F 1/20; G06F 1/181; G06F 1/187; G06F
 1/183; G06F 1/184; G06F 1/16; H05K
 7/16; H05K 7/1424; H05K 7/1409; H05K
 7/20; H05K 7/20545; H05K 7/20727;
 H05K 7/1425; H05K 7/1488; H05K
 7/183; H05K 7/14; H05K 5/00; H05K
 7/00; H05H 7/20; G11B 33/126; G11B
 33/128; G11B 33/08; G11B 33/02

See application file for complete search history.

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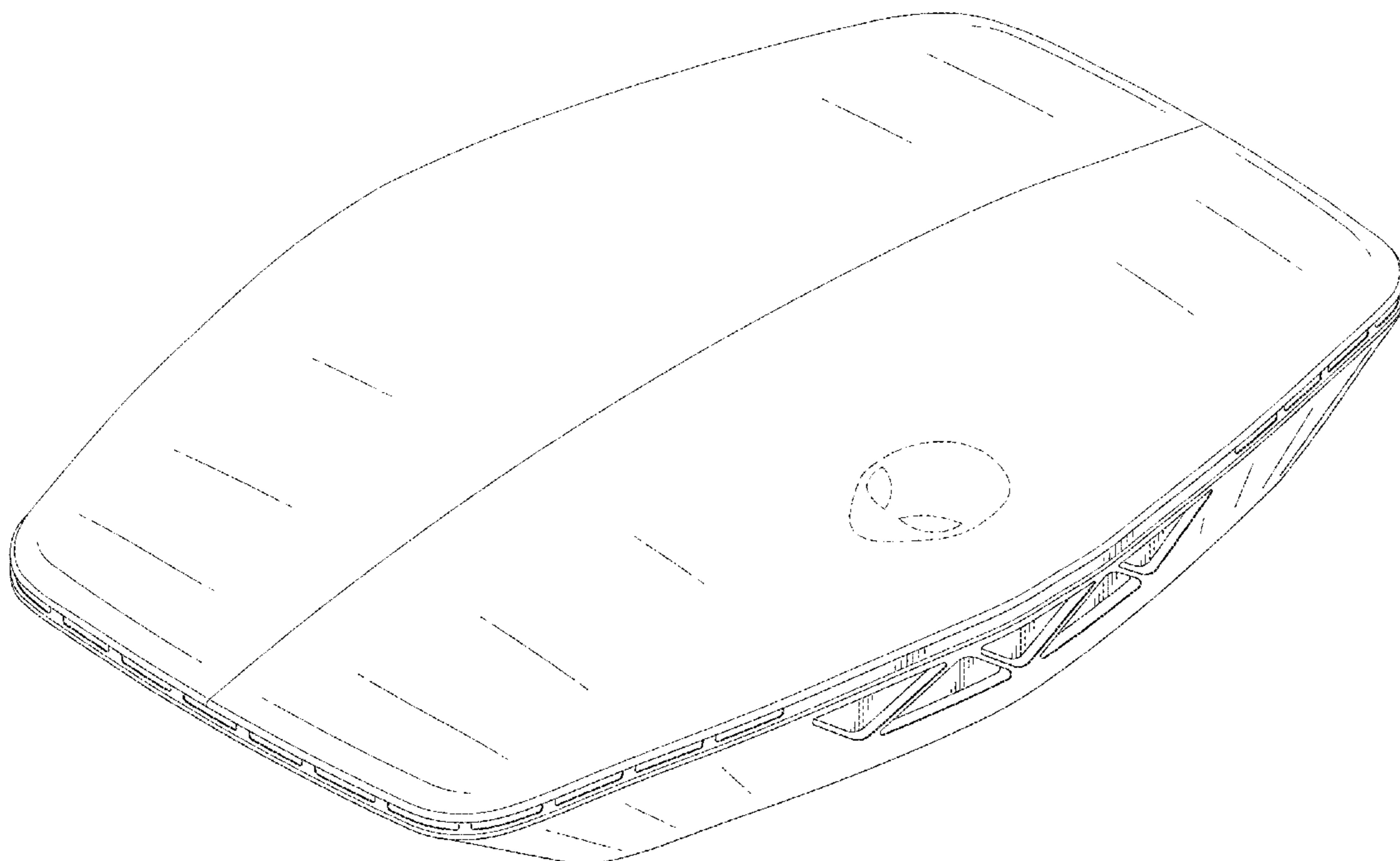
(57) **CLAIM**

We claim the ornamental design for the input/output device, as shown and described.

DESCRIPTION

FIG. 1 is a front right perspective view of the input/output device comprising the present invention;
 FIG. 2 is a front view thereof;
 FIG. 3 is a rear view thereof;
 FIG. 4 is a right side view thereof;
 FIG. 5 is a left side view thereof;
 FIG. 6 is a top view thereof; and,
 FIG. 7 is a bottom view thereof.
 Broken lines shown in the drawings illustrate portions of the input/output device, and form no part of the claimed design.

1 Claim, 5 Drawing Sheets



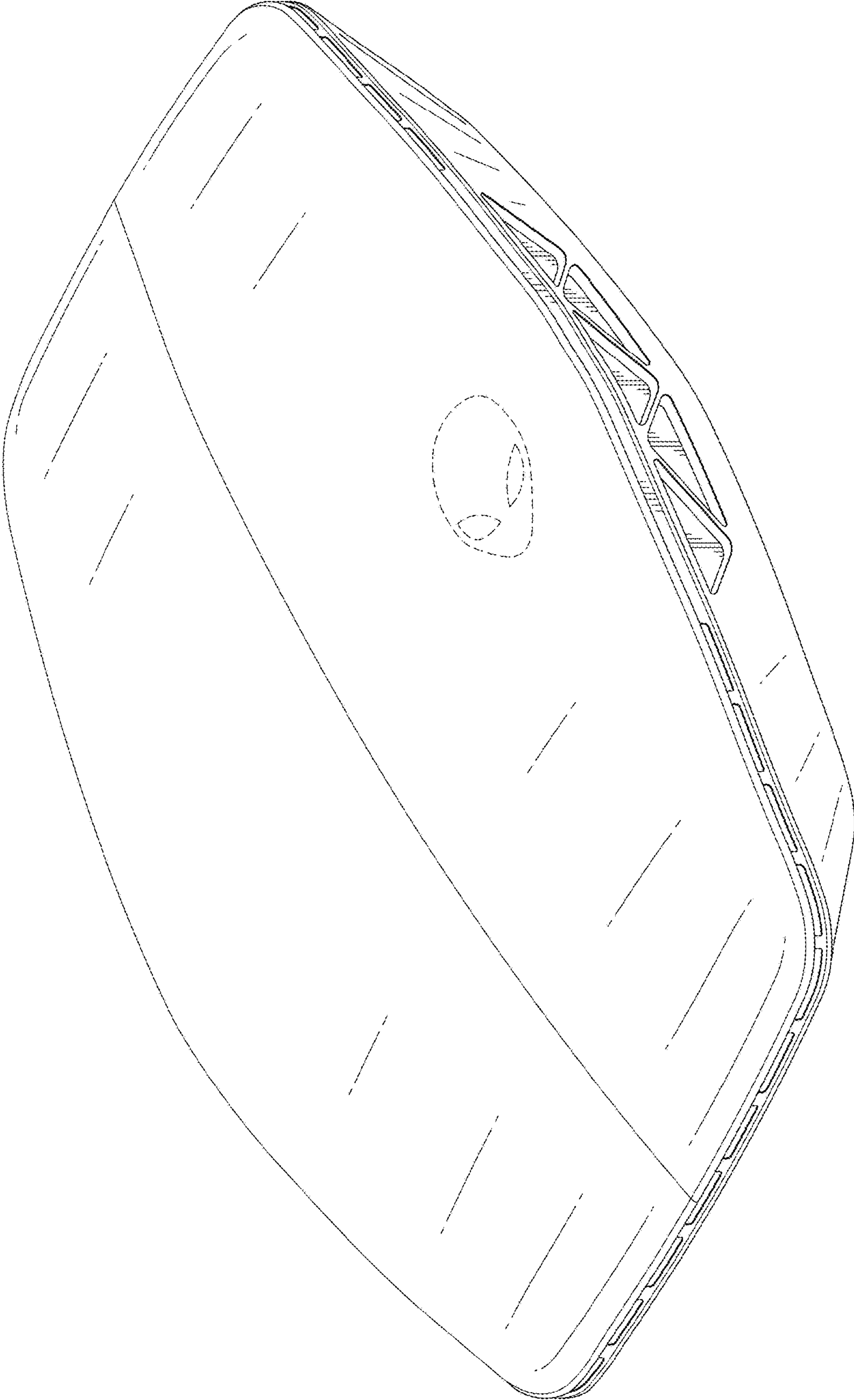


FIG. 1

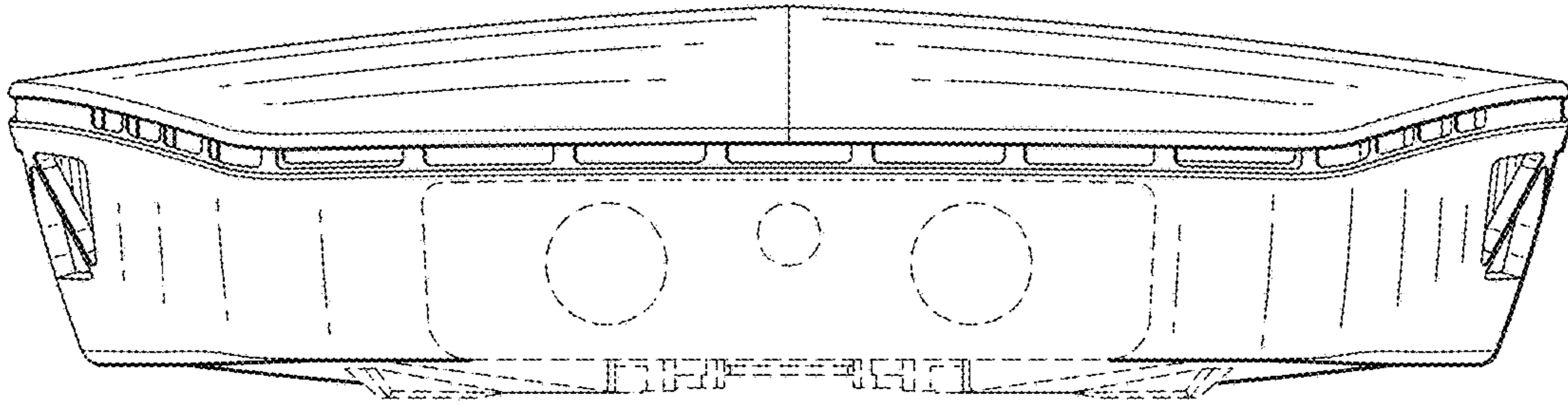


FIG. 2

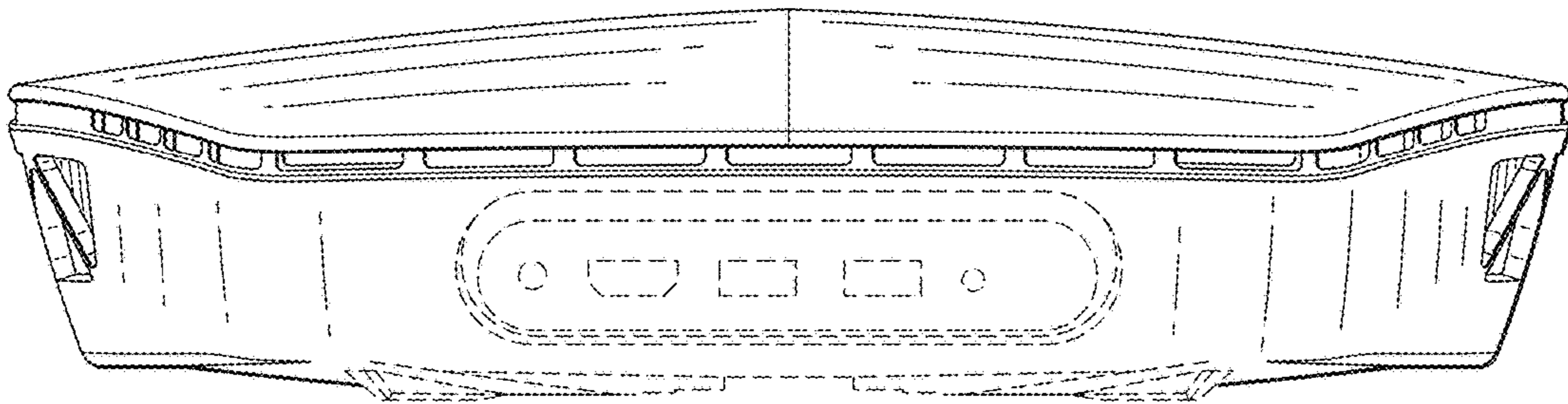


FIG. 3

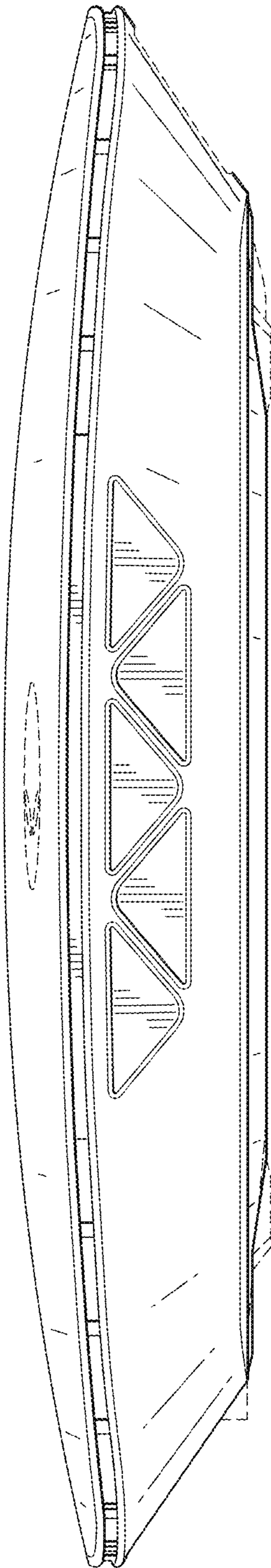


FIG. 4

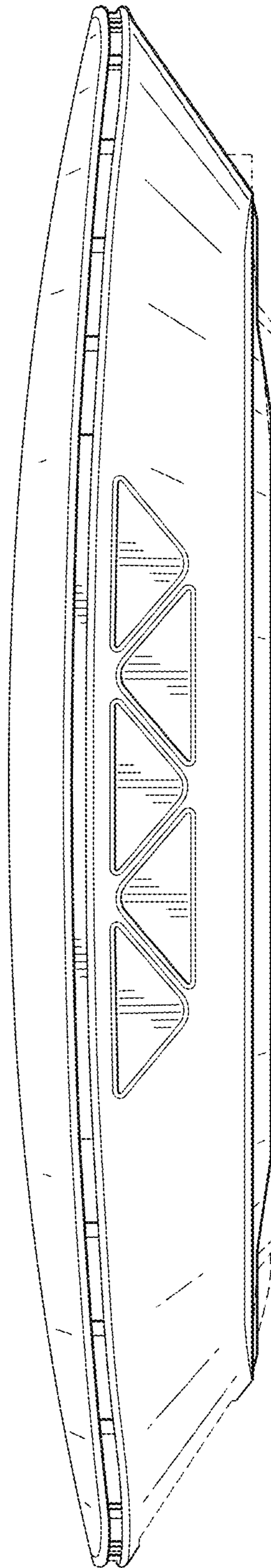


FIG. 5

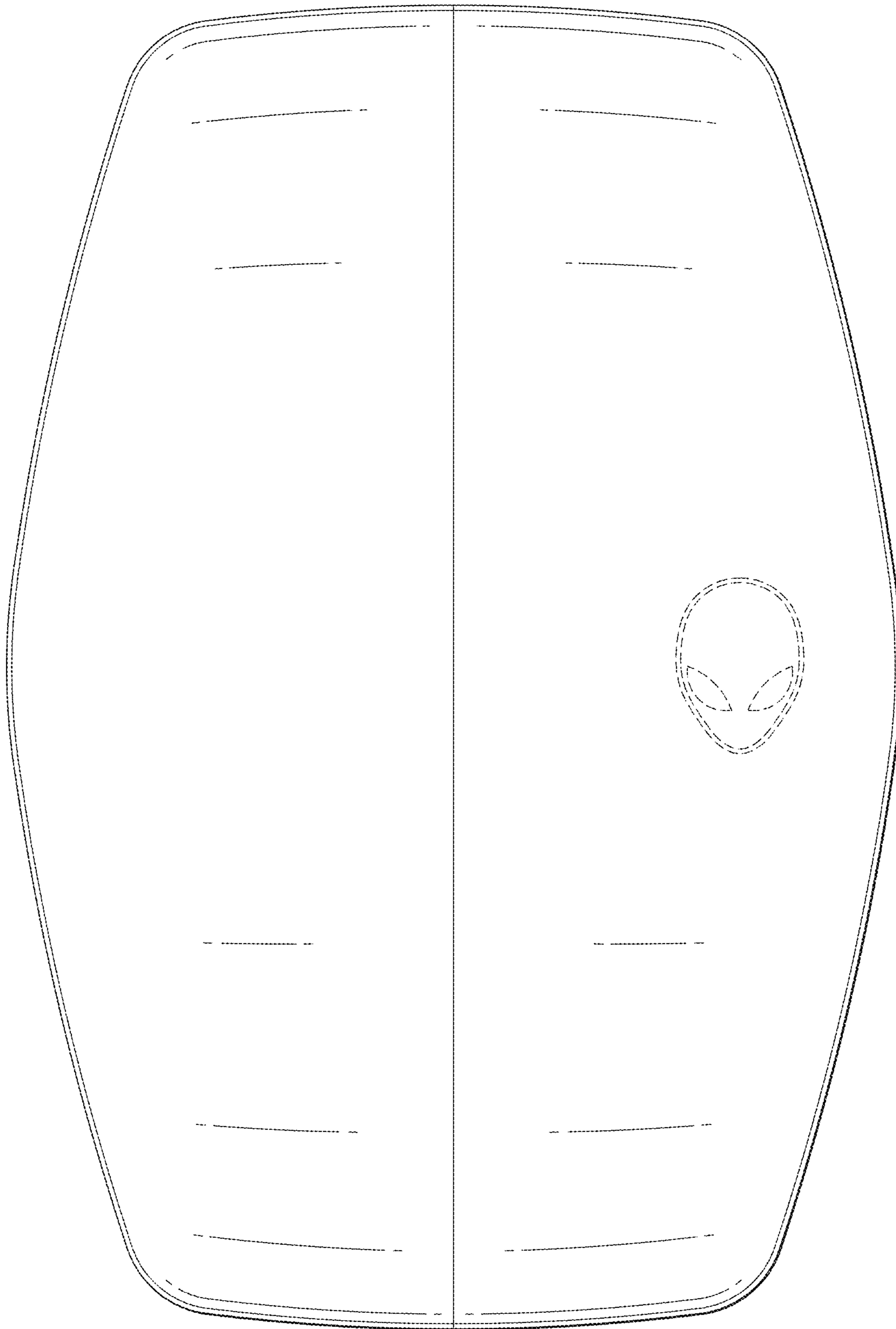


FIG. 6

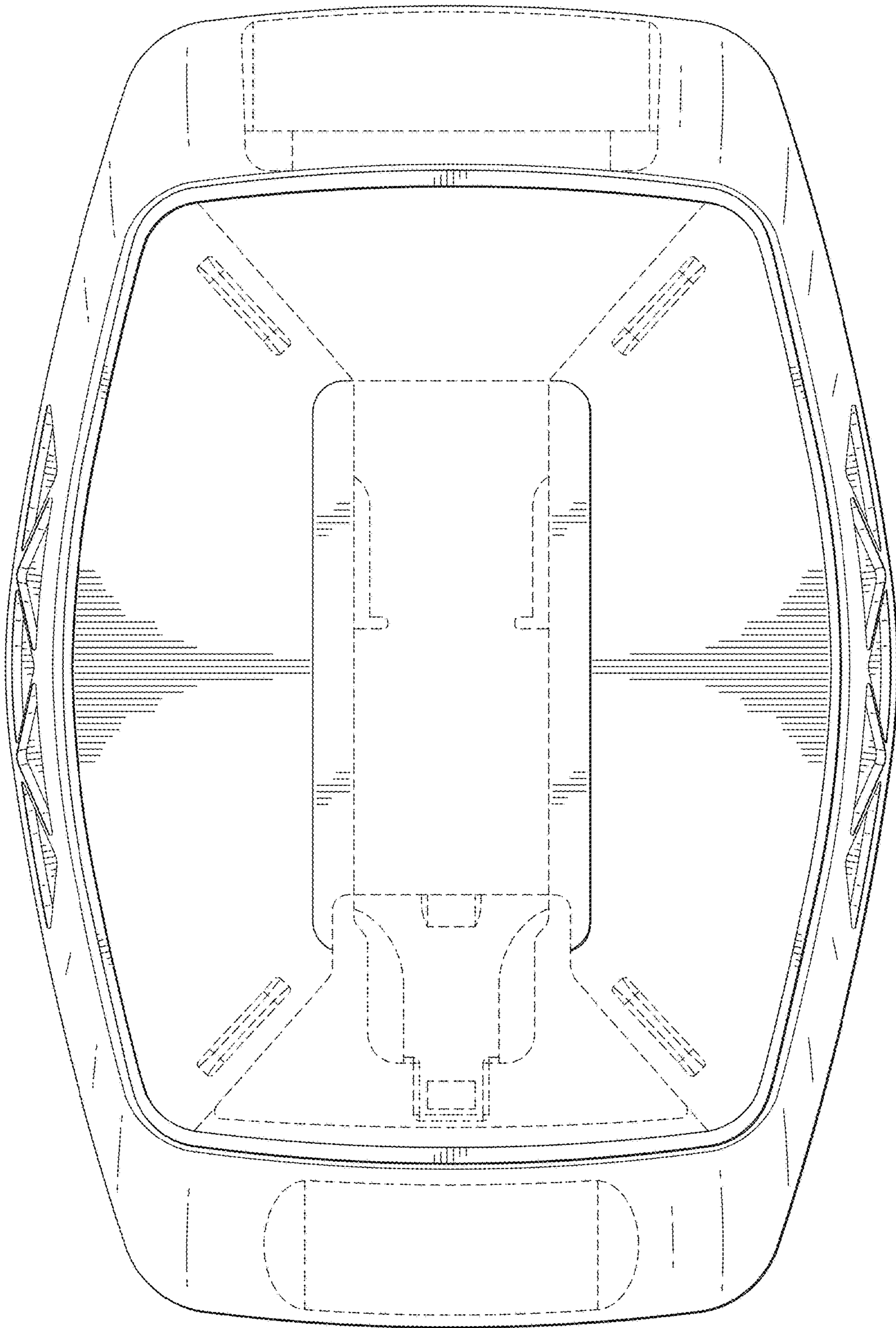


FIG. 7