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(12) **United States Design Patent** (10) **Patent No.:** **US D800,132 S**
Maus et al. (45) **Date of Patent:** **** Oct. 17, 2017**

(54) **UNIVERSAL SERIAL BUS DATA KEY**

D689,871 S * 9/2013 Anderson D14/480.1
D758,371 S * 6/2016 Daniel D14/435.1
2013/0190047 A1* 7/2013 Roberts, Jr. H05K 5/0278
455/557

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(**) Term: **14 Years**

(21) Appl. No.: **29/514,527**

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(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/435.1**

(58) **Field of Classification Search**
USPC D14/435.1, 480.1-480.7, 411, 433, 436, D14/484.1, 137, 155, 203.1-203.8, 240, D14/242; D13/133, 154
CPC H05K 5/0278; G06K 19/07732
See application file for complete search history.

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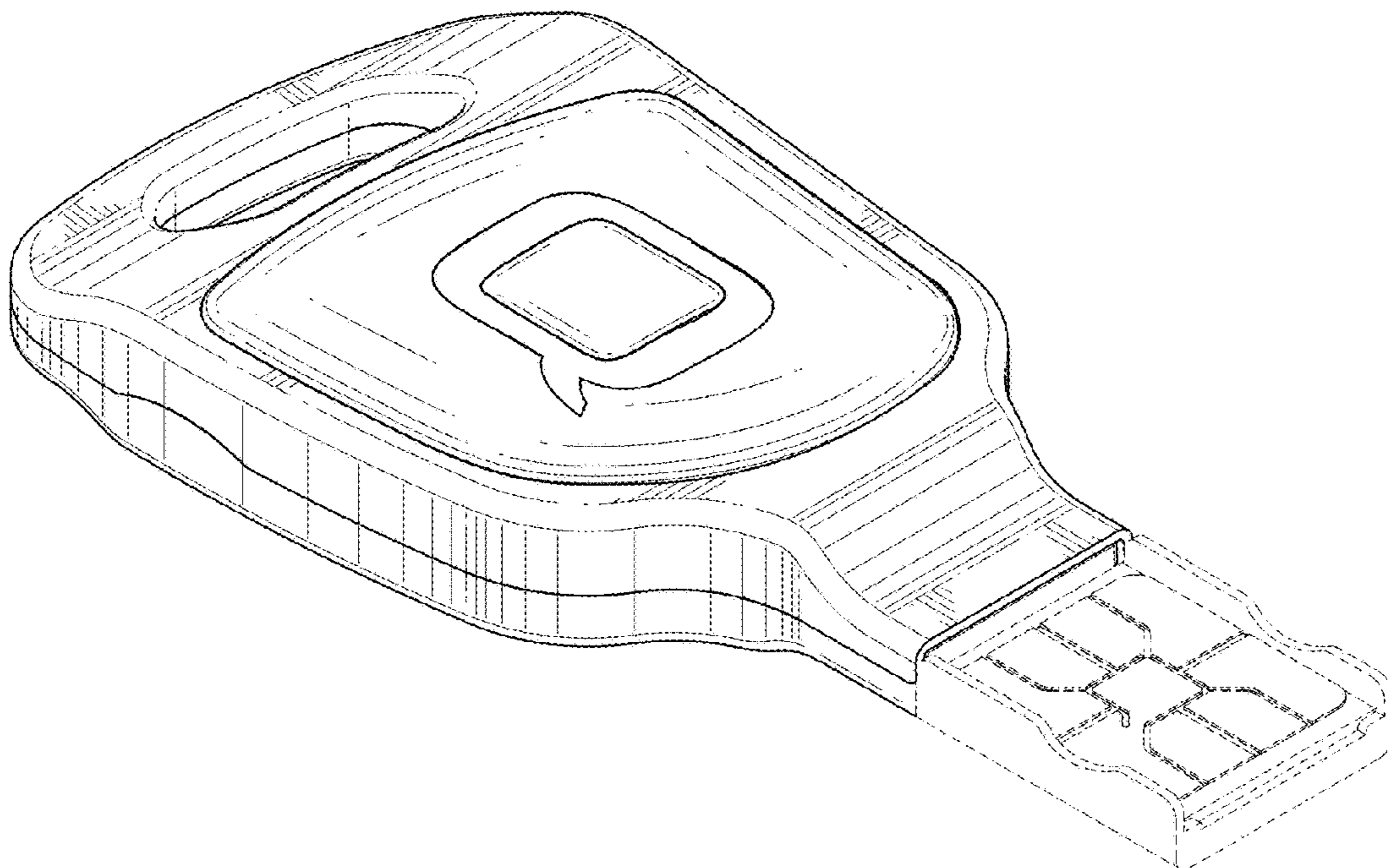
(57) **CLAIM**

The ornamental design for a universal serial bus data key, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a universal serial bus data key.
FIG. 2 is a front view of the universal serial bus data key.
FIG. 3 is a rear view of the universal serial bus data key.
FIG. 4 is a right side view of the universal serial bus data key.
FIG. 5 is a left side of the universal serial bus data key.
FIG. 6 is a top view of the universal serial bus data key; and,
FIG. 7 is a bottom view of the universal serial bus data key.

1 Claim, 5 Drawing Sheets



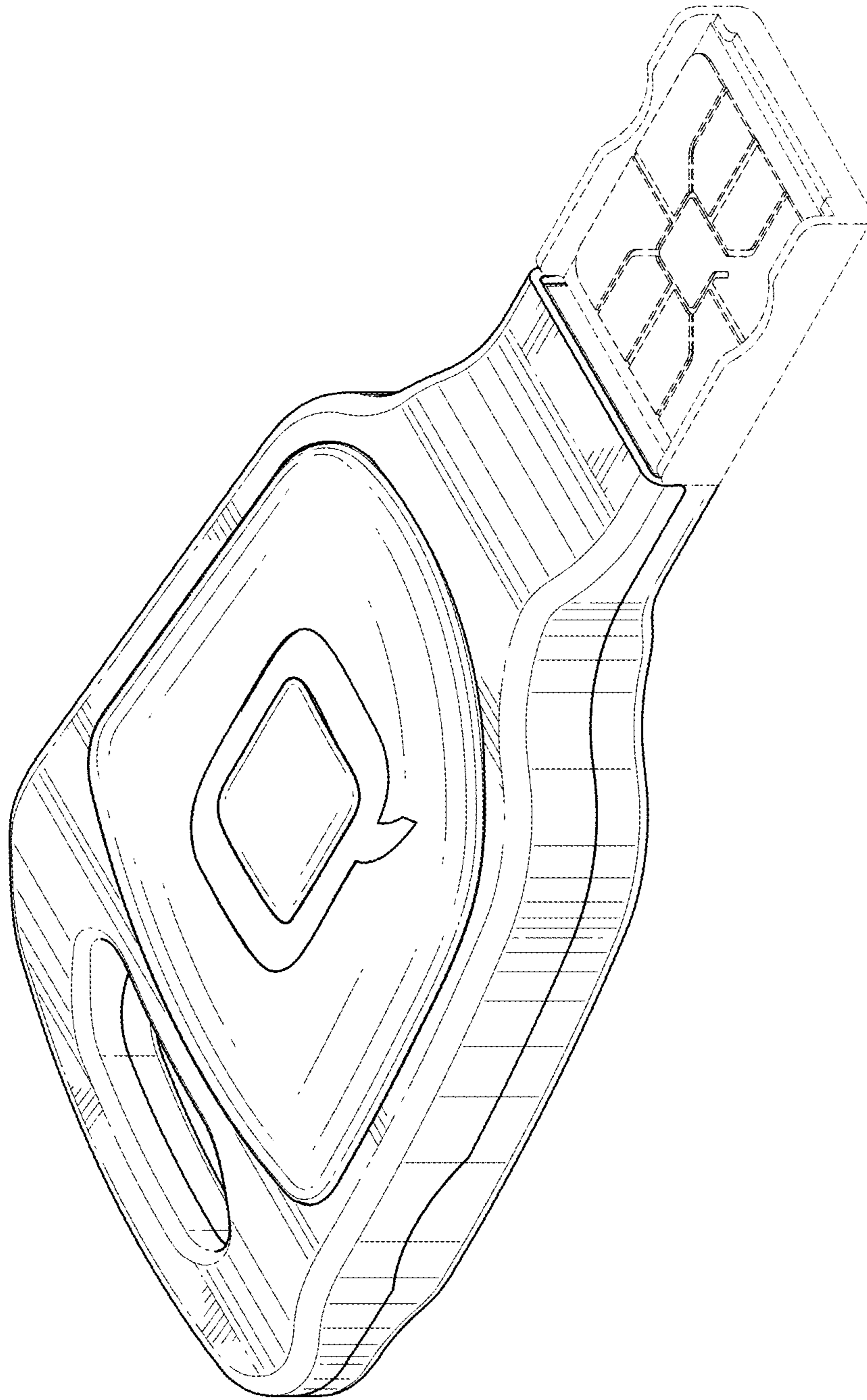


FIG. 1

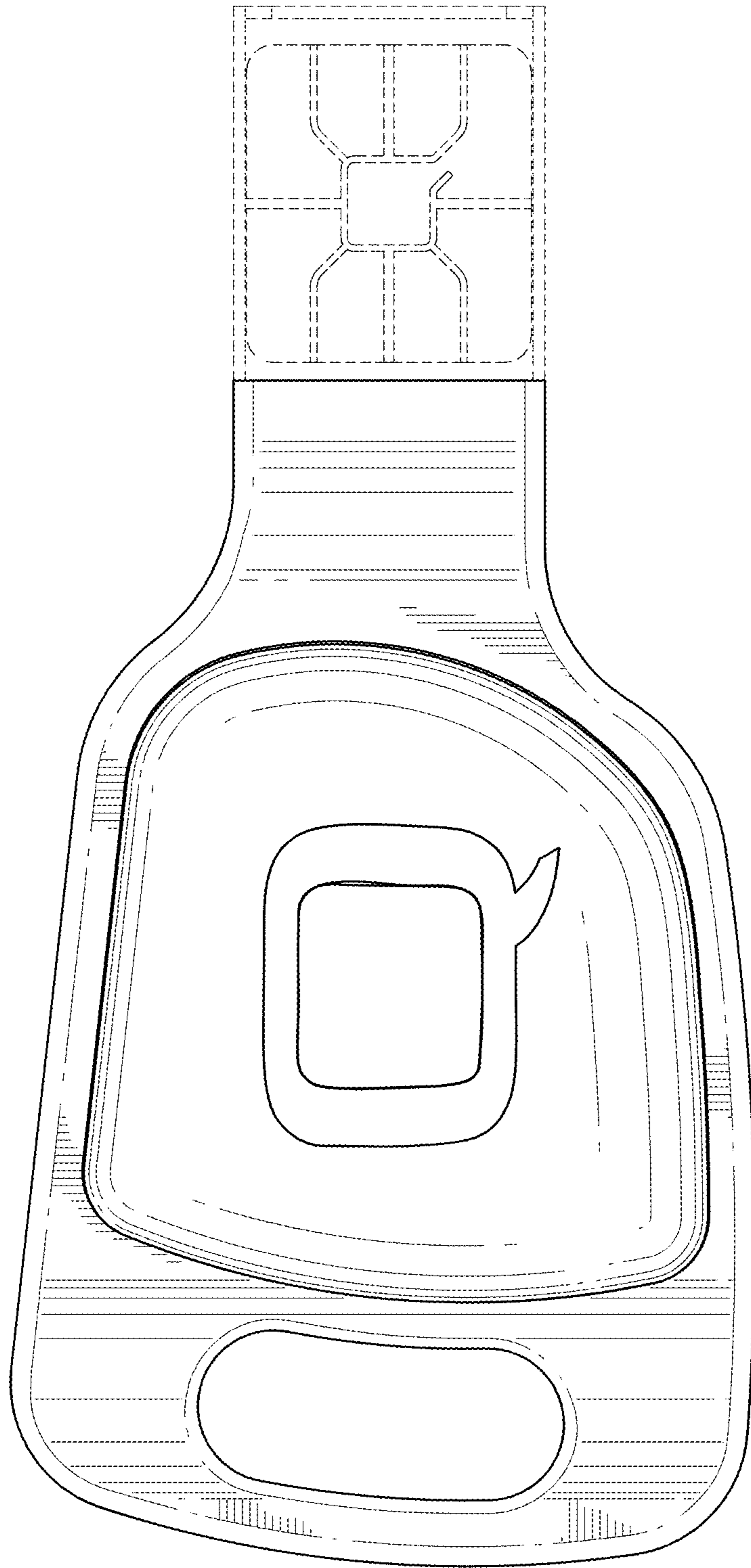


FIG. 2

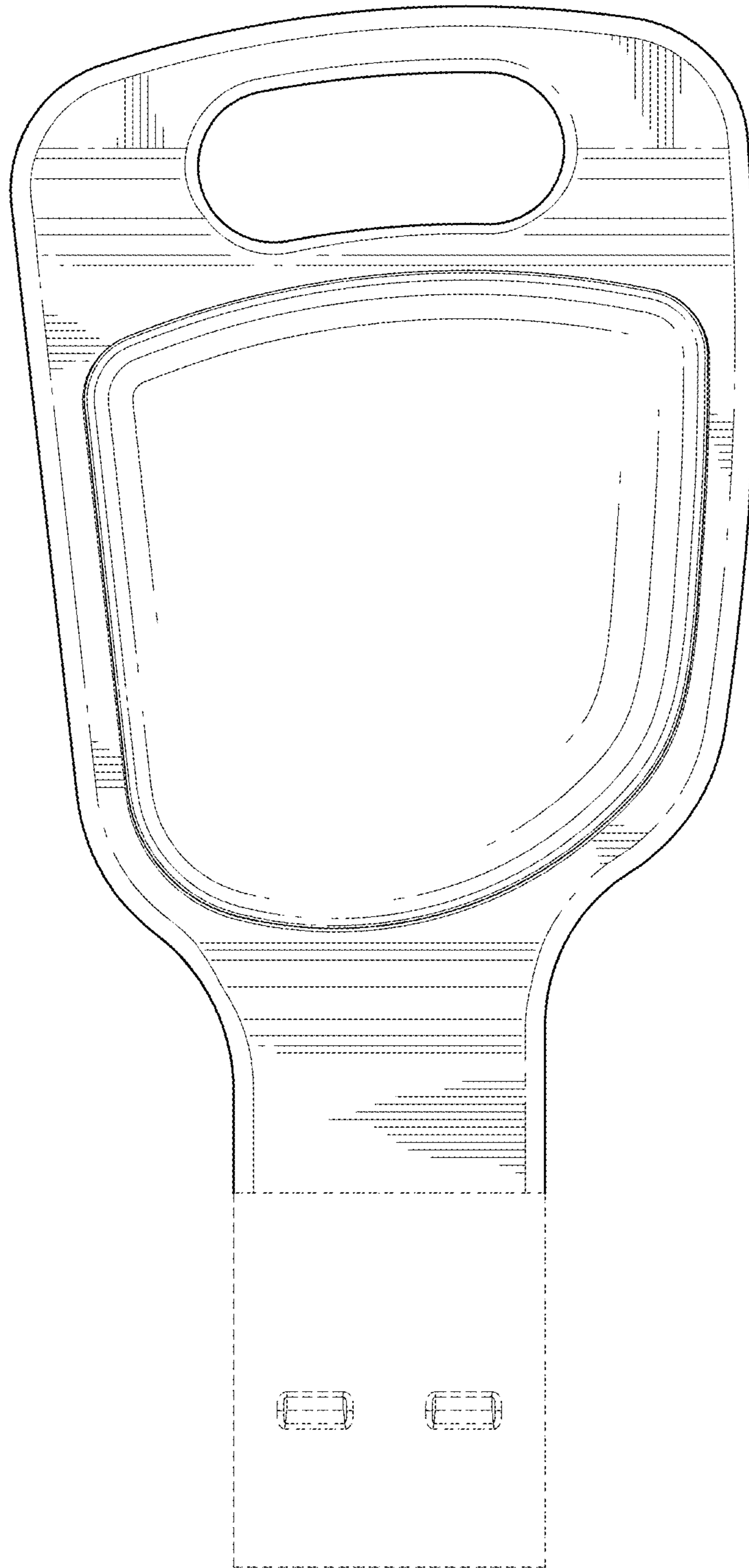


FIG. 3

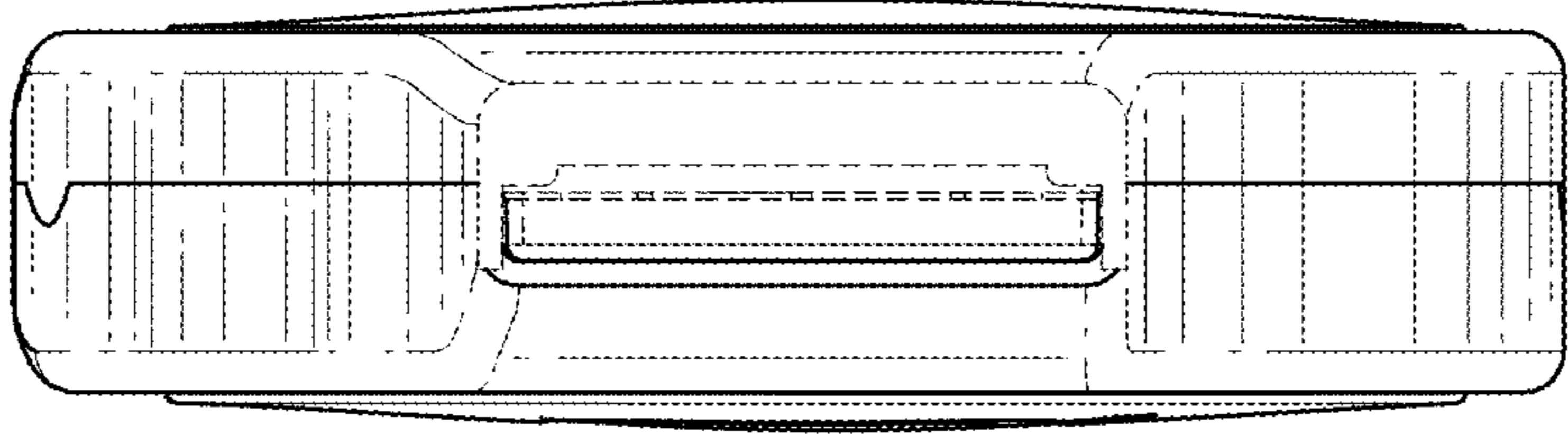


FIG. 5

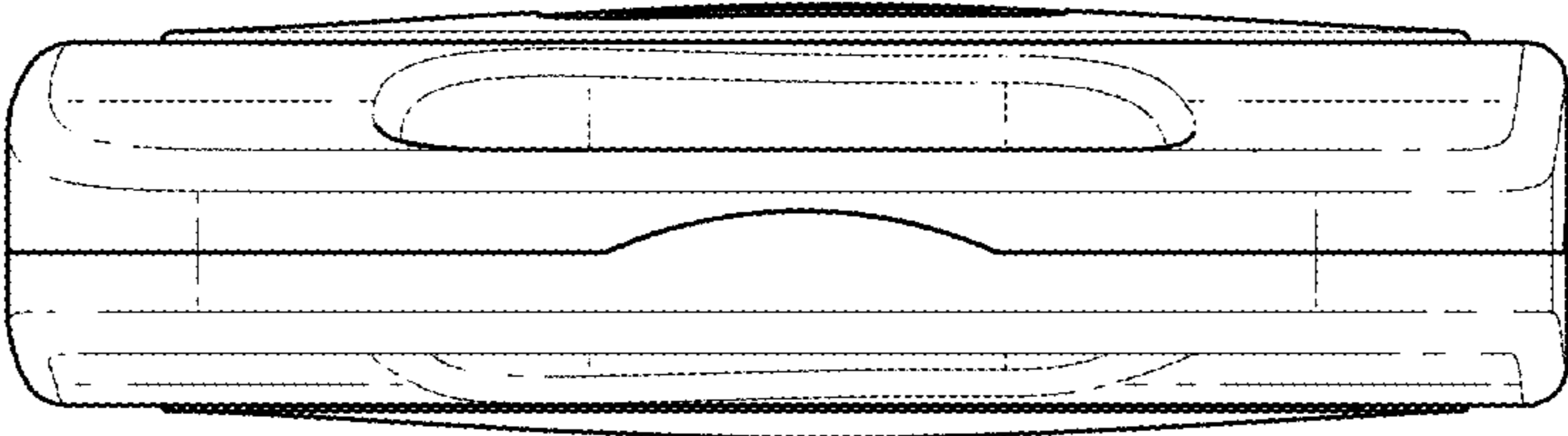


FIG. 4

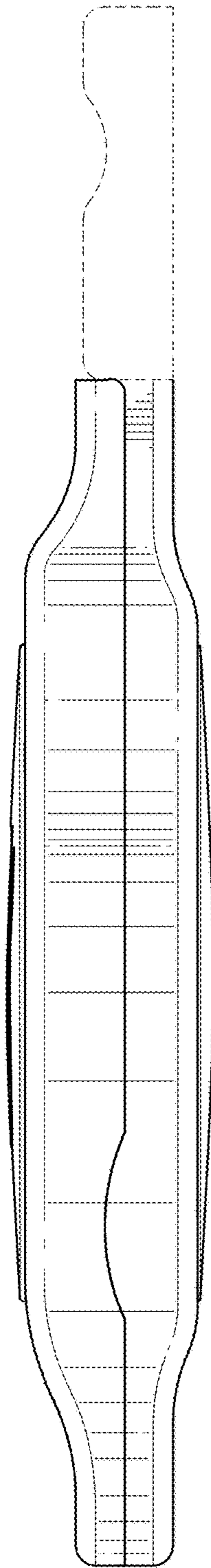


FIG. 6

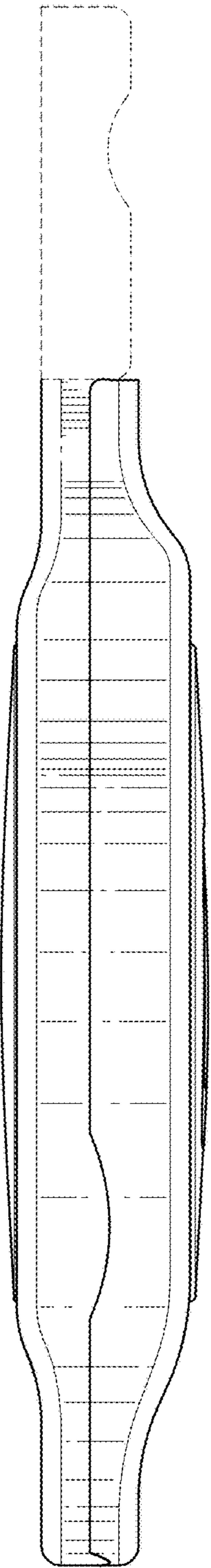


FIG. 7