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(12) **United States Design Patent**
Takahashi et al.

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(54) **CIRCUIT BOARD**

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(30) **Foreign Application Priority Data**

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(52) **U.S. Cl.**
USPC **D13/182**

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318/567, 568.1; 361/600, 601, 718, 719,
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361/761, 807; 439/55, 65, 68, 69, 76.1,
439/92, 93, 95

CPC H05K 3/00; H05K 3/30; H05K 3/301;
H05K 3/303; H05K 3/34; H05K 3/3405;
H05K 3/341; H05K 3/36; H05K 3/361;
H05K 3/363; H05K 3/40; H05K 7/14;
H05K 7/1422; H05K 7/00; H05K 1/18;
H05K 1/02; H05K 1/181; H05K 1/182;
H05K 1/183; H05K 1/184; H05K 1/189;
H05K 1/00

See application file for complete search history.

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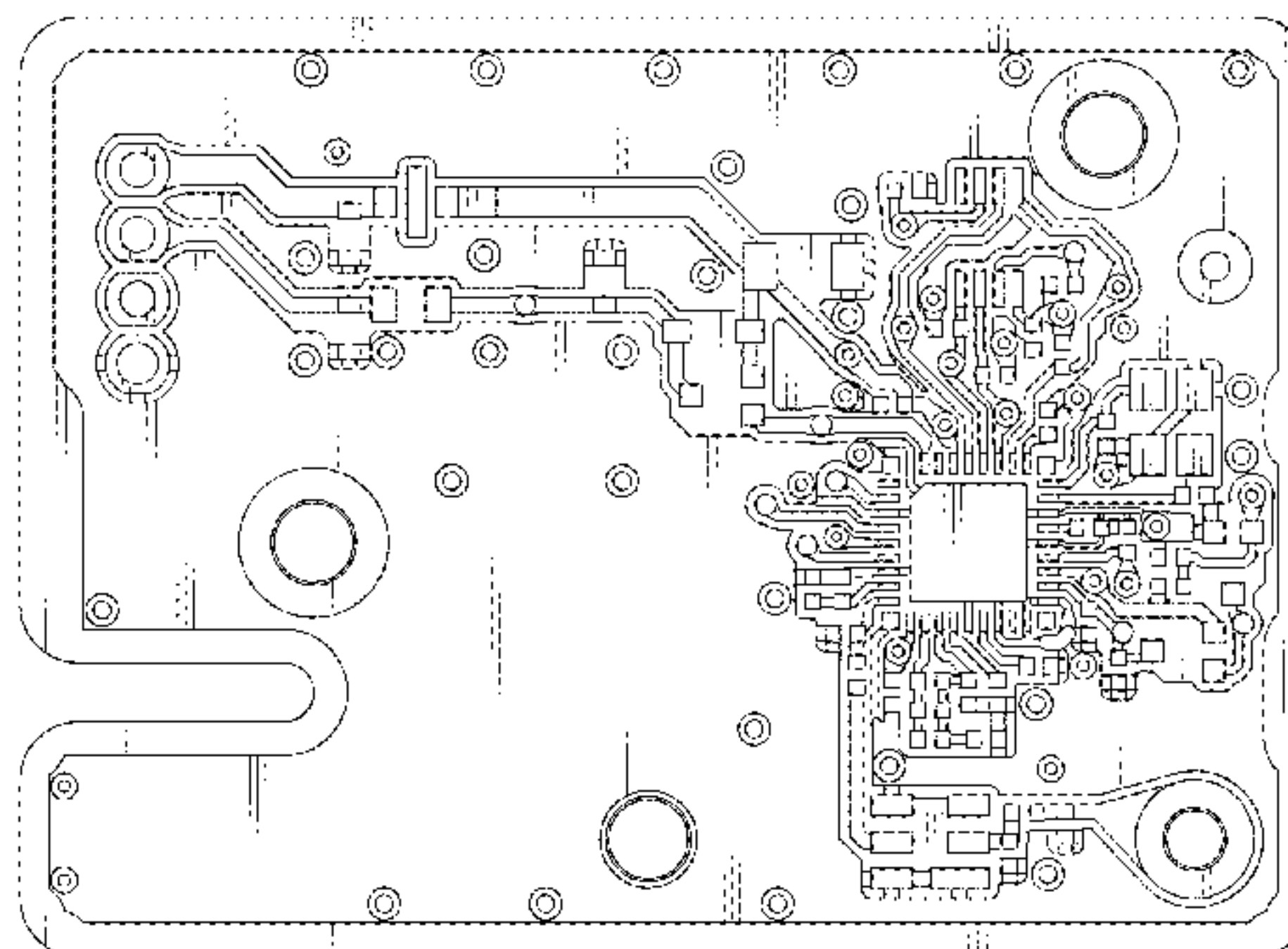
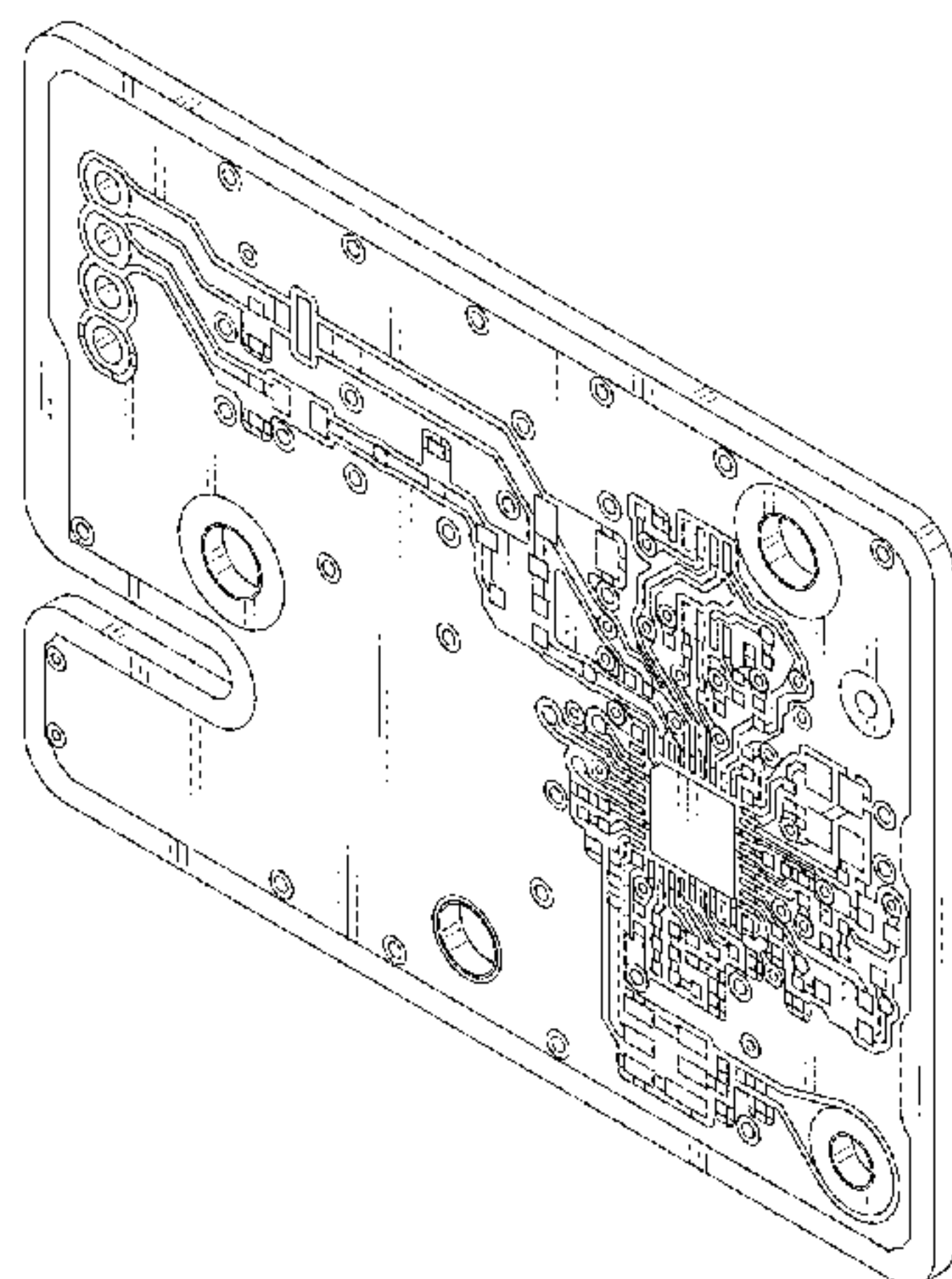
(57) **CLAIM**

The ornamental design for a circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a front, right, top perspective view of a circuit board showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a left side view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a top view thereof; and,
FIG. 7 is a bottom view thereof.
The broken lines illustrate portions of the circuit board that form no part of the claimed design.

1 Claim, 5 Drawing Sheets



(56)

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Fig. 1

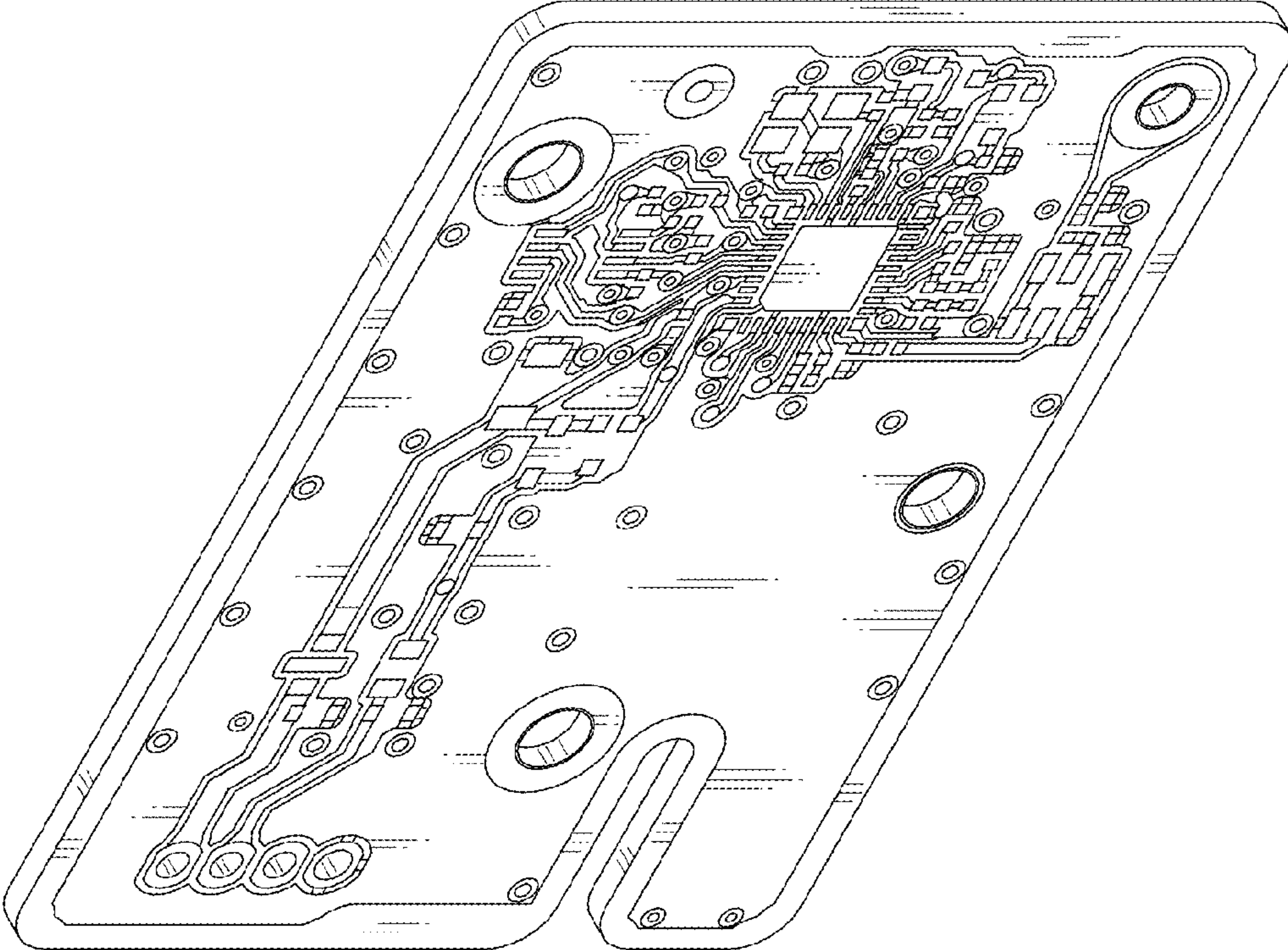


Fig. 2

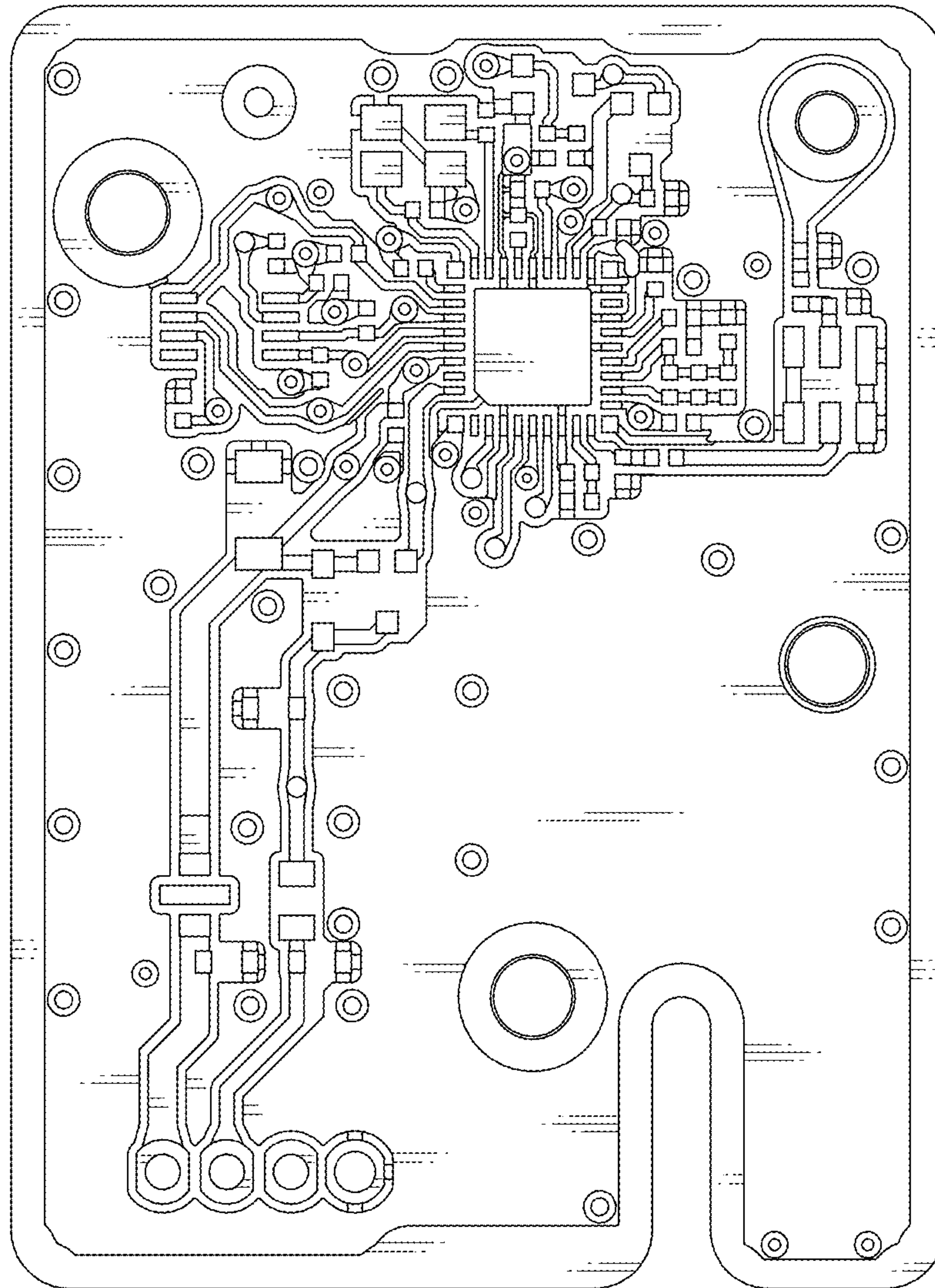


Fig. 3

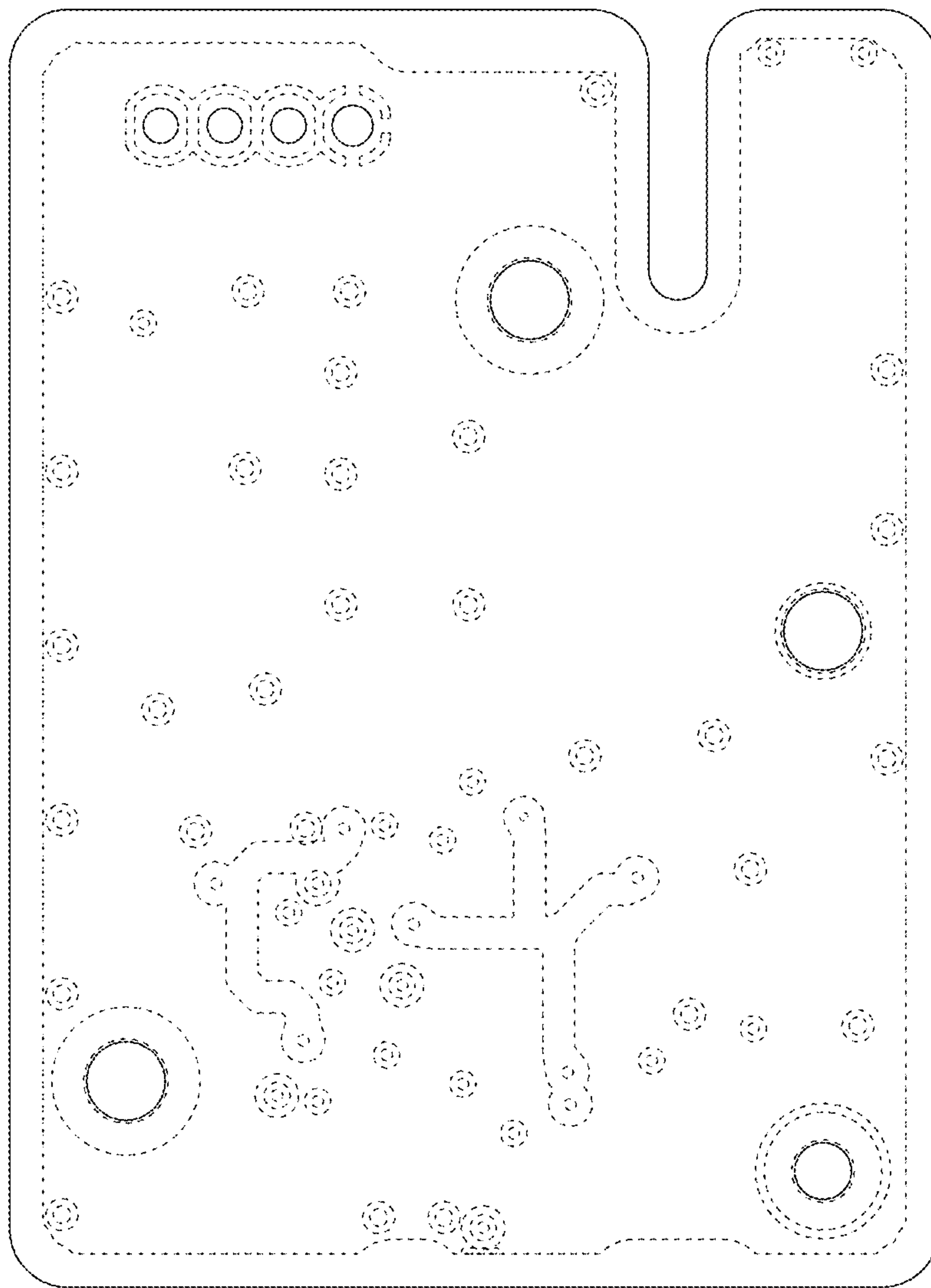


Fig. 5

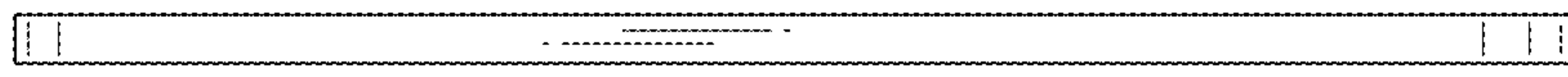


Fig. 4

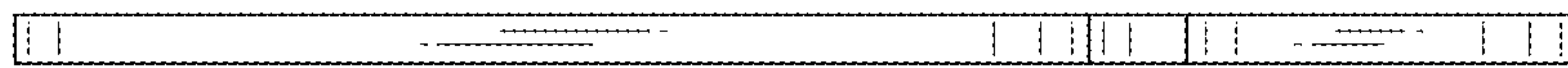


Fig. 6



Fig. 7

