



US00D797722S

(12) **United States Design Patent**
Lee et al.

(10) **Patent No.:** **US D797,722 S**
(45) **Date of Patent:** **** Sep. 19, 2017**

(54) **ELECTRONIC DEVICE**

(71) Applicant: **Samsung Electronics Co., Ltd.**,
Suwon-si (KR)

(72) Inventors: **Ji Young Lee**, Seoul (KR); **Hyok Su Choi**, Seoul (KR); **Hong Ku Yeo**,
Seongnam-si (KR); **Min-Hyouk Lee**,
Seoul (KR)

(73) Assignee: **SAMSUNG ELECTRONICS CO., LTD.**, Suwon-si (KR)

(**) Term: **14 Years**

(21) Appl. No.: **29/521,972**

(22) Filed: **Mar. 26, 2015**

(30) **Foreign Application Priority Data**

Dec. 31, 2014 (KR) 30-2014-0064599

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/341**

(58) **Field of Classification Search**
USPC D14/341-347, 137, 138 R, 138 AA,
D14/138 C, 138 G, 496, 203.1, 203.3,
D14/203.4, 203.7, 129, 130, 147, 218,
D14/247, 248, 250, 385, 388, 389,
D14/315-318, 420, 426, 439; D21/324,
D21/329, 330, 332
CPC . A63H 33/3016; G06F 1/1613; G06F 1/1647;
G06F 1/1641; G06F 3/04883; G06F
3/0486; G06F 3/04847; G06F 3/04845;
G06F 3/0484; H04M 1/0279; H04M
1/0281; H04M 1/0283
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D663,280 S * 7/2012 Lee D14/138 AD
D672,741 S * 12/2012 Lylyk D14/138 G

D675,586 S * 2/2013 Choi D14/138 G
D687,403 S * 8/2013 Park D14/138 G
D696,221 S * 12/2013 Lee D14/138 G
D696,247 S * 12/2013 Kim D14/341
D701,192 S * 3/2014 Park D14/138 G
D701,851 S * 4/2014 Bae D14/138 G
D706,251 S * 6/2014 Park D14/248
D710,815 S * 8/2014 Kim D14/138 G
D714,751 S * 10/2014 Kim D14/138 G
D714,752 S * 10/2014 Im D14/138 G
D717,263 S * 11/2014 Wozniak D14/138 G
D718,267 S * 11/2014 Hu D14/138 G
D720,711 S * 1/2015 Lin D14/138 G
D721,347 S * 1/2015 Lee D14/138 G
D729,189 S * 5/2015 Kangasmaa D14/138 G

(Continued)

Primary Examiner — Barbara Fox

Assistant Examiner — Dana K Weiland

(74) *Attorney, Agent, or Firm* — McAndrews Held &
Malloy, Ltd.

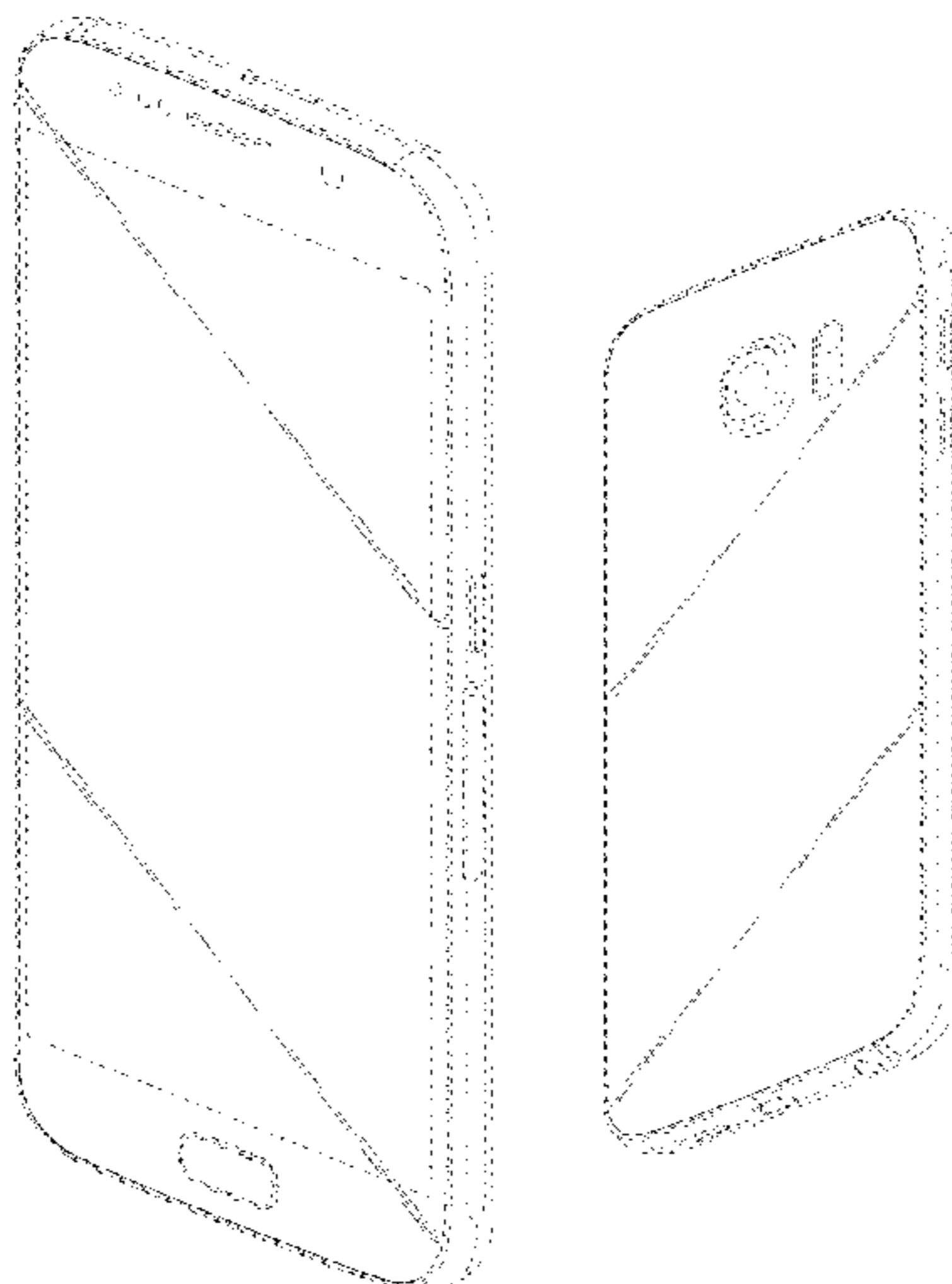
(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a top right front perspective view of an electronic device showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a left-side elevation view thereof;
FIG. 5 is a right-side elevation view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is a bottom left rear perspective view thereof.
The broken lines shown in the drawings illustrate portions of the electronic device that form no part of the claimed design.

1 Claim, 6 Drawing Sheets



(56)

References Cited

U.S. PATENT DOCUMENTS

D729,190 S * 5/2015 Kwak D14/138 G
D731,481 S * 6/2015 Akana D14/341
D732,016 S * 6/2015 Park D14/341
D744,447 S * 12/2015 Ryu D14/138 G
D744,448 S * 12/2015 Im D14/138 AA
D744,972 S * 12/2015 Im D14/138 AA
D751,523 S * 3/2016 Min D14/138 G
D770,431 S * 11/2016 Lee D14/248
2010/0207903 A1* 8/2010 Kim G06F 1/1626
345/173
2016/0054759 A1* 2/2016 Lee G06F 1/1656
361/679.28

* cited by examiner

FIG.1

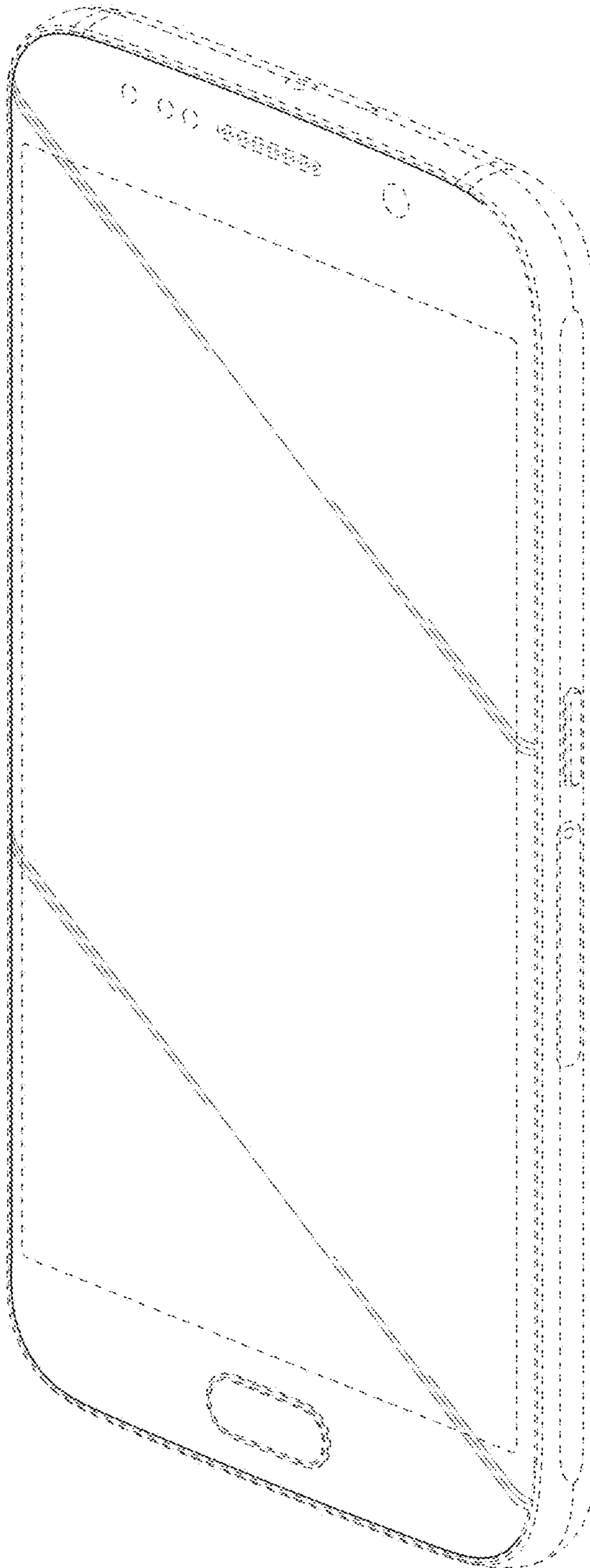


FIG.2

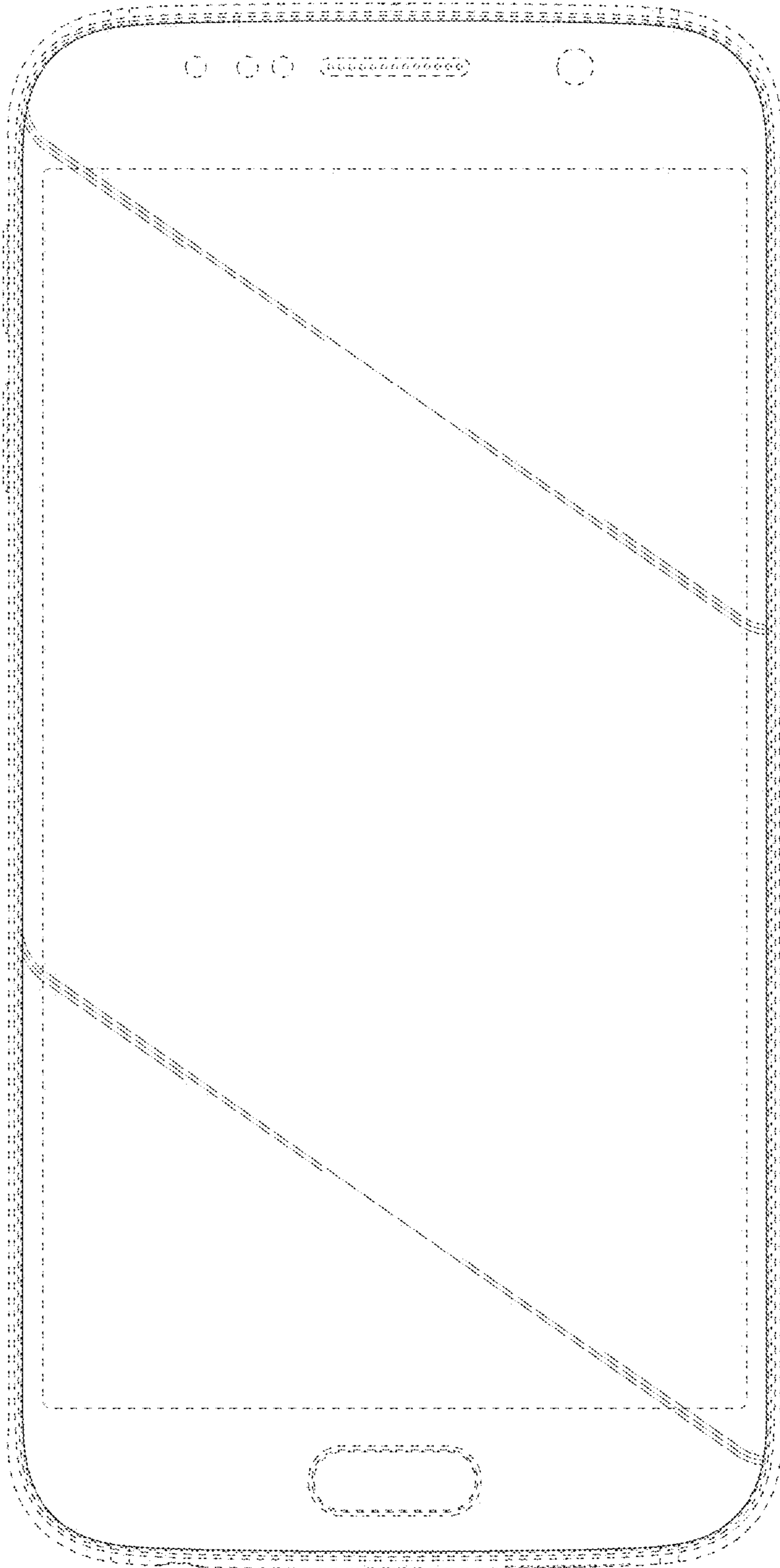


FIG.3

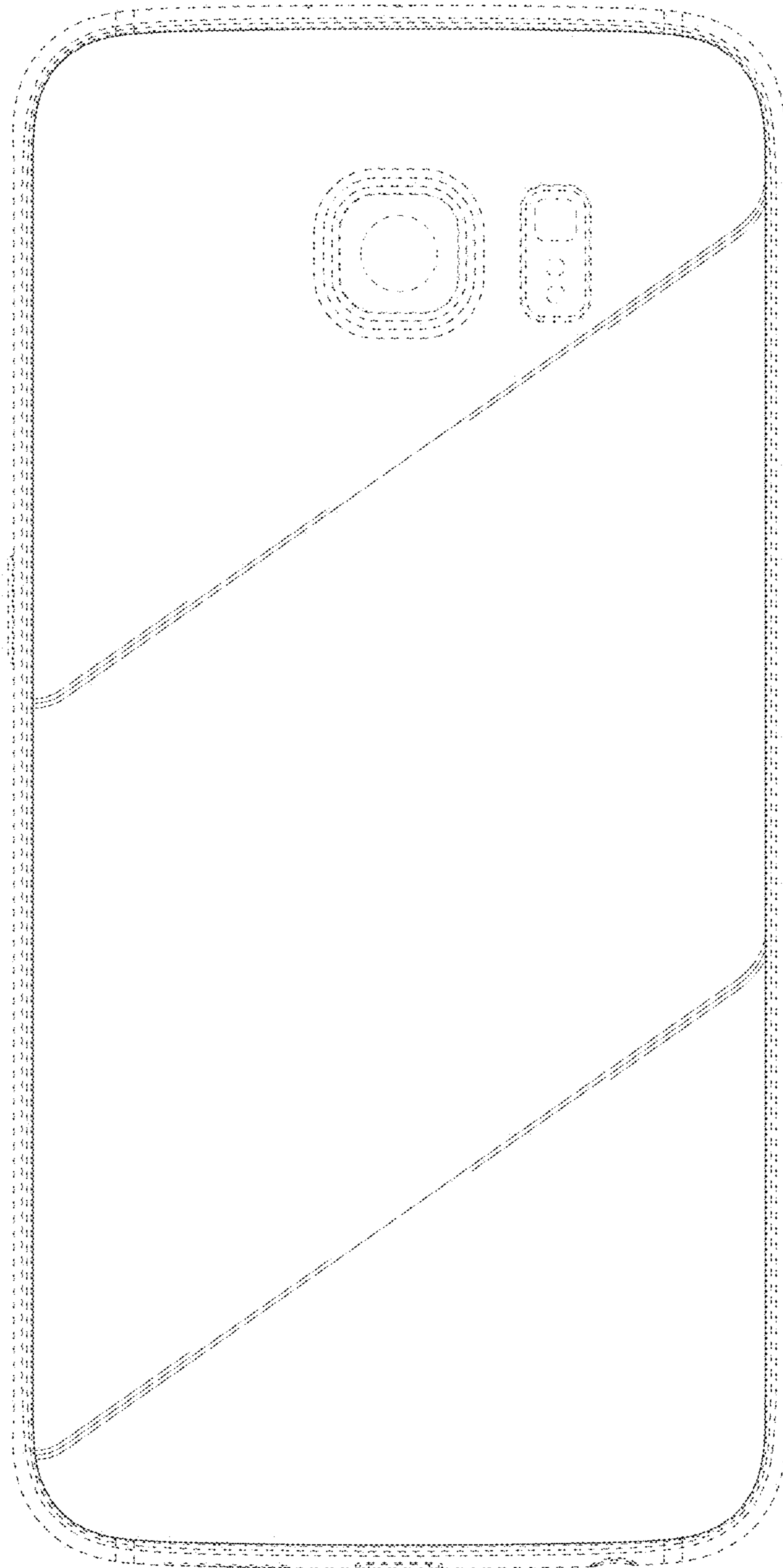


FIG.4

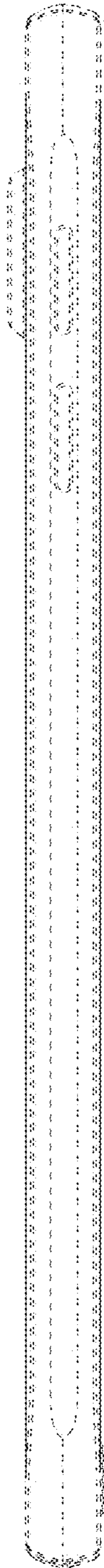


FIG.5

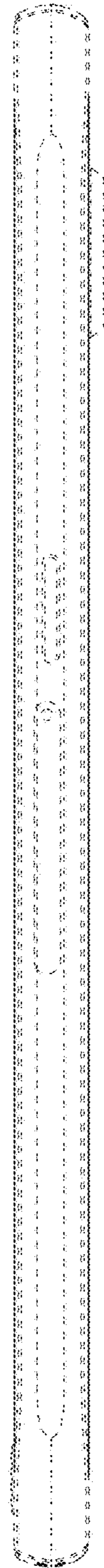


FIG.6

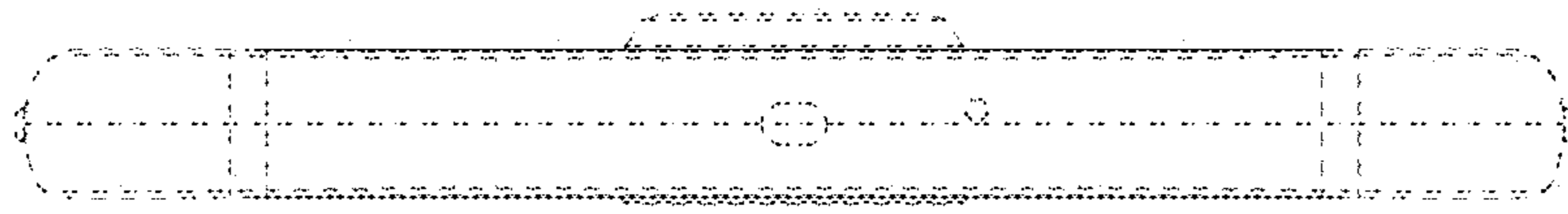


FIG.7

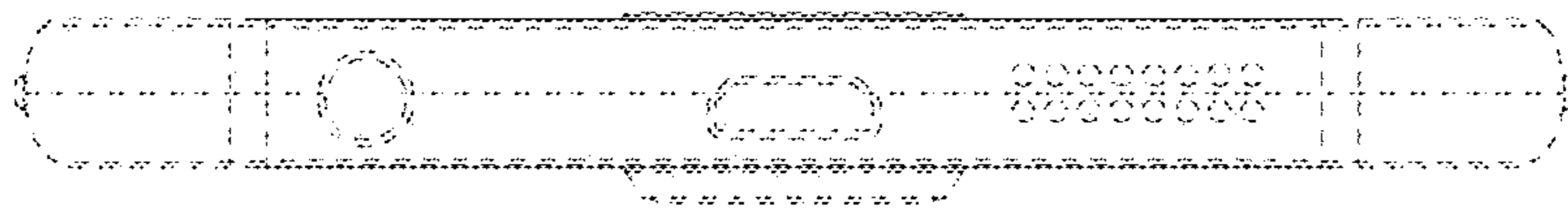


FIG.8

