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(12) **United States Design Patent** (10) **Patent No.:** **US D795,725 S**
Katsumoto et al. (45) **Date of Patent:** **** Aug. 29, 2017**

(54) **CONTAINER FOR MEASURING ELECTRICAL CHARACTERISTICS**

2300/0809; B01L 2300/0819; B01L 2300/0825; G01N 21/03; G01N 21/031; G01N 21/0317; G01N 21/0332; G01N 21/05

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See application file for complete search history.

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(56) **References Cited**

(73) Assignee: **SONY CORPORATION**, Tokyo (JP)

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(**) Term: **15 Years**

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(21) Appl. No.: **29/575,947**

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(22) Filed: **Aug. 30, 2016**

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Related U.S. Application Data

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(63) Continuation of application No. 29/502,866, filed on Sep. 19, 2014, now Pat. No. Des. 766,760.

(30) **Foreign Application Priority Data**

Primary Examiner — Antoine D Davis

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Mar. 19, 2014	(JP)	D2014-005816
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(57) **CLAIM**

The ornamental design for a container for measuring electrical characteristics, as shown and described.

(51) **LOC (10) Cl.** **10-04**

(52) **U.S. Cl.**
USPC **D10/81**

DESCRIPTION

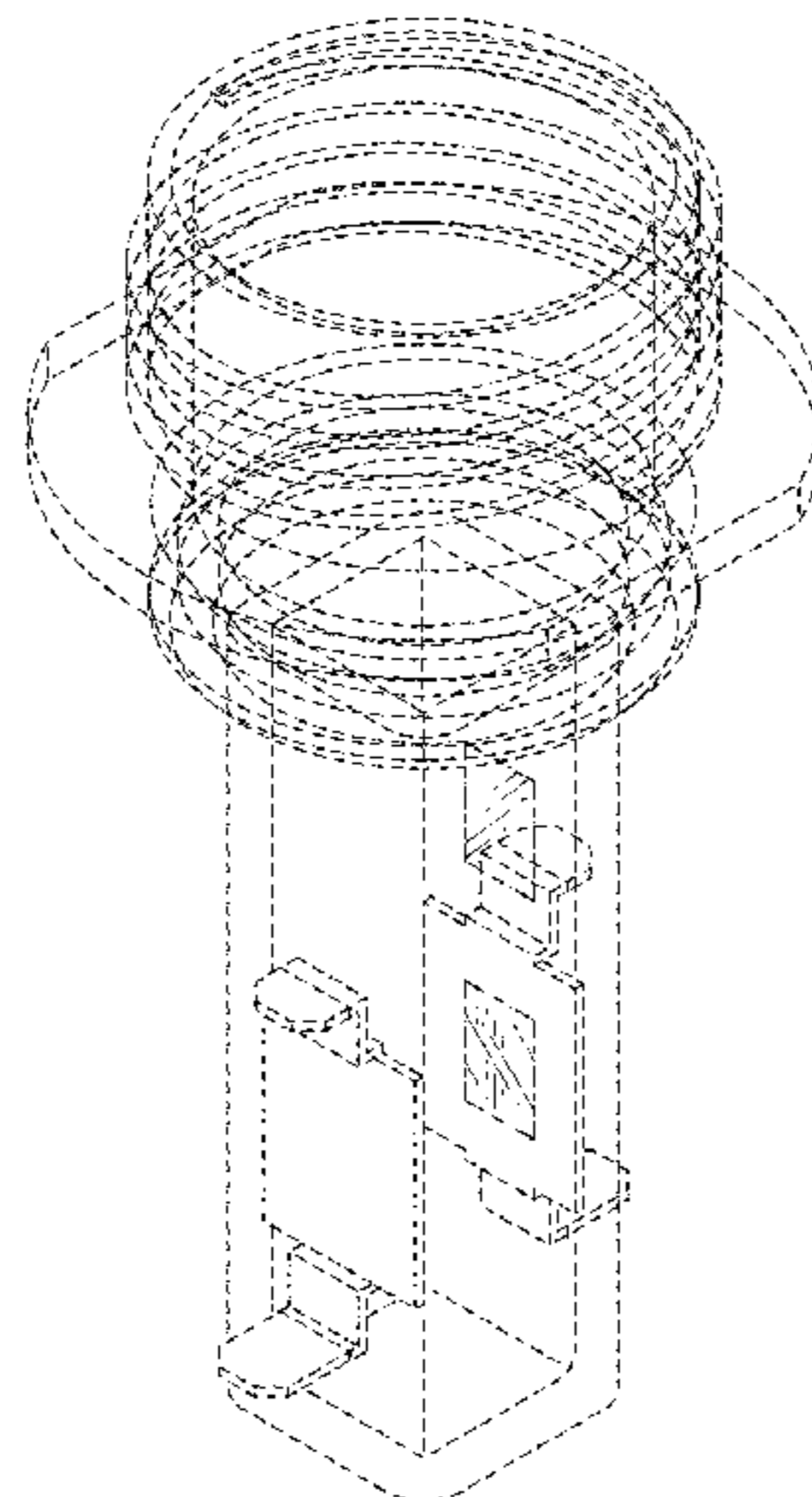
(58) **Field of Classification Search**

USPC D10/81; D24/224
CPC B01L 2300/0832; B01L 2300/0835; B01L 2300/0838; B01L 2300/0841; B01L 2300/0845; B01L 3/5021; B01L

FIG. 1 is a perspective view of a container for measuring electrical characteristics showing our new design;
FIG. 2 is a left side elevational view thereof;
FIG. 3 is a cross sectional view along the line 3-3 of FIG. 2; and,
FIG. 4 is a right side elevational view thereof.

The even-dash broken lines showing of unclaimed portions of the container for measuring electrical characteristics and form no part of the claimed design. The dot-dash broken lines define the boundaries of, but not included in, the claimed design.

1 Claim, 2 Drawing Sheets



(56)

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FIG.3

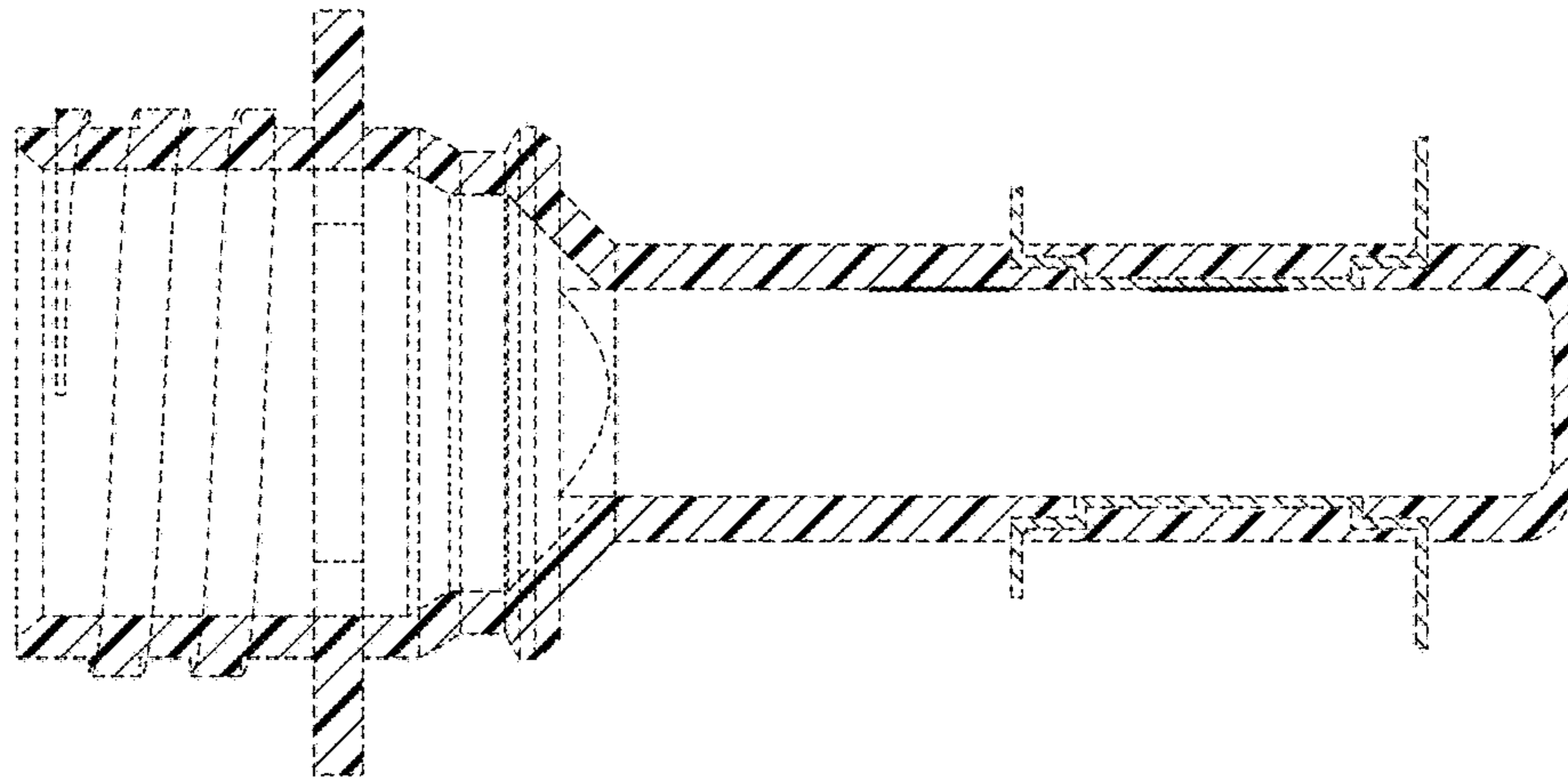


FIG.2

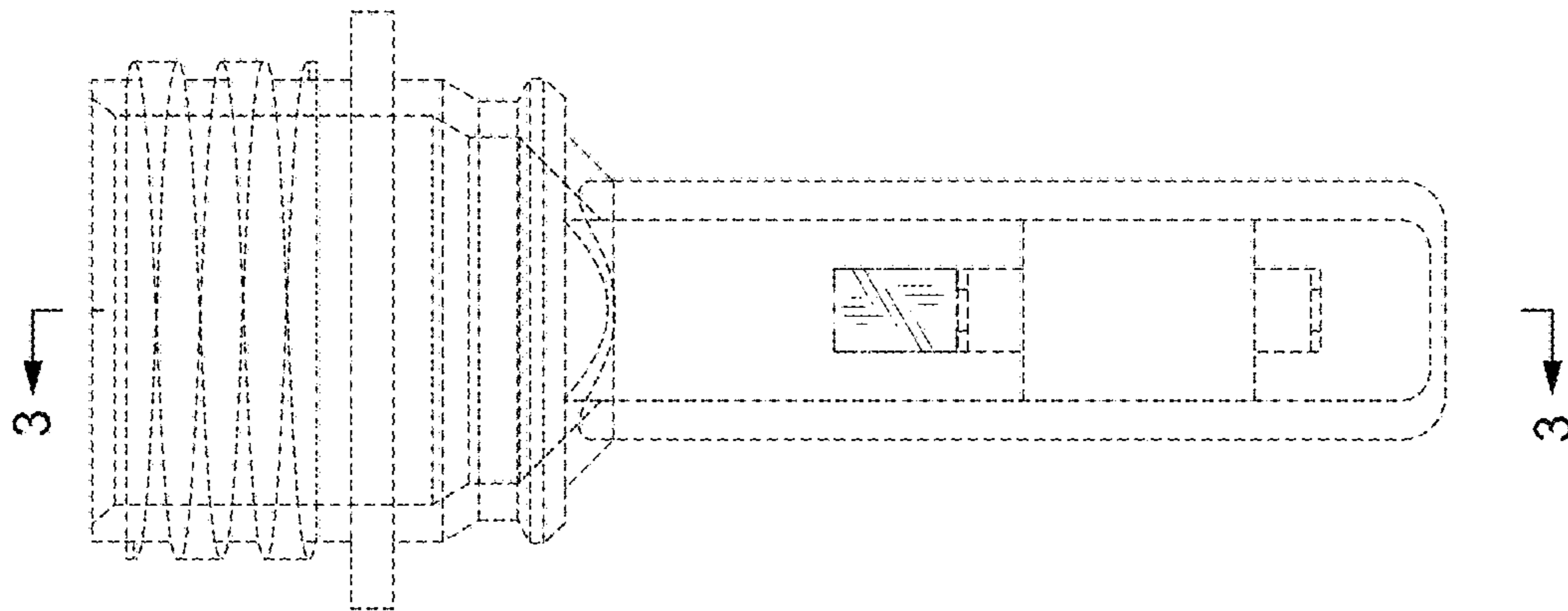


FIG.1

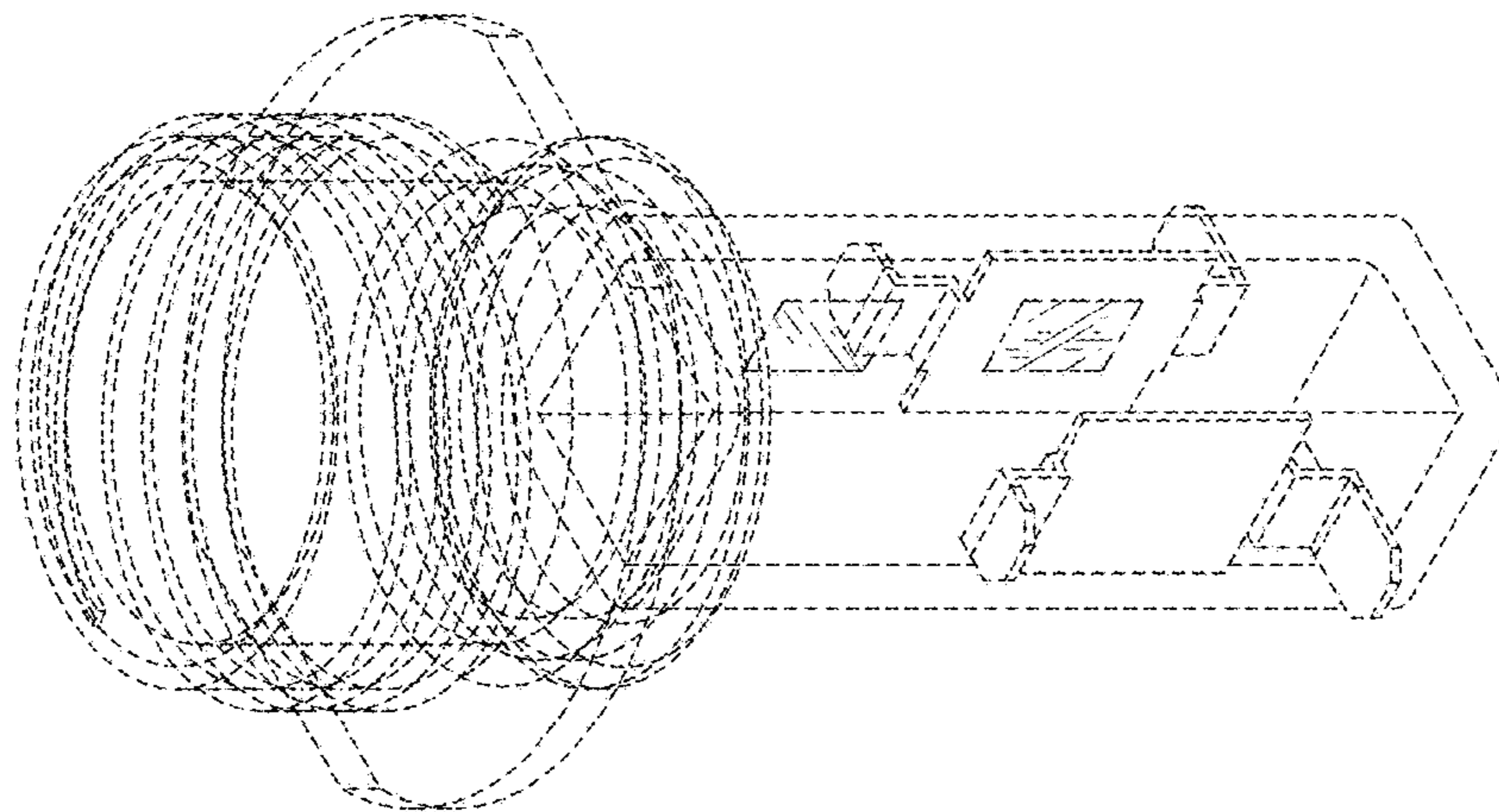


FIG.4

