

US00D794621S

(12) **United States Design Patent** (10) **Patent No.:** **US D794,621 S**
Kim et al. (45) **Date of Patent:** **** Aug. 15, 2017**

(54) **ELECTRONIC DEVICE**

(71) Applicant: **Samsung Electronics Co., Ltd.**,
Suwon-si (KR)

(72) Inventors: **Nam-Su Kim**, Seoul (KR); **Do-Hyung Ha**, Gwangmyeong-si (KR); **Sae-Hee Lee**, Incheon (KR); **Hyun-Sook Kim**, Seoul (KR)

(73) Assignee: **Samsung Electronics Co., Ltd.**,
Suwon-si (KR)

(**) Term: **14 Years**

(21) Appl. No.: **29/506,339**

(22) Filed: **Oct. 15, 2014**

(30) **Foreign Application Priority Data**

Oct. 2, 2014 (KR) 30-2014-0047856

(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/248**

(58) **Field of Classification Search**
USPC ... D14/138 AB, 138 G, 138 R, 203.1, 203.3,
D14/203.5, 203.7, 248, 250; D21/517,
D21/329
CPC . G06F 1/1626; H04M 1/0266; H04M 1/0202;
H04M 1/03; H04M 1/0214
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D678,270 S * 3/2013 Song D14/341
D697,493 S * 1/2014 Park D14/138 G
D698,331 S * 1/2014 Yoo D14/138 G

D700,902 S * 3/2014 Park D14/138 G
D701,191 S * 3/2014 Suk D14/138 G
D701,192 S * 3/2014 Park D14/138 G
D706,234 S * 6/2014 Park D14/138 G
D706,738 S * 6/2014 Kim D14/138 G
D709,474 S * 7/2014 Kim D14/138 G
D715,279 S * 10/2014 Lee D14/248
D715,280 S * 10/2014 Lee D14/248
D716,253 S * 10/2014 Lee D14/138 AB
D718,265 S * 11/2014 Lee D14/138 G
D718,732 S * 12/2014 Lee D14/138 G
D720,339 S * 12/2014 Kim D14/248
D720,340 S * 12/2014 Kim D14/248

* cited by examiner

Primary Examiner — George D Kirschbaum

Assistant Examiner — Joseph Kukella

(74) *Attorney, Agent, or Firm* — NSIP Law

(57) **CLAIM**

We claim the ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic device, showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a left-side view thereof;
FIG. 5 is a right-side view thereof;
FIG. 6 is a top view thereof; and,
FIG. 7 is a bottom view thereof.

The broken lines illustrating portions of the electronic device are for environmental purposes only and form no part of the claimed design. The dot-dash lines in the figures represent an unclaimed boundary line and form no part of the claimed design.

1 Claim, 5 Drawing Sheets

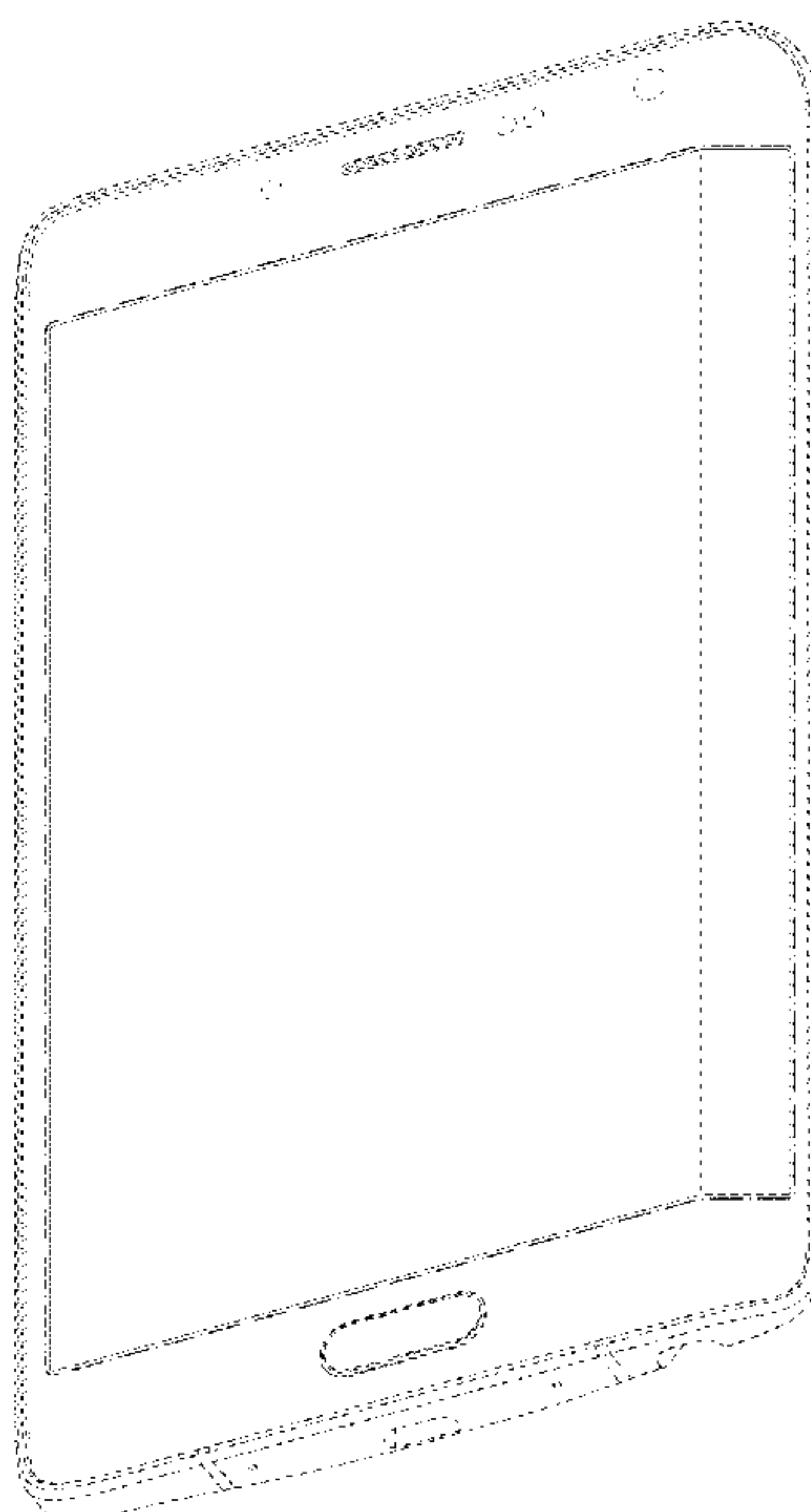


FIG.1

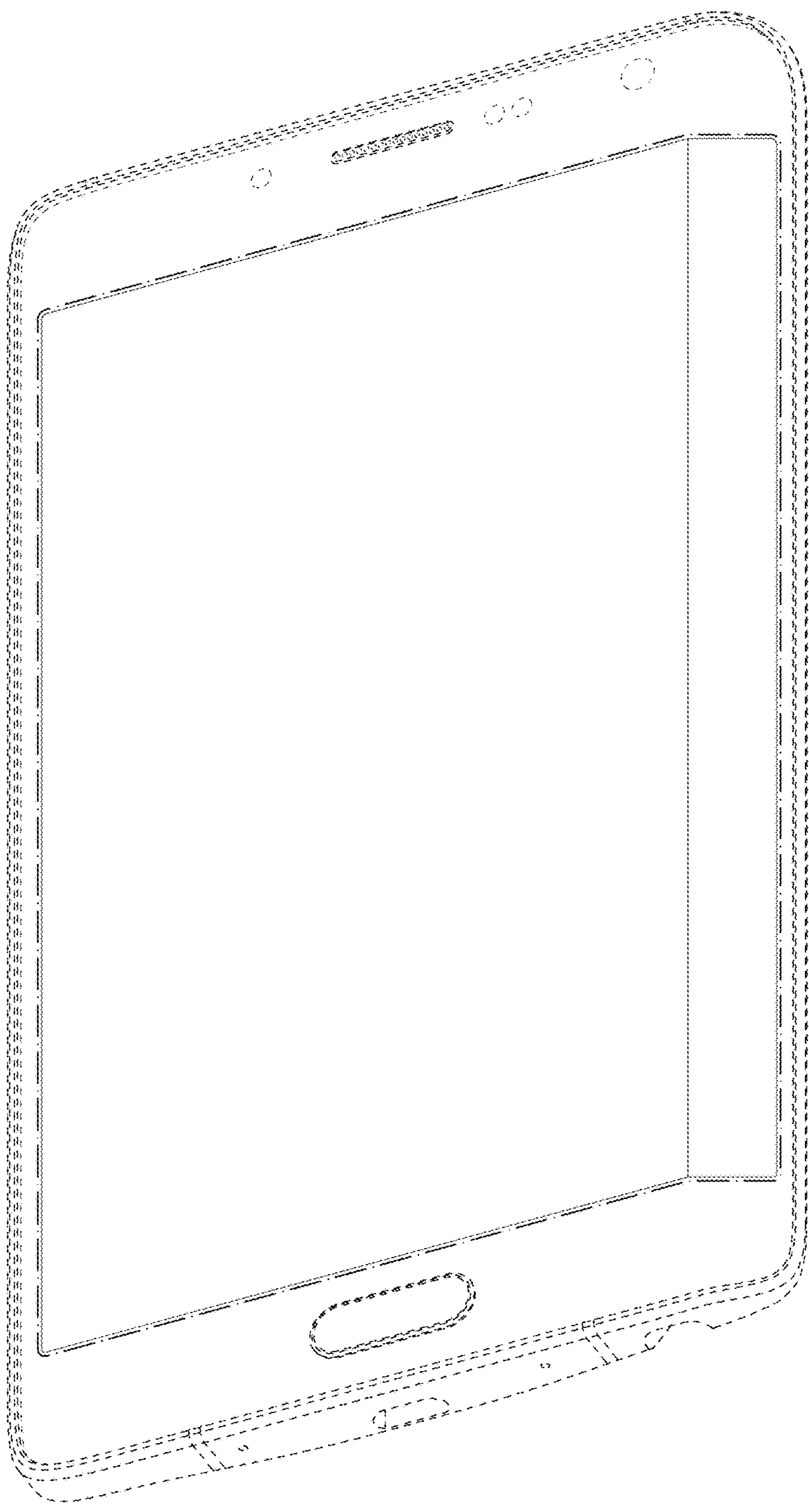


FIG.2

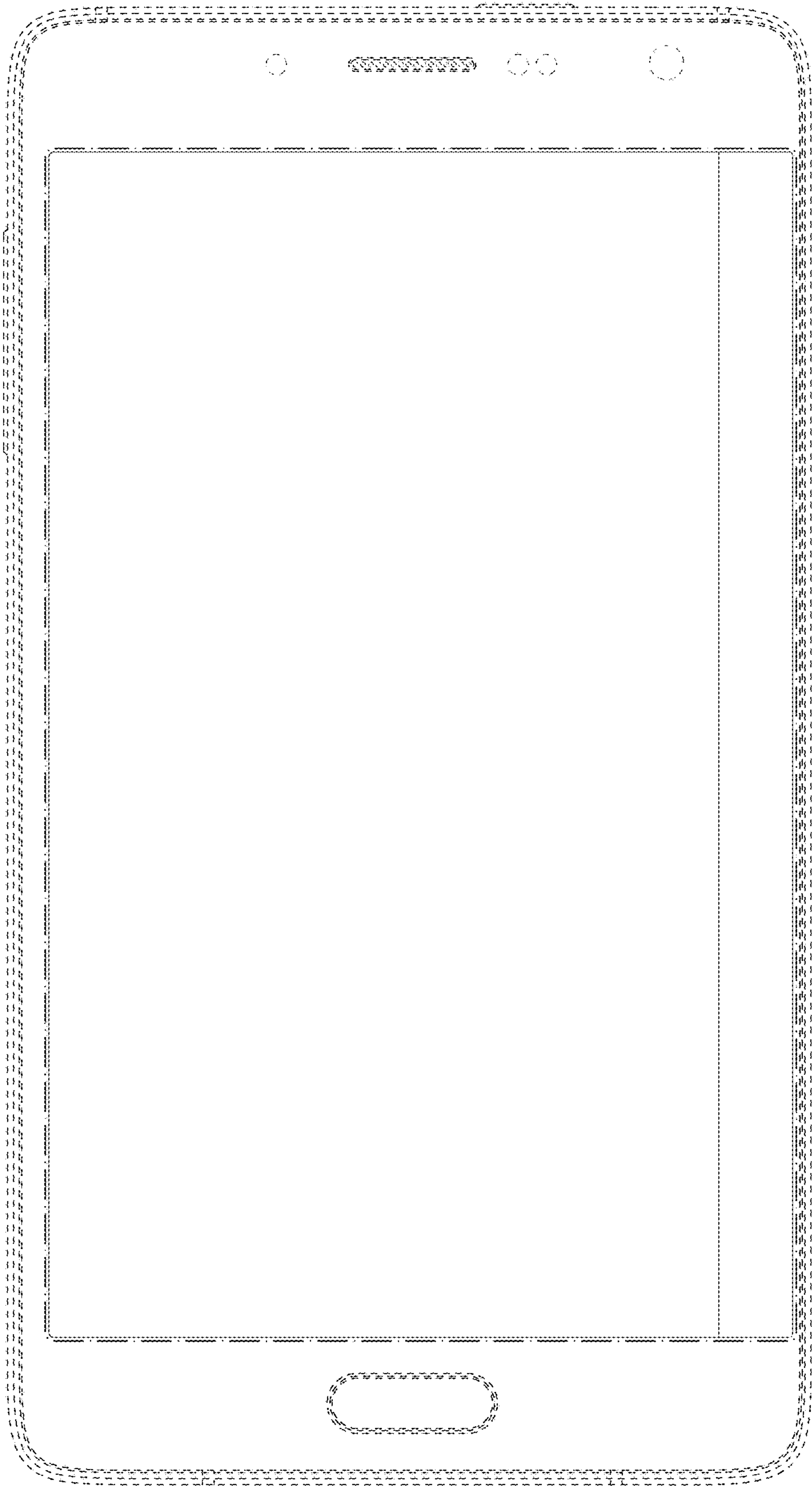


FIG.3

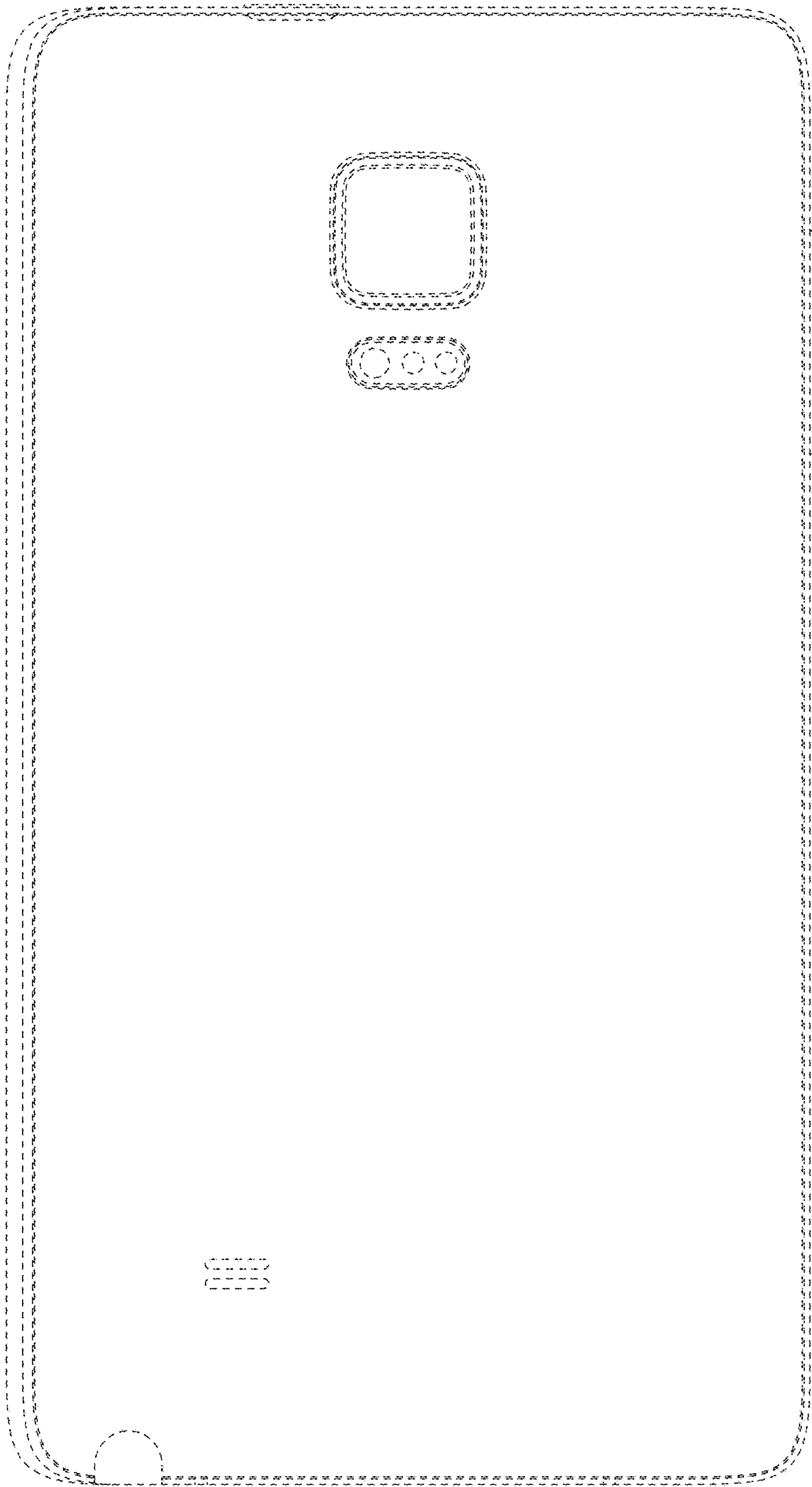


FIG.4

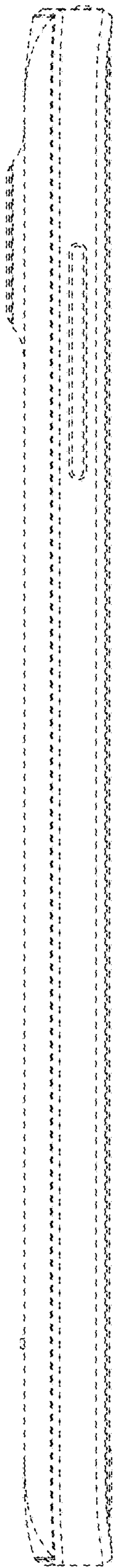


FIG.5

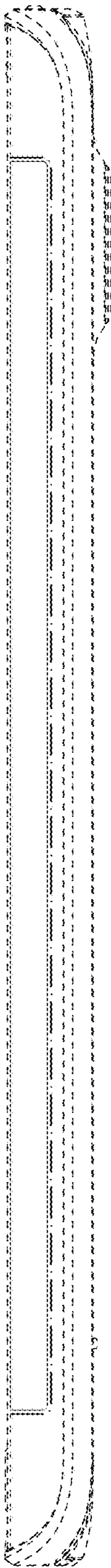


FIG.6

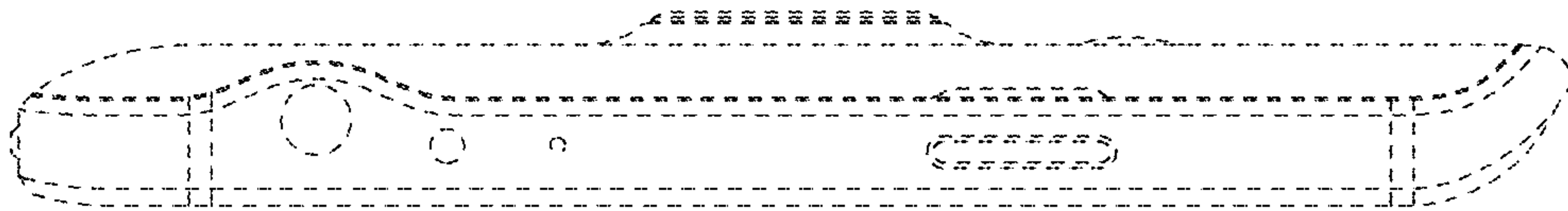


FIG.7

