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(54) **WAFER CARRIER WITH A 31-POCKET CONFIGURATION**

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CPC H01R 13/5213; B60Q 1/04; B60Q 1/26; G10D 3/006; G10D 13/023; G10D 13/027; B25B 15/001; G10H 3/143; G10H 3/181; G10G 5/005; A42B 1/04; B64C 11/04; B64C 11/16; B64C 11/18; B64C 11/20; B64C 2001/009; F03D 3/062; F03D 11/04; F04D 19/002; F04D 25/068; F04D 25/088; F24F 1/0018; B64D 13/02; E04C 2/427; E06B 3/5892; B24B 37/04
See application file for complete search history.

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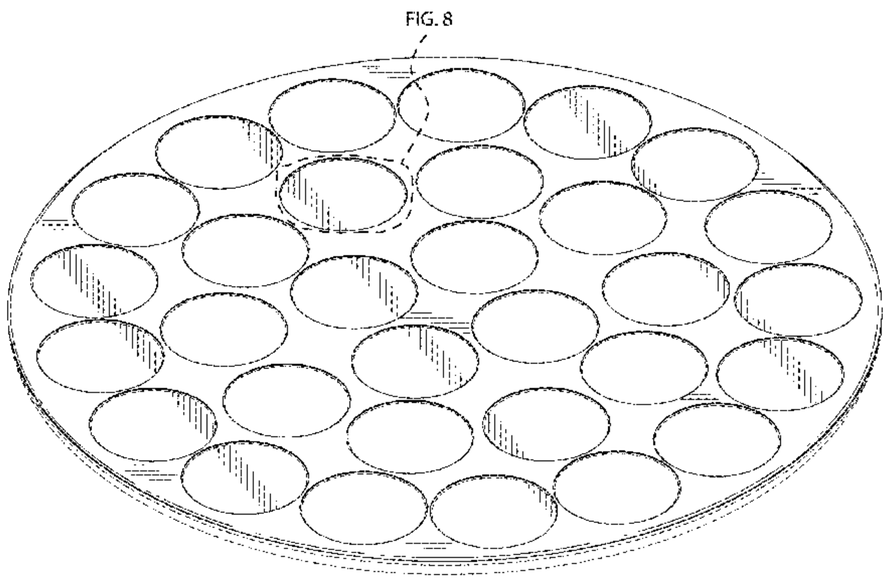
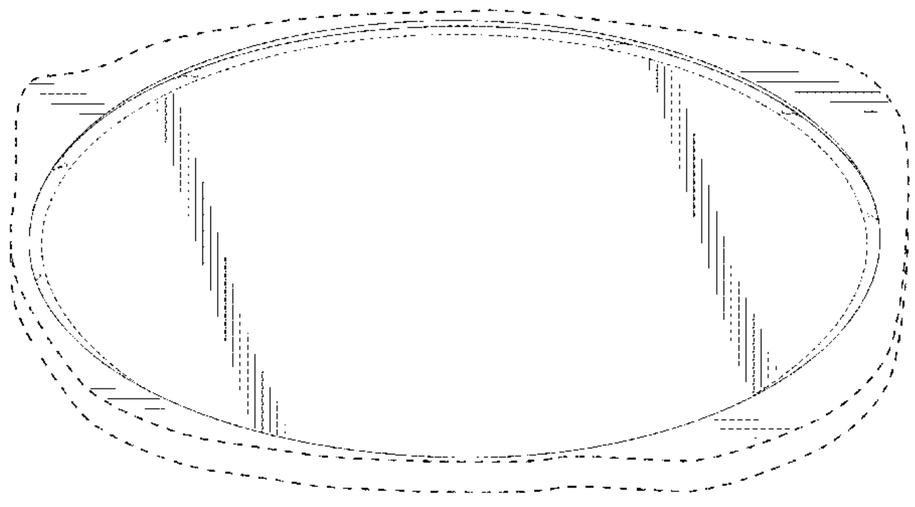
FIG. 8





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Assistant Examiner — Sean D Lough
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(57) **CLAIM**

We claim the ornamental design for a wafer carrier with a 31-pocket configuration, as shown and described.

DESCRIPTION

FIG. **1** is a perspective view of a wafer carrier with a 31-pocket configuration according to the new design of the first embodiment.

FIG. **2** is a top plan view of a wafer carrier with a 31-pocket configuration according to the new design thereof.

FIG. **3** is a right side view of a wafer carrier with a 31-pocket configuration according to the new design, with the left side, front, and rear views being the same as the right side view thereof.

FIG. **4** is a bottom plan view of a wafer carrier with a 31-pocket configuration according to the new design thereof.

FIG. **5** is a perspective view of a wafer carrier with a 31-pocket configuration according to the new design of the second embodiment.

FIG. **6** is a right side view of a wafer carrier with a 31-pocket configuration according to the new design, with the left side, front, and rear views being the same as the right side view thereof.

FIG. **7** is a bottom plan view of a wafer carrier with a 31-pocket configuration according to the new design thereof; and,

FIG. **8** is a detail view of a portion of the wafer carrier with a 31-pocket configuration shown in FIG. **1** according to the new design showing a single pocket from a perspective view of the first embodiment.

The broken lines, where present, in FIGS. **1**, **2**, **3**, **4**, and **8**, illustrate portions of the wafer carrier with a 31-pocket configuration that represent environment of the claimed design and form no part of the claimed design. The broken lines of FIG. **1** that define the area corresponding to the enlarged portion shown in FIG. **8** form no part of the claimed design. The broken lines in FIG. **8** that represent the edge of the enlarged portion of the design form no part of the claimed design.

1 Claim, 8 Drawing Sheets

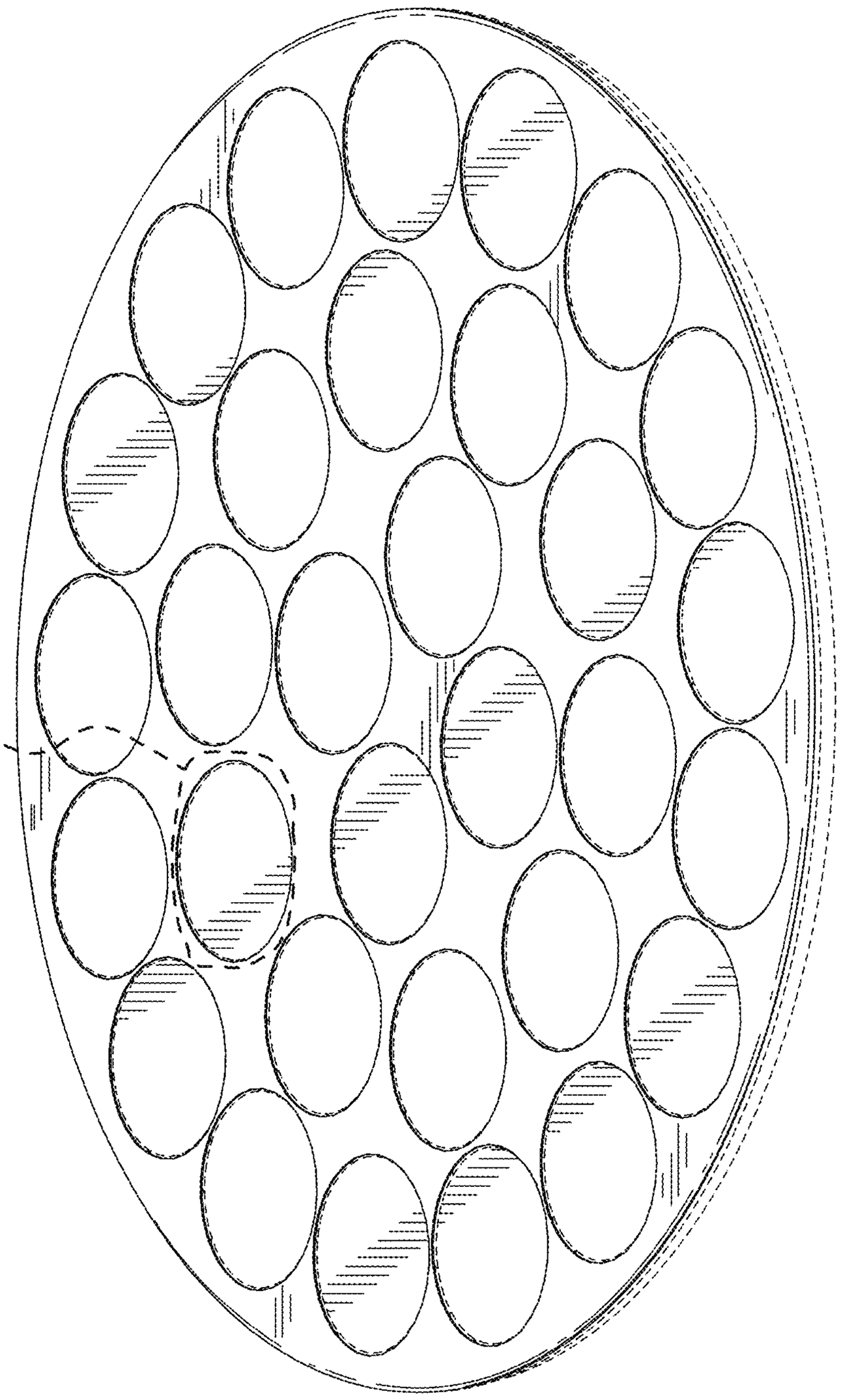

Fig. 1

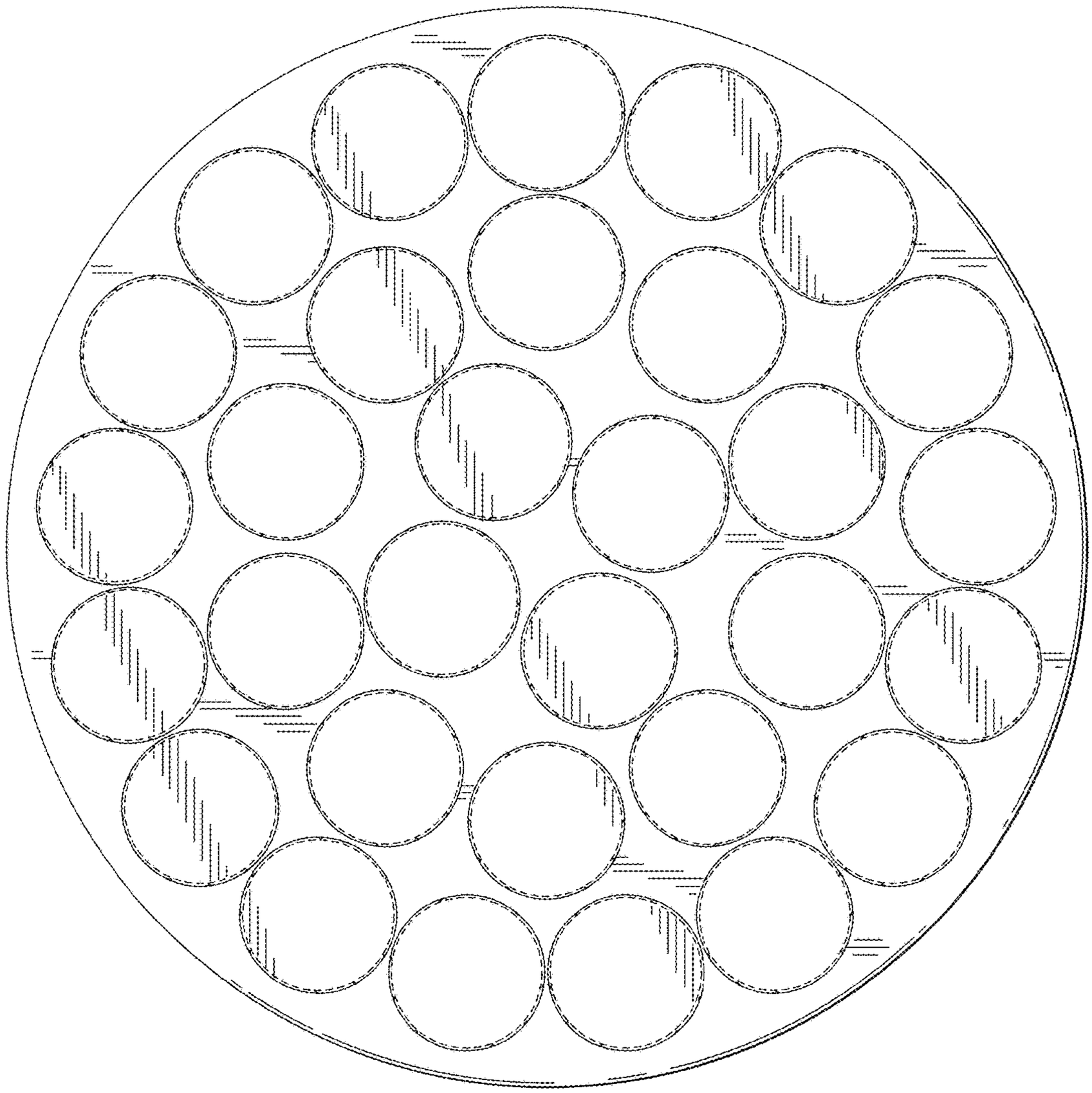

Fig. 2

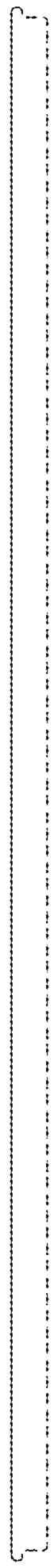

Fig. 3

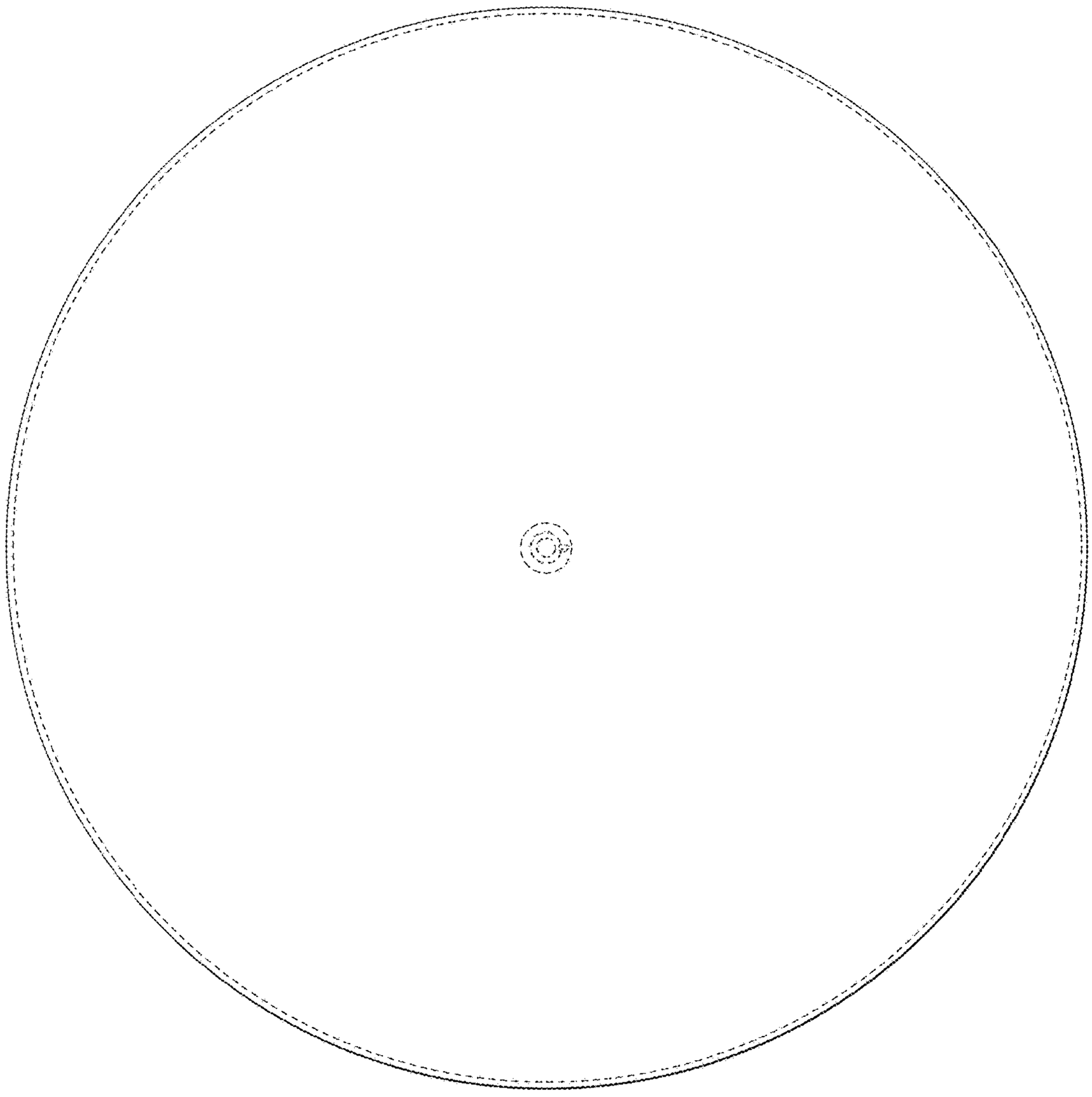

Fig. 4

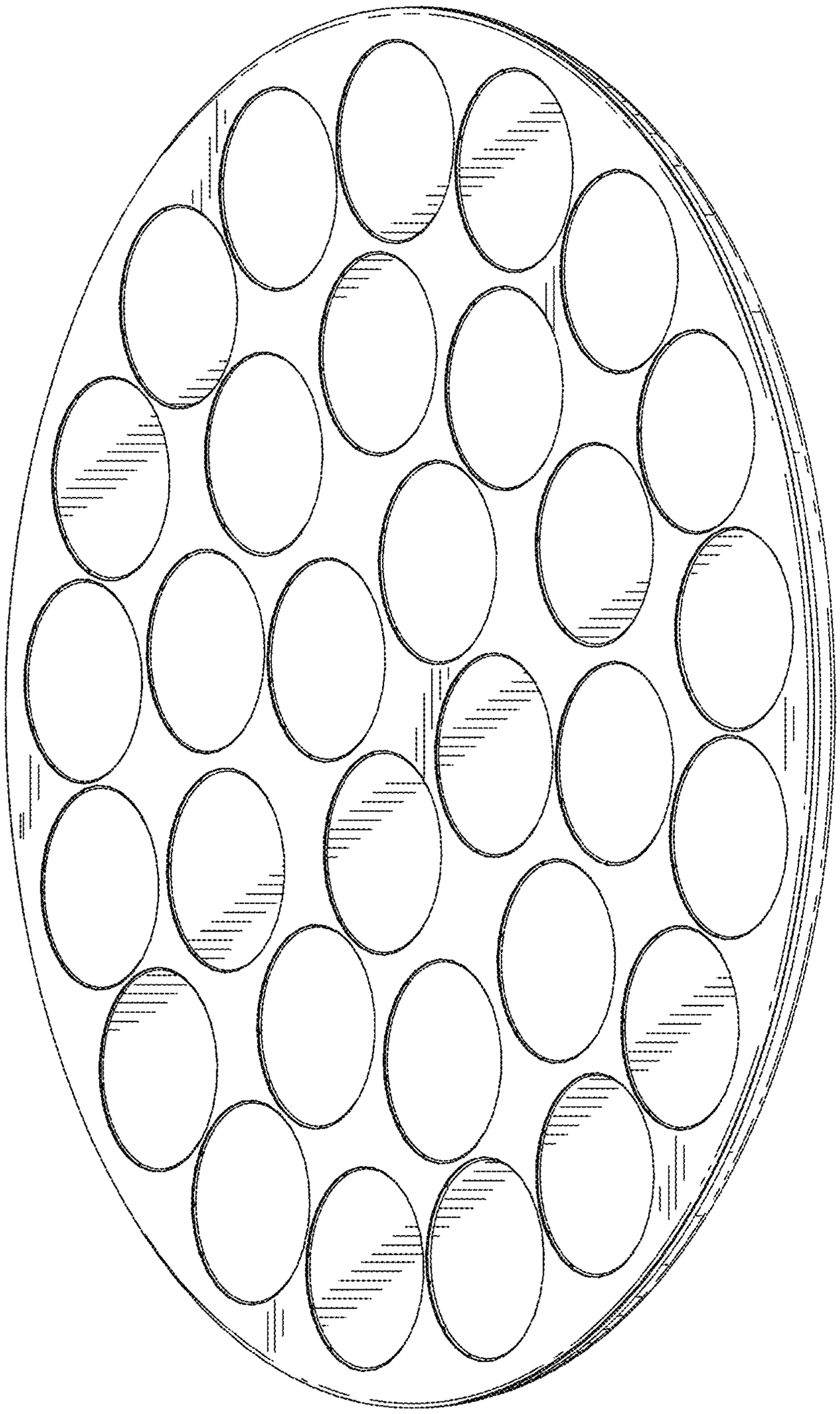

Fig. 5

Fig. 6

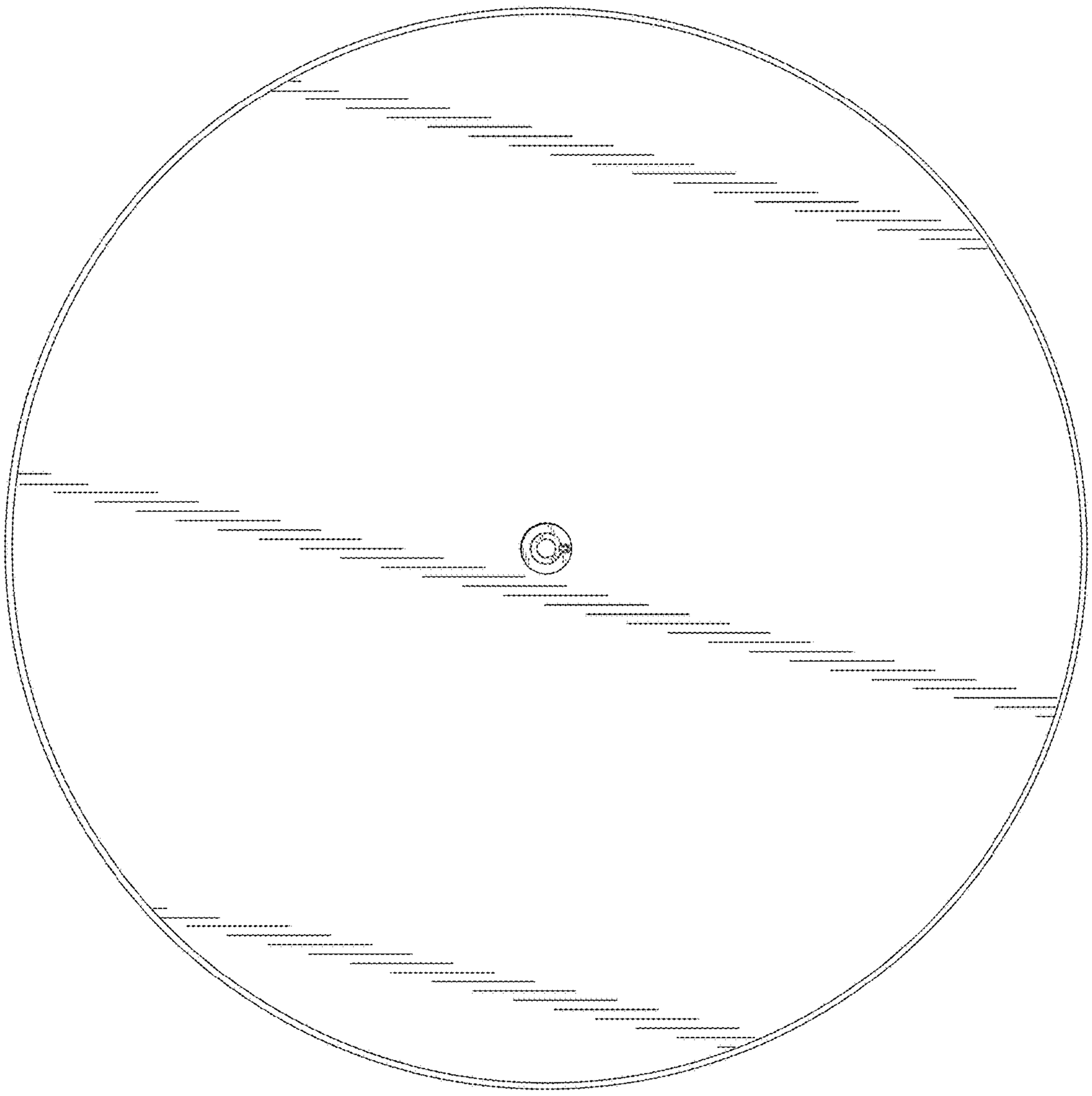

Fig. 7

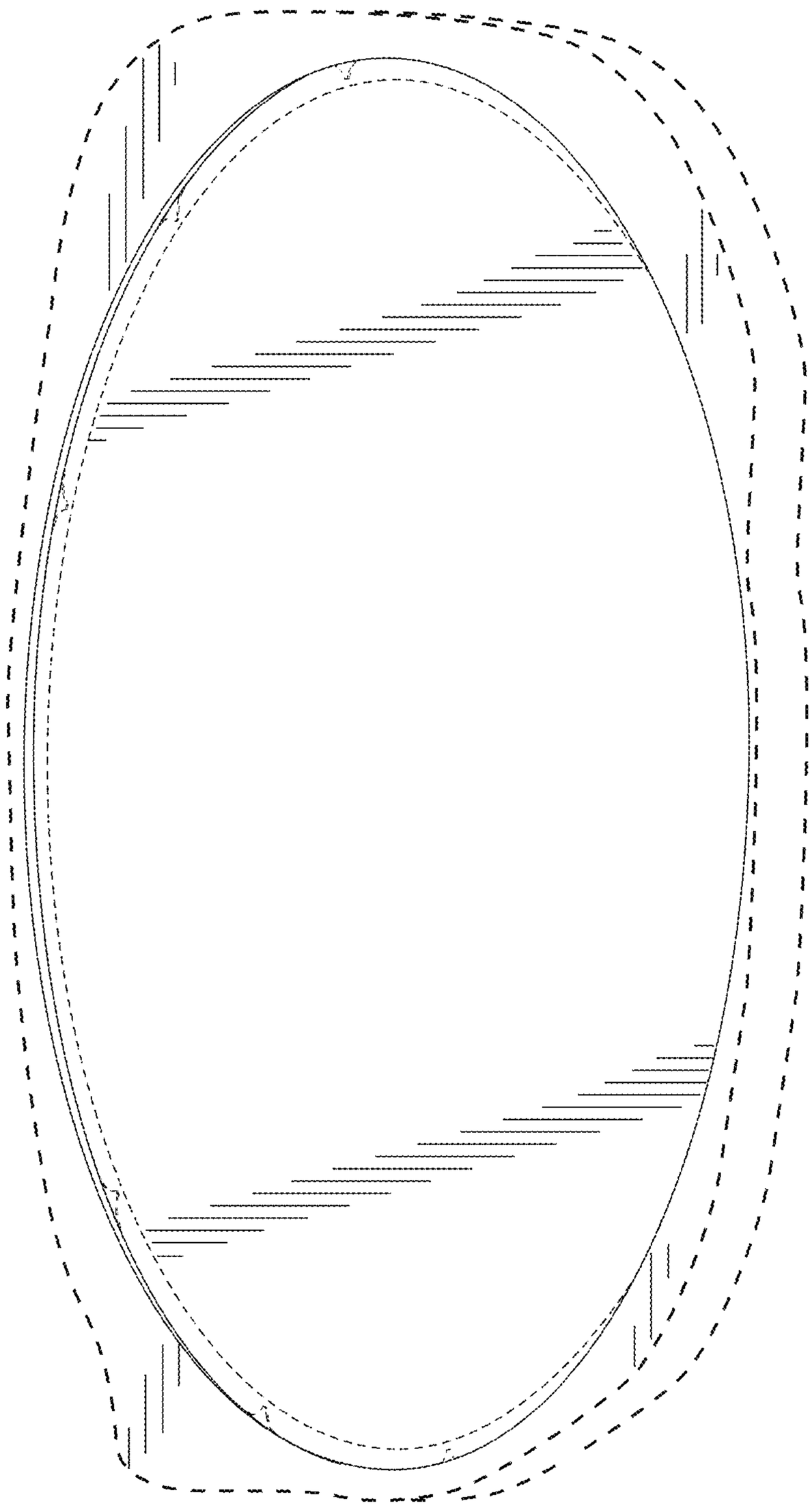

Fig. 8