

US00D789303S

(12) **United States Design Patent**  
**Papakos et al.**

(10) **Patent No.:** **US D789,303 S**

(45) **Date of Patent:** **\*\* Jun. 13, 2017**

(54) **FRONT PANEL FOR AN OPTICAL CIRCUIT CARD**

D371,540 S \* 7/1996 Siemon ..... D13/146  
D422,563 S \* 4/2000 Wu ..... D13/147  
D510,068 S \* 9/2005 Haggay ..... D13/147  
8,348,517 B2 \* 1/2013 Mudd ..... G02B 6/3849  
385/53

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\* cited by examiner

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(\*\*) Term: **15 Years**

(57) **CLAIM**

(21) Appl. No.: **29/538,087**

The ornamental design for a front panel for an optical circuit card, as shown and described.

(22) Filed: **Sep. 1, 2015**

**DESCRIPTION**

(51) **LOC (10) Cl.** ..... **13-03**

(52) **U.S. Cl.**  
USPC ..... **D13/147**

(58) **Field of Classification Search**  
USPC ..... D13/133, 146, 147, 154, 155, 156, 184,  
D13/199  
CPC ... G02B 6/12; G02B 6/26; G02B 6/36; G02B  
6/42; G02B 6/43; G02B 6/44; H01R  
13/62

FIG. 1 is a front elevation view of a front panel for an optical circuit card showing our new design; FIG. 2 is a side elevation view thereof; FIG. 3 is a top plan view thereof; FIG. 4 is a bottom plan view thereof; and, FIG. 5 is a top perspective view of a portion thereof, on an enlarged scale.

See application file for complete search history.

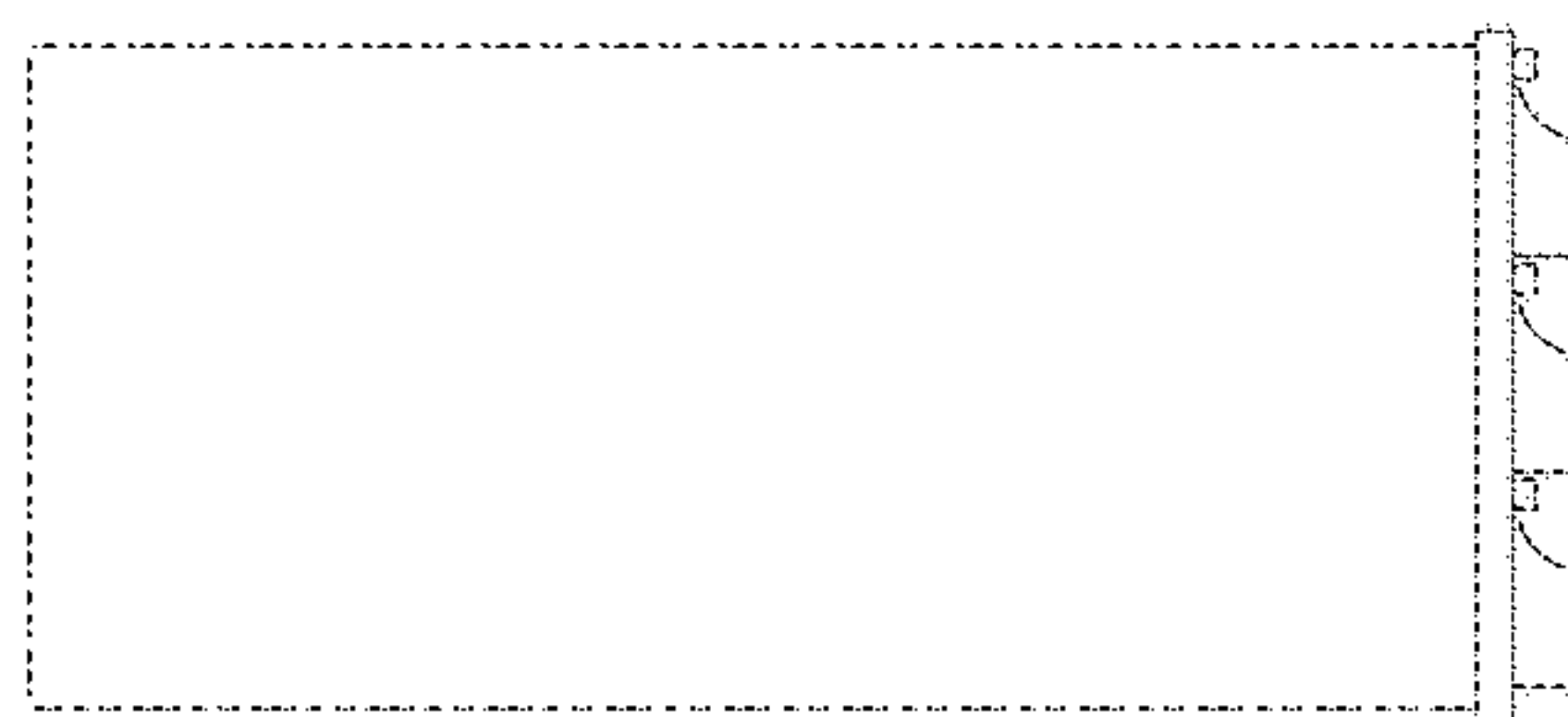
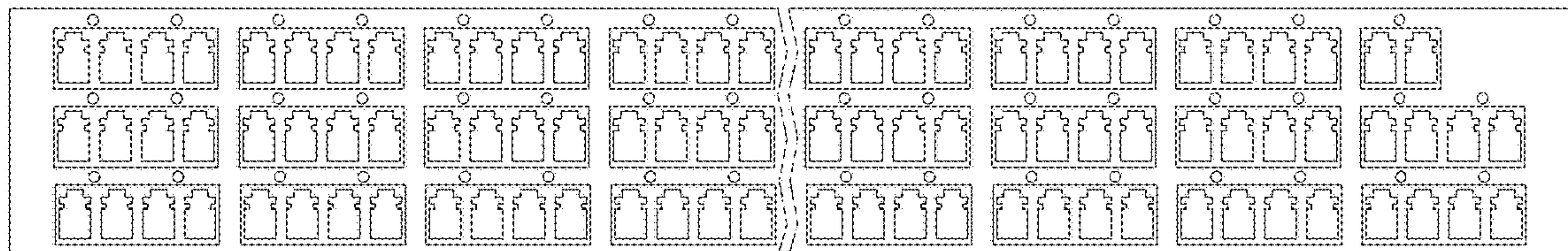
The broken line showing of unclaimed portions of the front panel for an optical circuit card is for the purposes of illustrating environment structure and forms no part of the claimed design. The break lines showing in FIGS. 1, 3 and 4 represents indeterminate length, and portions of the article between the break lines forms no part of the claimed design.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D317,750 S \* 6/1991 Bellomo ..... D13/147  
D345,729 S \* 4/1994 Dickens ..... D13/147

**1 Claim, 5 Drawing Sheets**



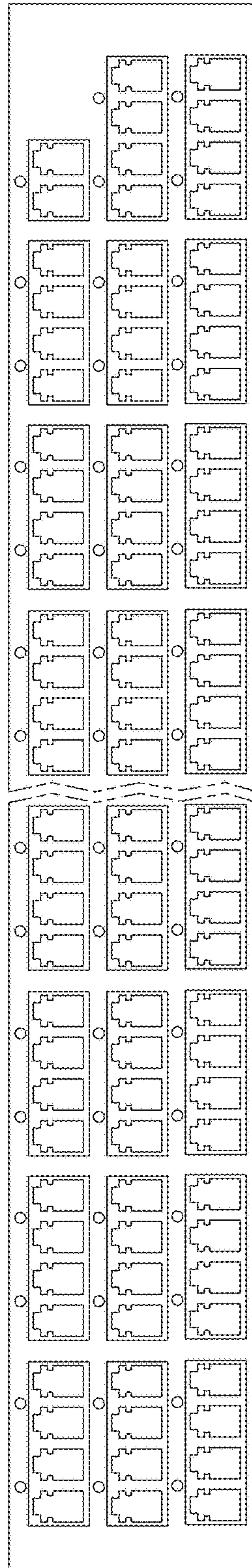


FIG. 1

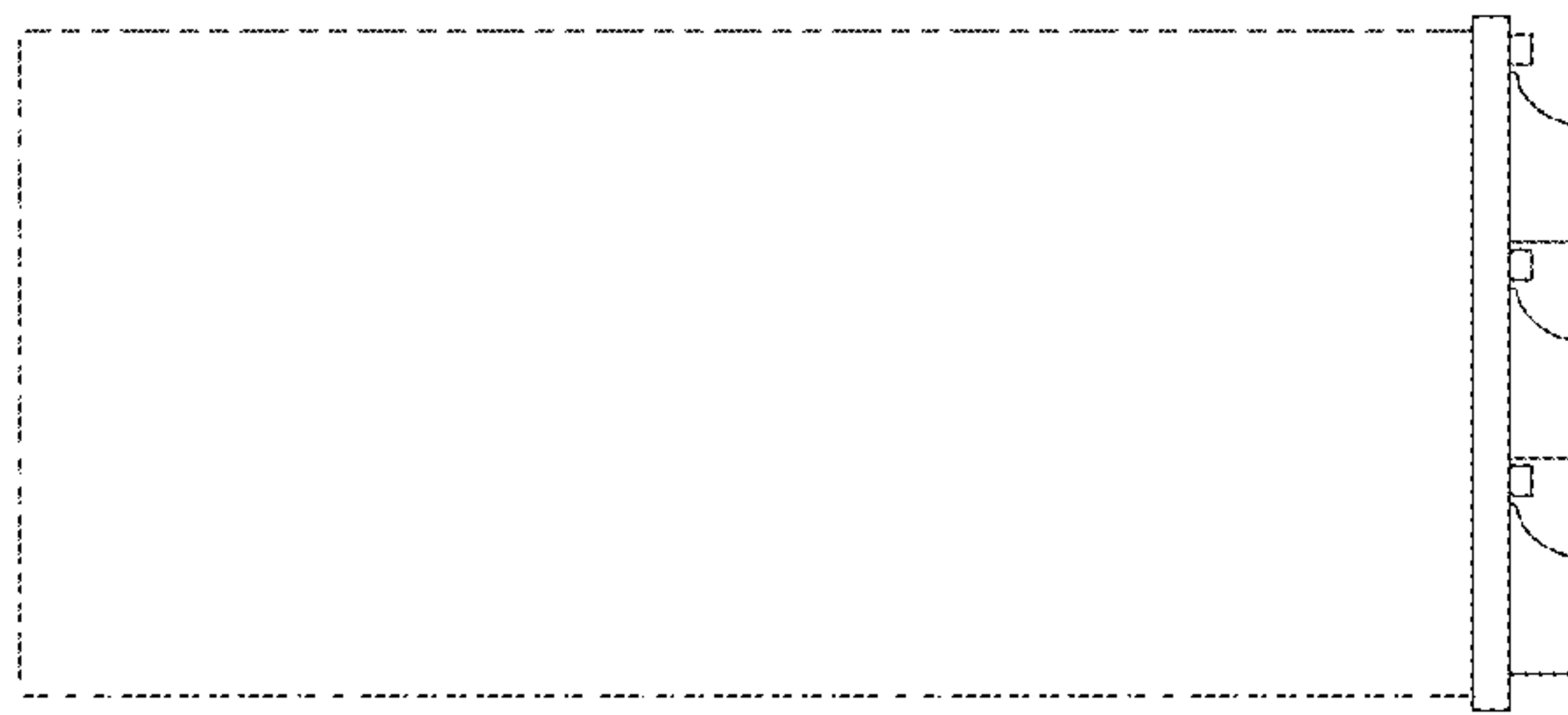


FIG. 2

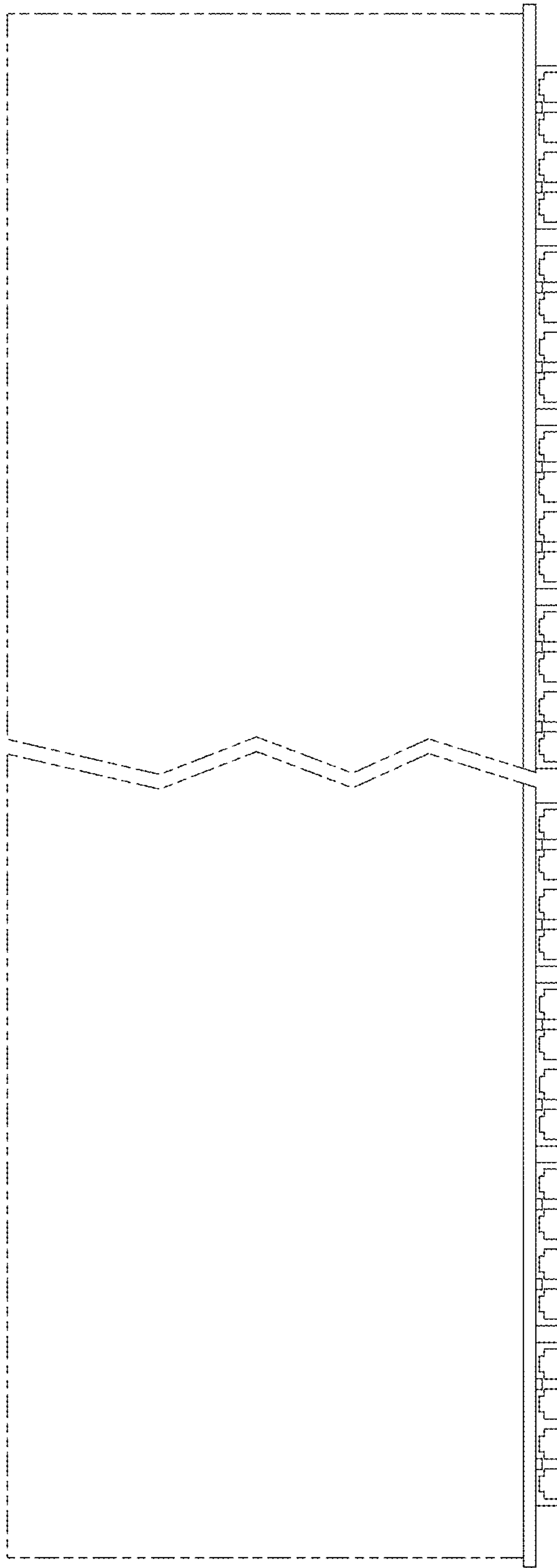


FIG. 3

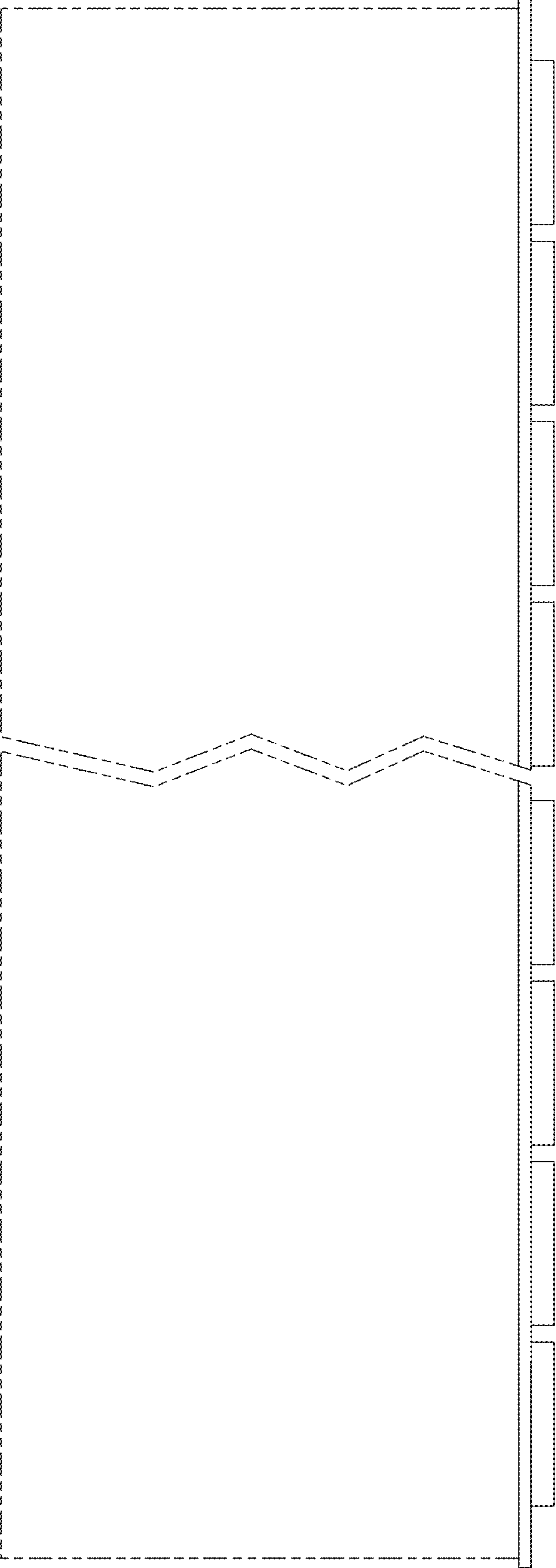


FIG. 4

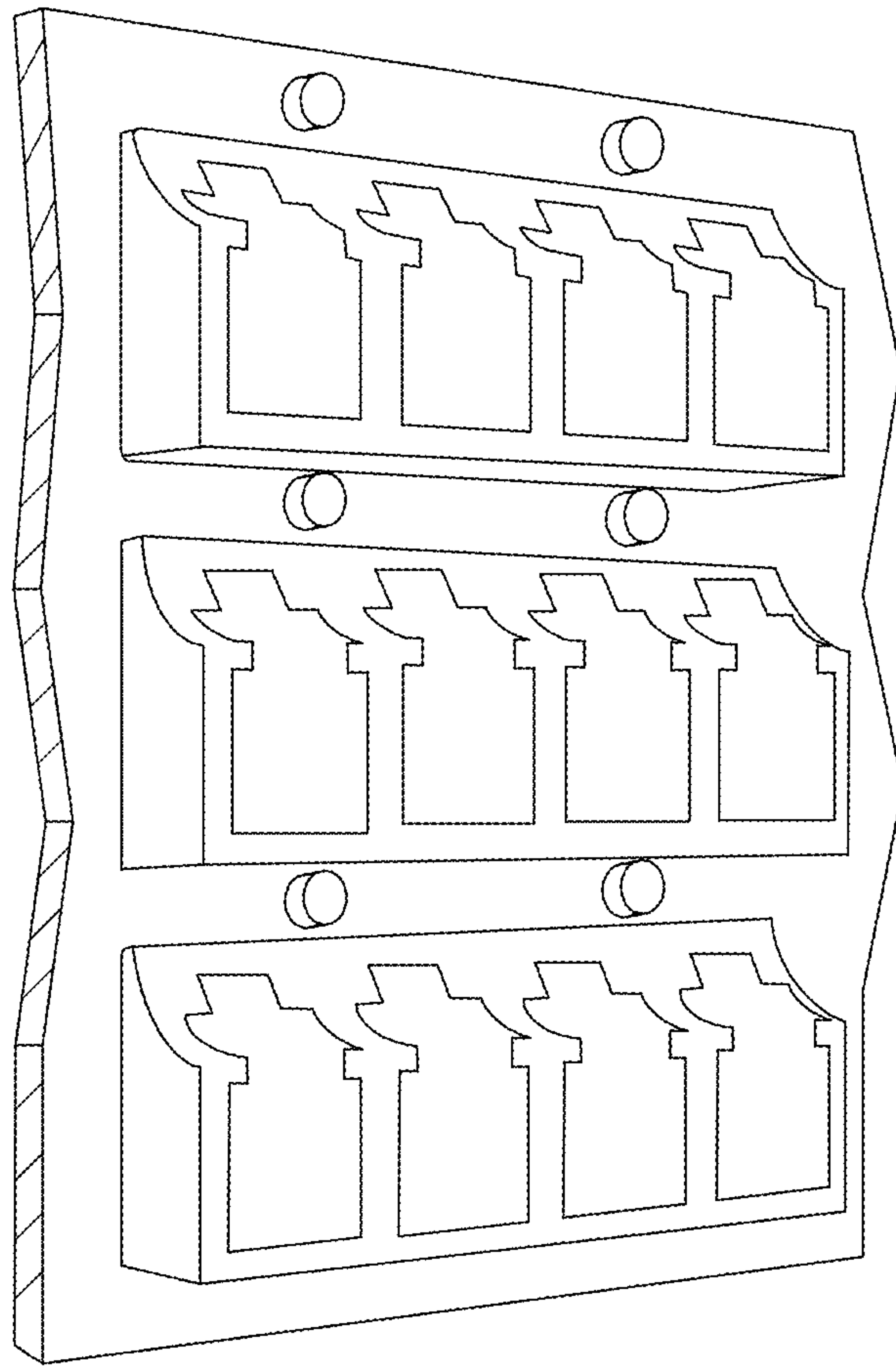


FIG. 5