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(12) **United States Design Patent**  
**Papakos et al.**

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(54) **FRONT PANEL FOR A CIRCUIT CARD**

D413,305 S \* 8/1999 West ..... D13/155

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\* cited by examiner

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(57) **CLAIM**

(\*\*) Term: **15 Years**

The ornamental design for a front panel for a circuit card, as shown and described.

(21) Appl. No.: **29/538,091**

**DESCRIPTION**

(22) Filed: **Sep. 1, 2015**

(51) **LOC (10) Cl.** ..... **13-03**

(52) **U.S. Cl.**  
USPC ..... **D13/154**

(58) **Field of Classification Search**  
USPC ..... D13/133, 146, 147, 154, 155, 156, 184,  
D13/199  
CPC ... G02B 6/12; G02B 6/26; G02B 6/36; G02B  
6/42; G02B 6/43; G02B 6/44; H01R  
13/62  
See application file for complete search history.

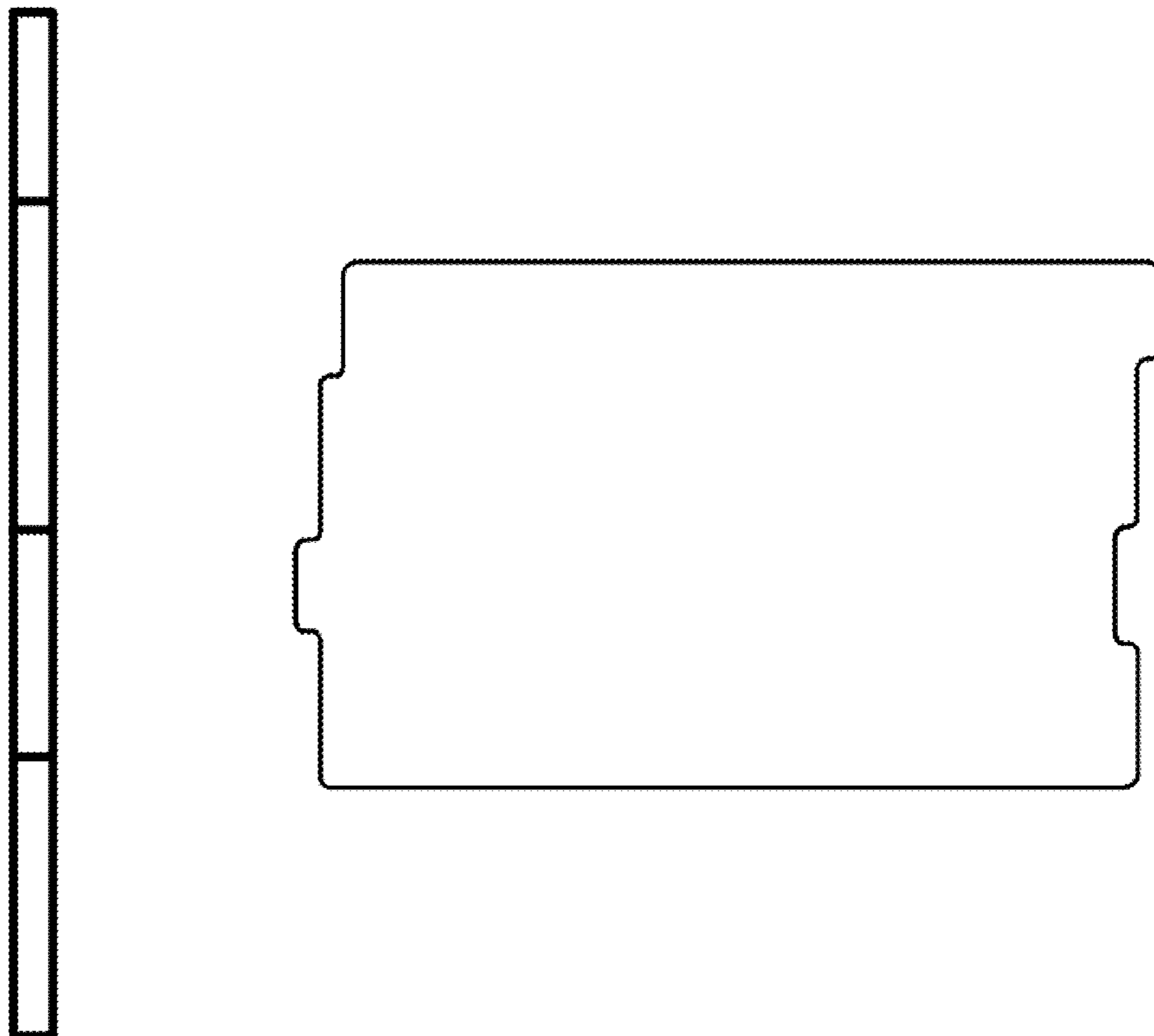
FIG. 1 is a front elevation view of a front panel for a circuit card, in indeterminate length, showing our new design; FIG. 2 is a top plan view thereof; FIG. 3 is a bottom plan view thereof; FIG. 4 is a left side elevation view thereof; FIG. 5 is a right side elevation view thereof; FIG. 6 is a front elevation view of a front panel for a circuit card, in a deterministic size, showing our new design; and, FIG. 7 is a front elevation view thereof, in use condition. The broken line showing of unclaimed portions of the front panel for a circuit card is for the purposes of illustrating environment structure and forms no part of the claimed design. The break lines showing in FIGS. 1, 2 and 3 represents indeterminate length, and portions of the article between the break lines forms no part of the claimed design.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

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**1 Claim, 2 Drawing Sheets**



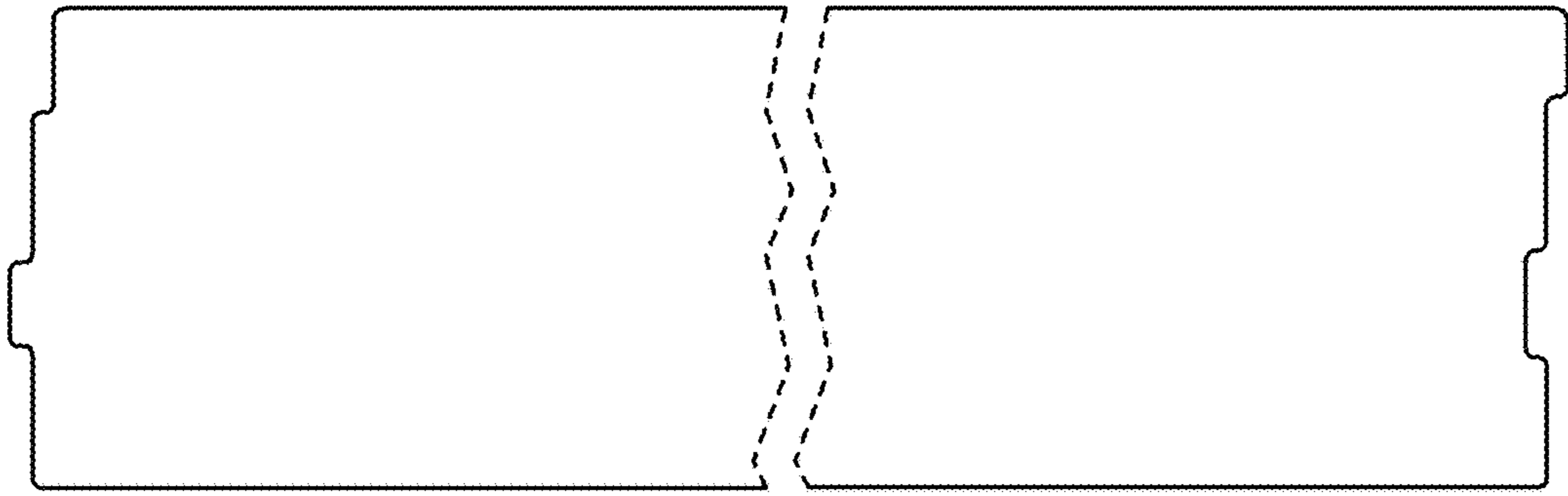


FIG. 1



FIG. 2



FIG. 3



FIG. 4



FIG. 5

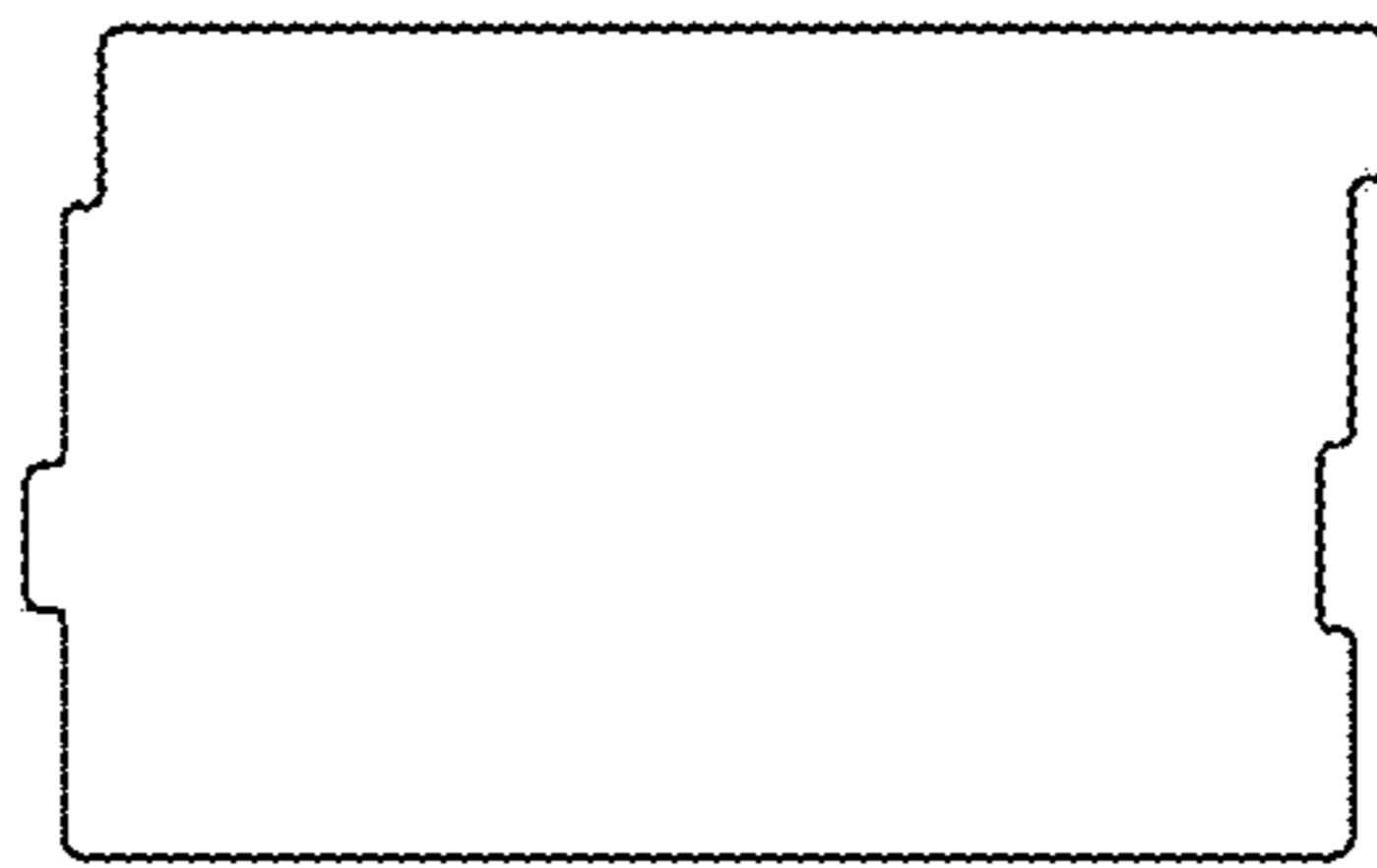


FIG. 6

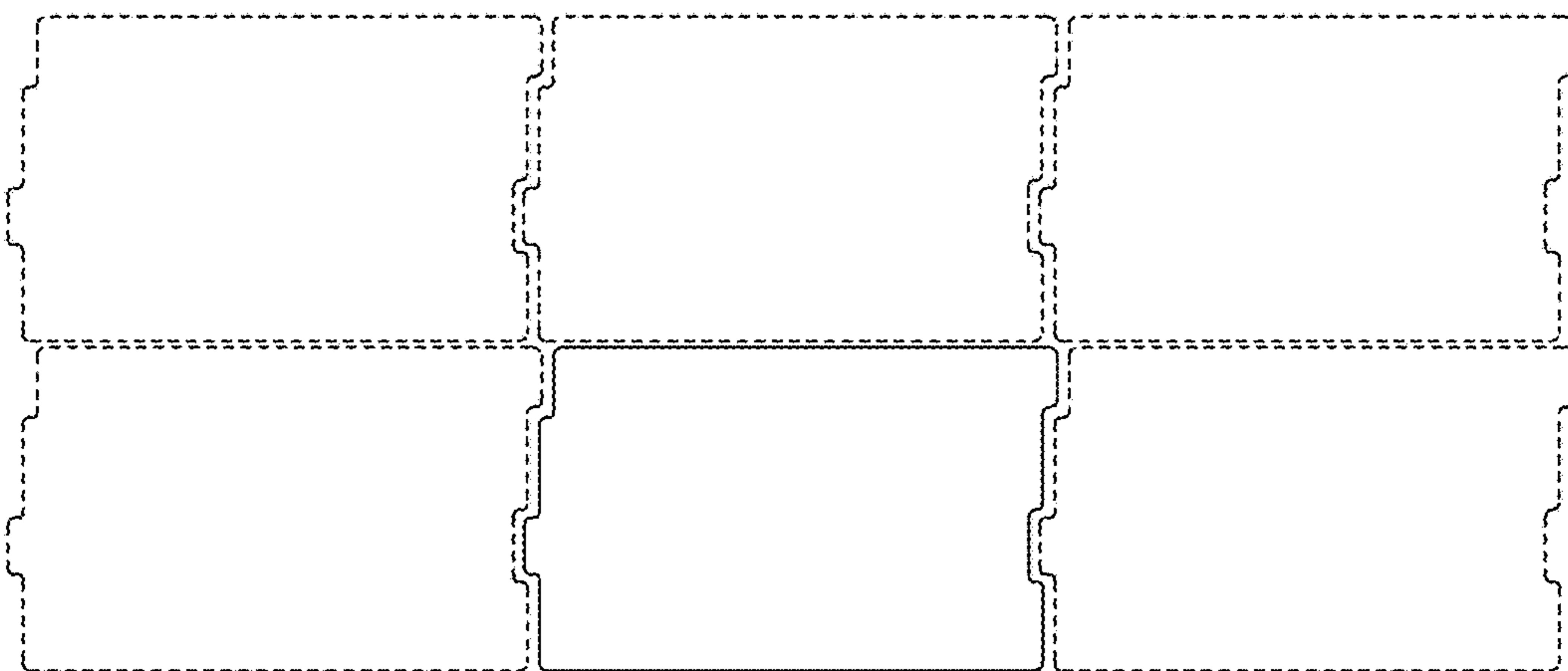


FIG. 7