

US00D788706S

(12) **United States Design Patent** (10) **Patent No.:** **US D788,706 S**
Tateno et al. (45) **Date of Patent:** **** Jun. 6, 2017**

(54) **HEAT INSULATING UNIT FOR SUBSTRATE PROCESSING APPARATUS**

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(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

Aug. 21, 2015 (JP) 2015-018408

(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/129; D13/184**

(58) **Field of Classification Search**
USPC D13/118, 129, 182, 179, 133, 154, 184;
D15/5, 127, 128, 140; 76/107.1
CPC C23C 16/44; C23C 16/4557; H01L 21/00;
H01L 21/02; H01L 21/28008; H01L
21/70; H01L 23/544
See application file for complete search history.

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(57) **CLAIM**

We claim the ornamental design for a heat insulating unit for substrate processing apparatus, as shown (and described).

DESCRIPTION

FIG. 1 is a front, top and right side perspective view of a heat insulating unit for substrate processing apparatus showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

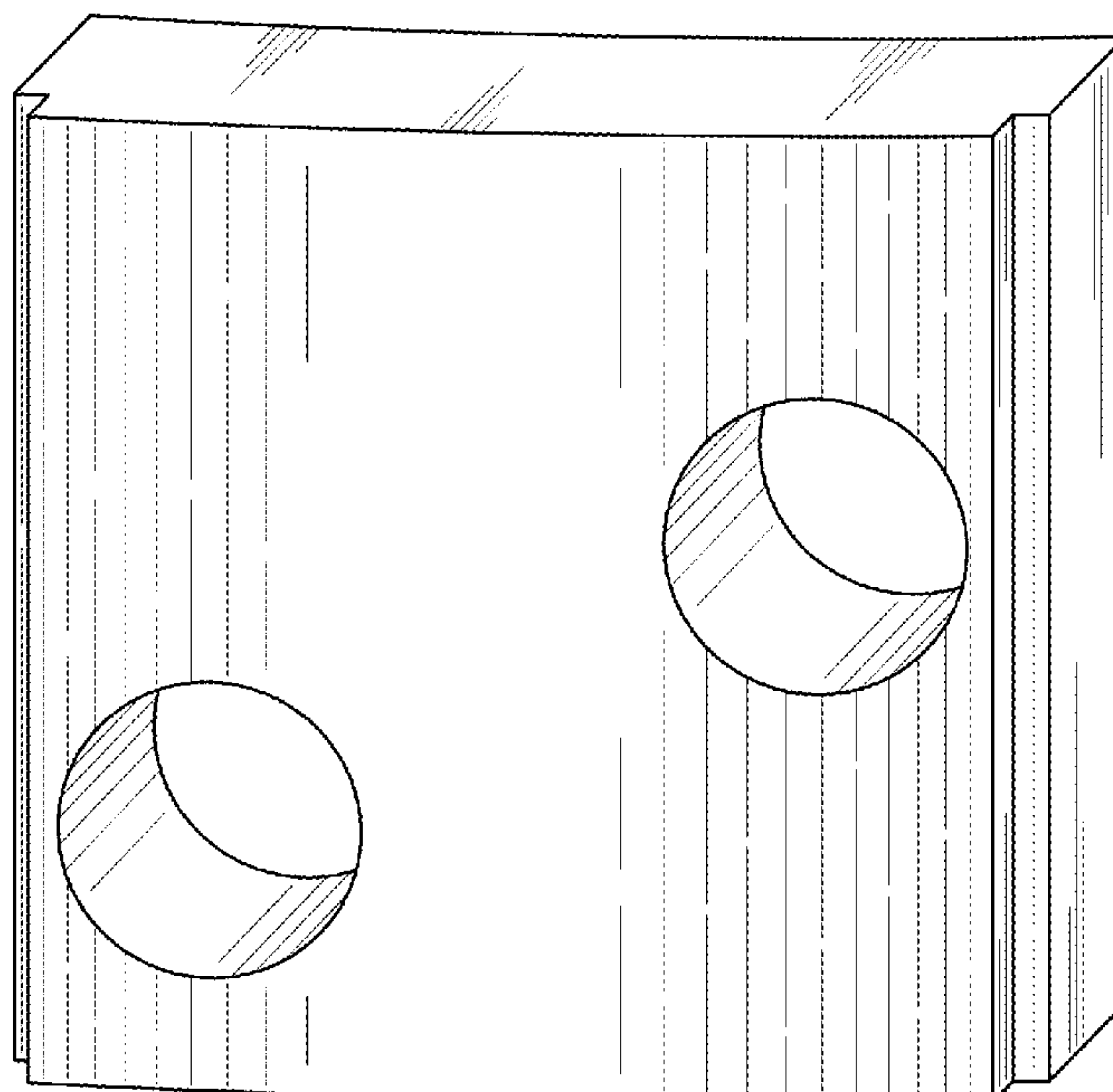
FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view.

1 Claim, 4 Drawing Sheets



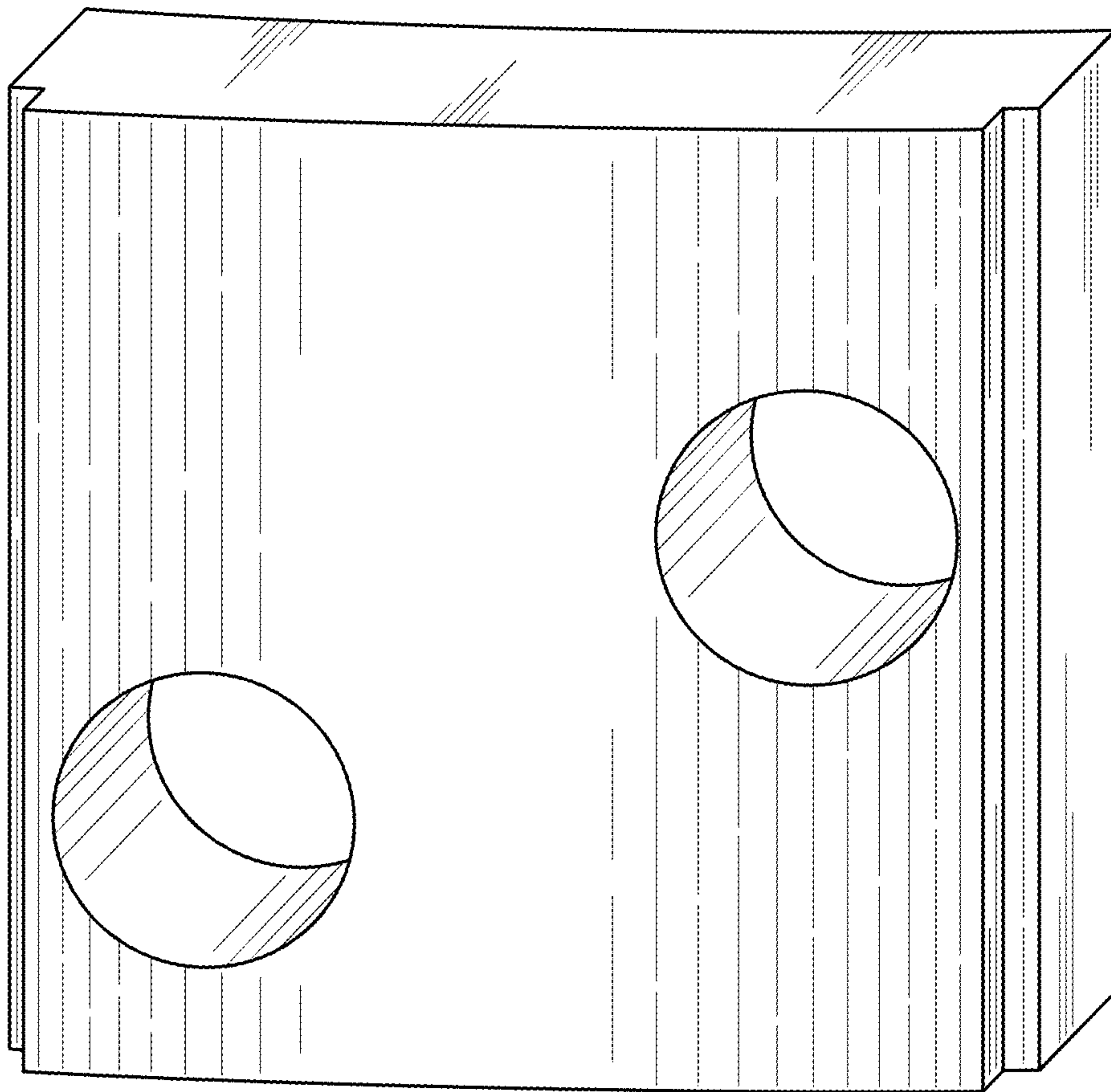


FIG. 1

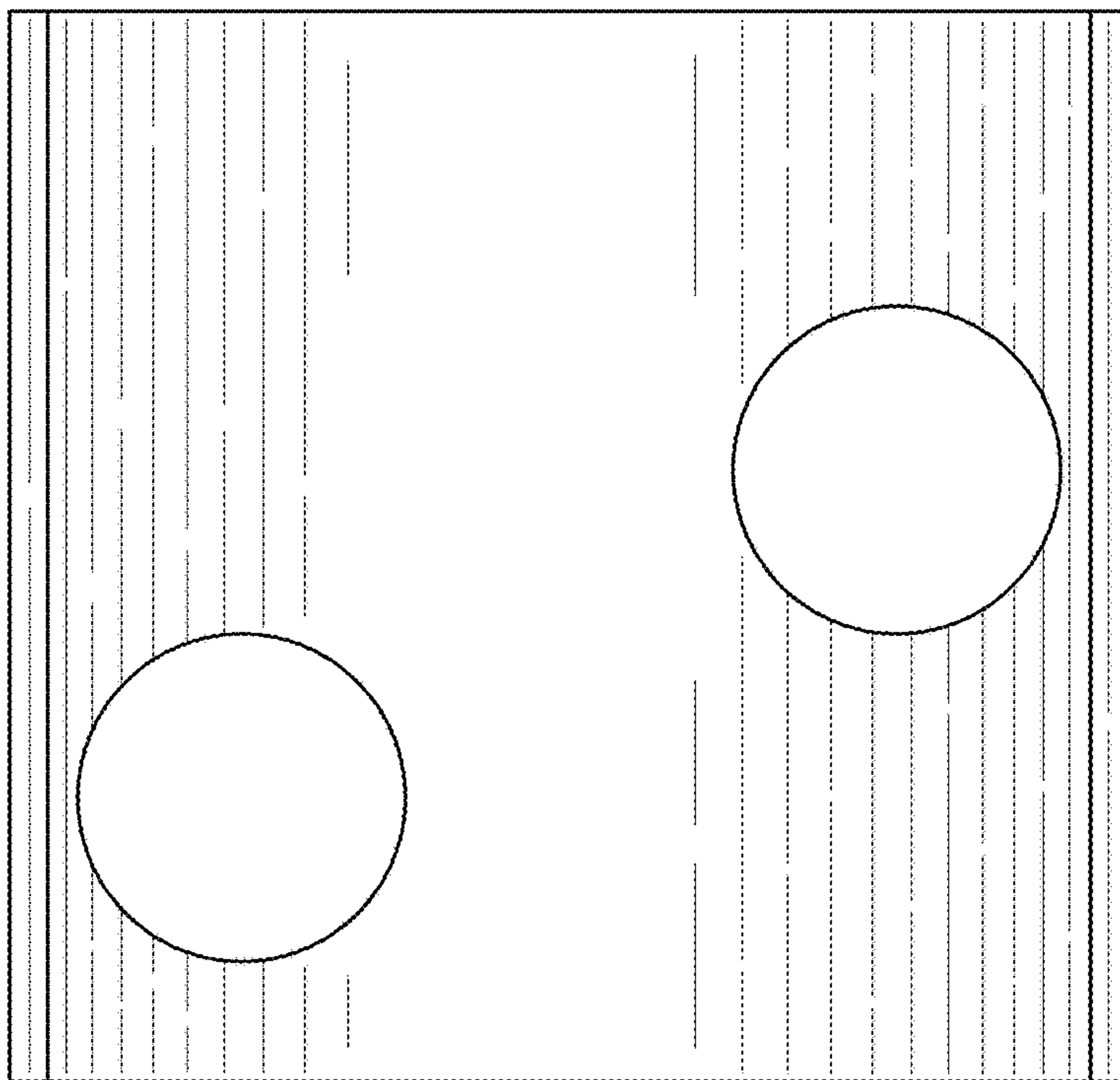


FIG. 2

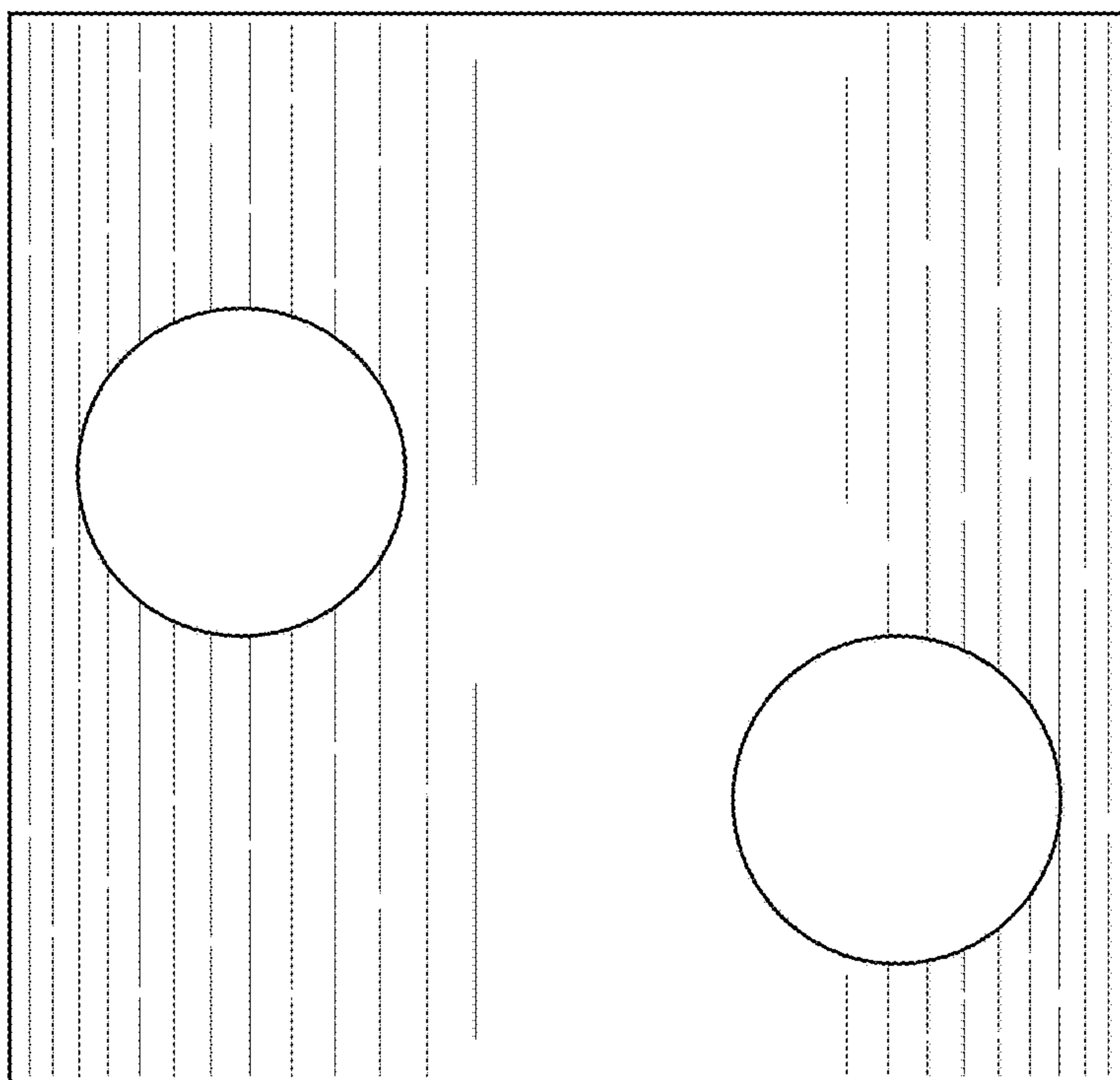


FIG. 3

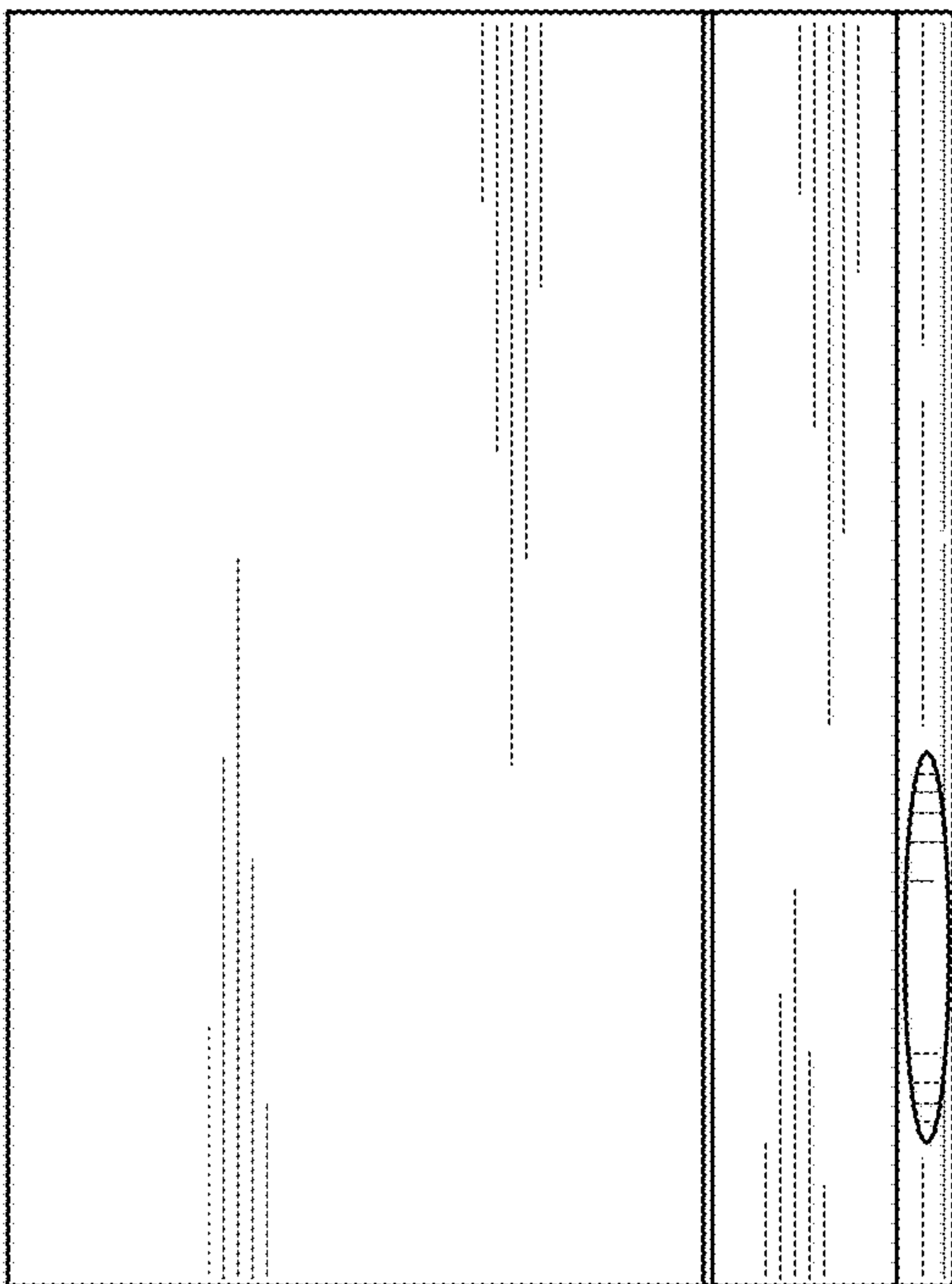


FIG. 4

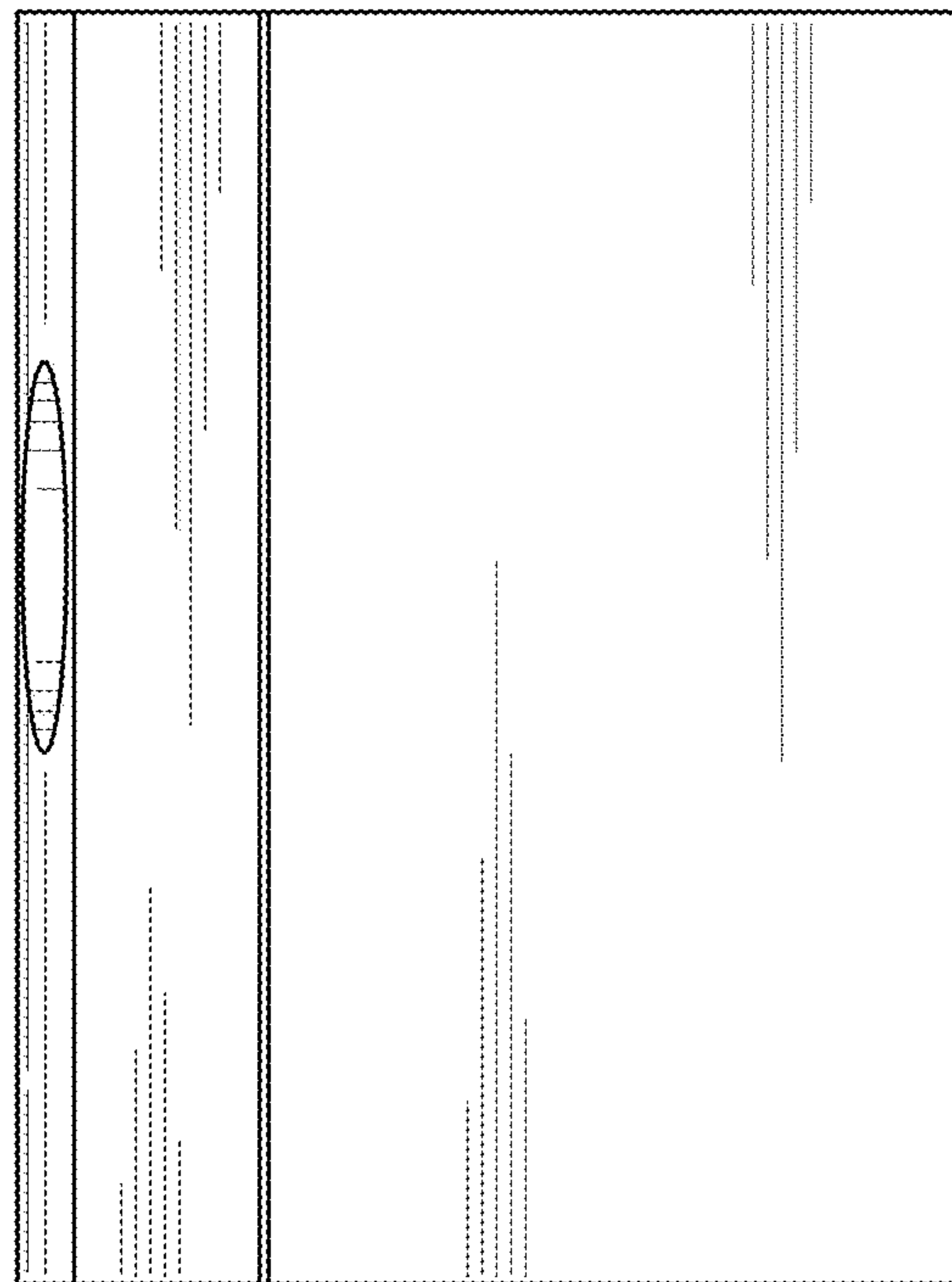


FIG. 5

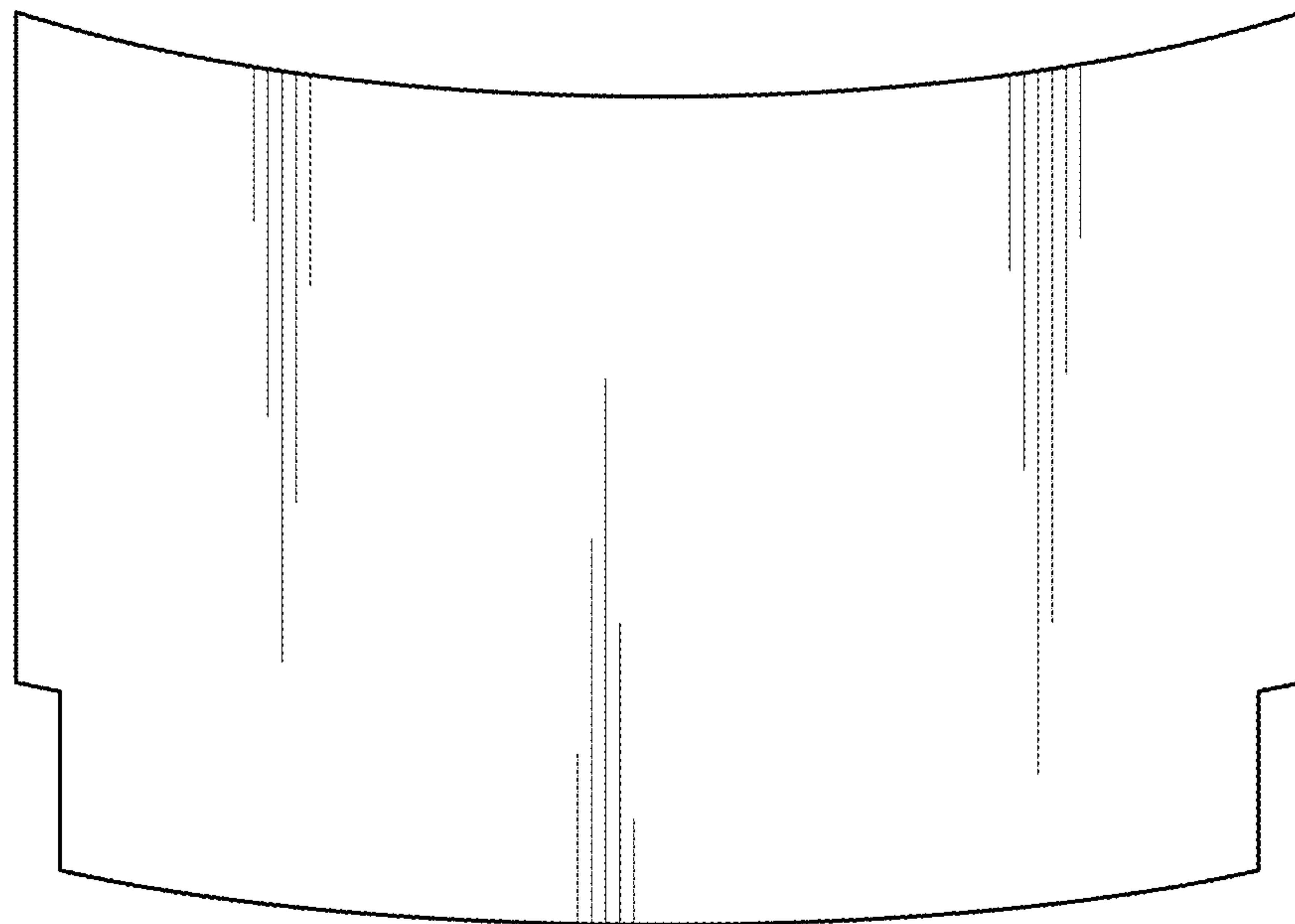


FIG. 6

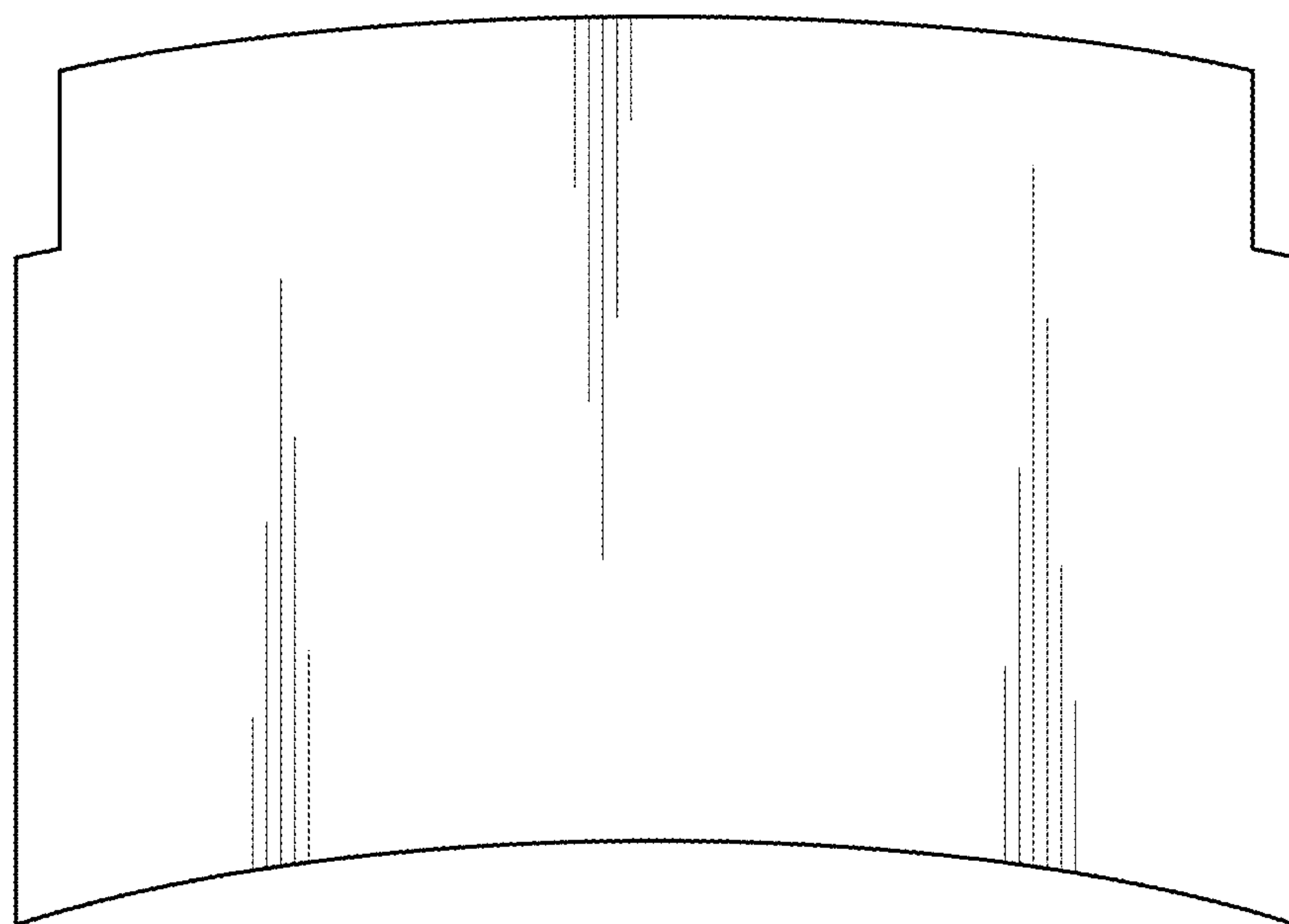


FIG. 7