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(12) **United States Design Patent** (10) **Patent No.:** **US D787,489 S**
Lee et al. (45) **Date of Patent:** **** May 23, 2017**

(54) **ELECTRONIC DEVICE**

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(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/248**

(58) **Field of Classification Search**
USPC D14/138 AA, 138 AB, 138 AD, 138 AC,
D14/138 C, 138 G, 138 R, 248, 250, 253,
D14/341, 440; D3/215, 218
CPC H05K 5/0217; H04M 1/03; H04B 1/3888;
H04N 5/2252; A45C 2011/002
See application file for complete search history.

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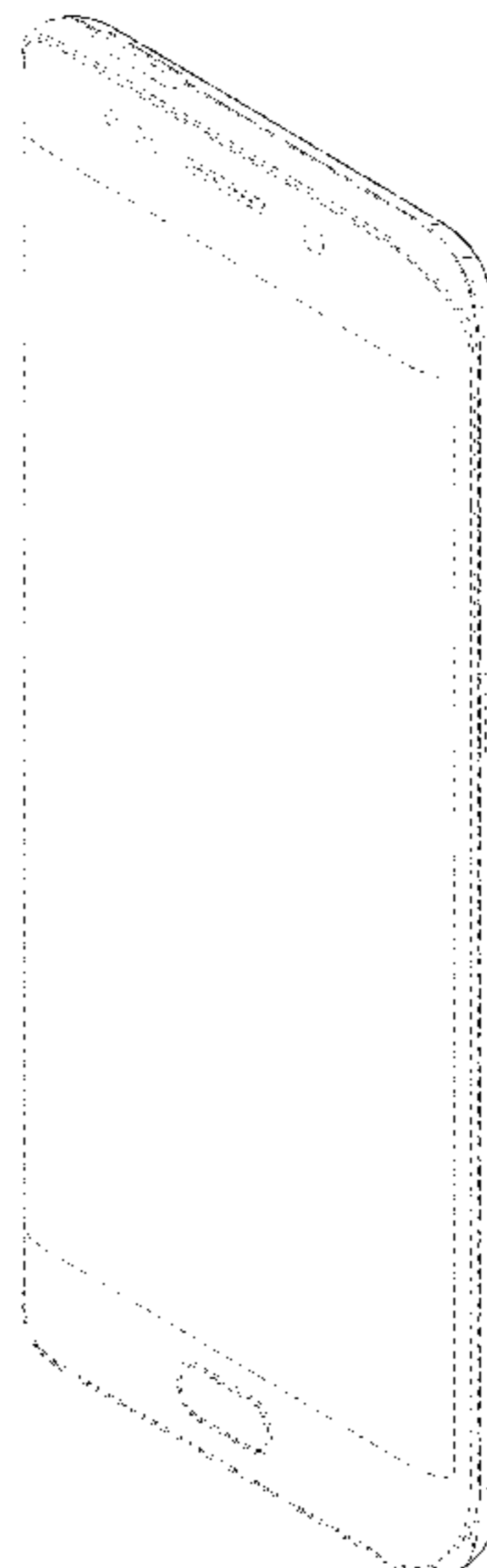
(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic device showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left-side elevation view thereof;
FIG. 5 is a right-side elevation view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof;
FIG. 8 is a rear perspective view thereof;
FIG. 9 is a cross-section view taken along line 9-9 of FIG. 3; and,
FIG. 10 is an enlarged view of the area encircled in FIG. 9. The dashed broken lines depict portions of the electronic device that form no part of the claimed design. The dot-dash broken lines represent an unclaimed boundary line, and form no part of the claimed design. The dot-dot-dash broken lines represent an area illustrated in an enlargement and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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FIG.1

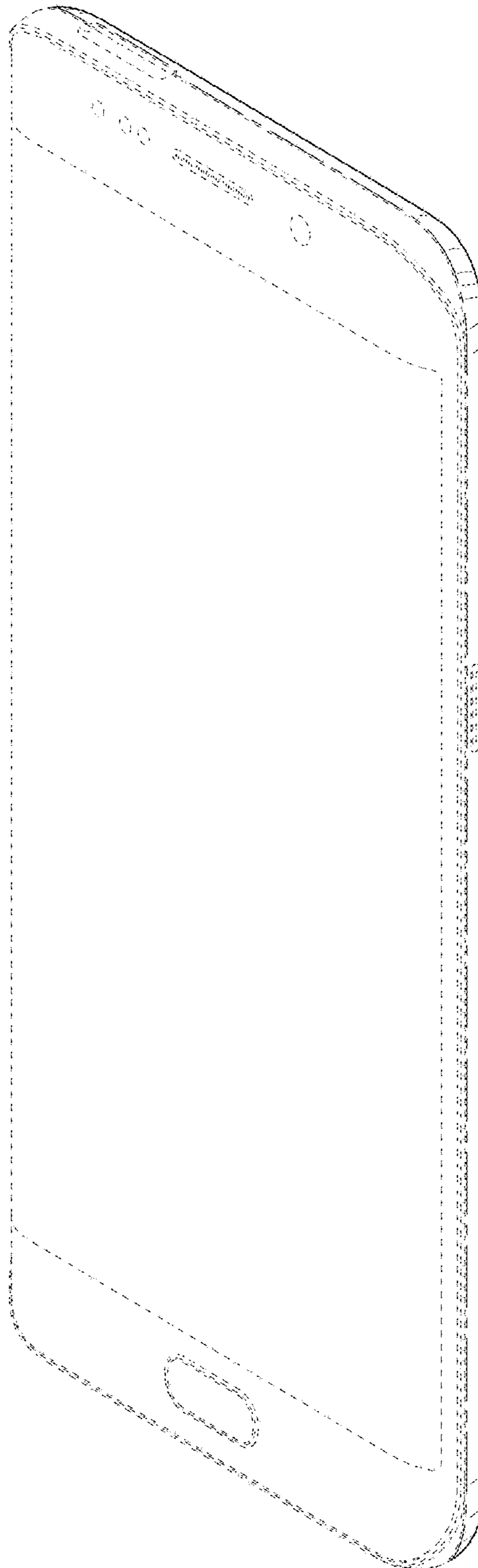


FIG.2

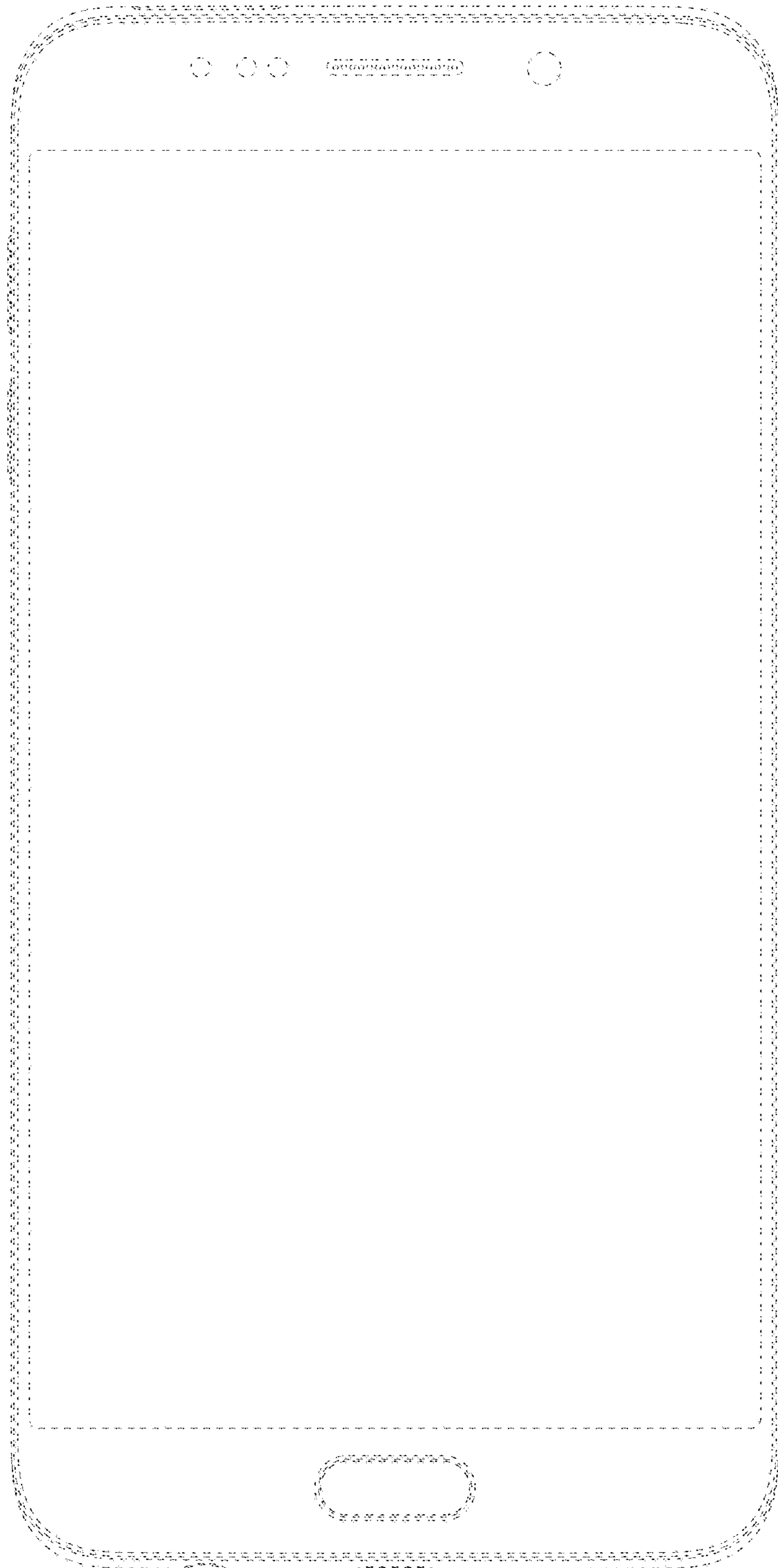


FIG.3

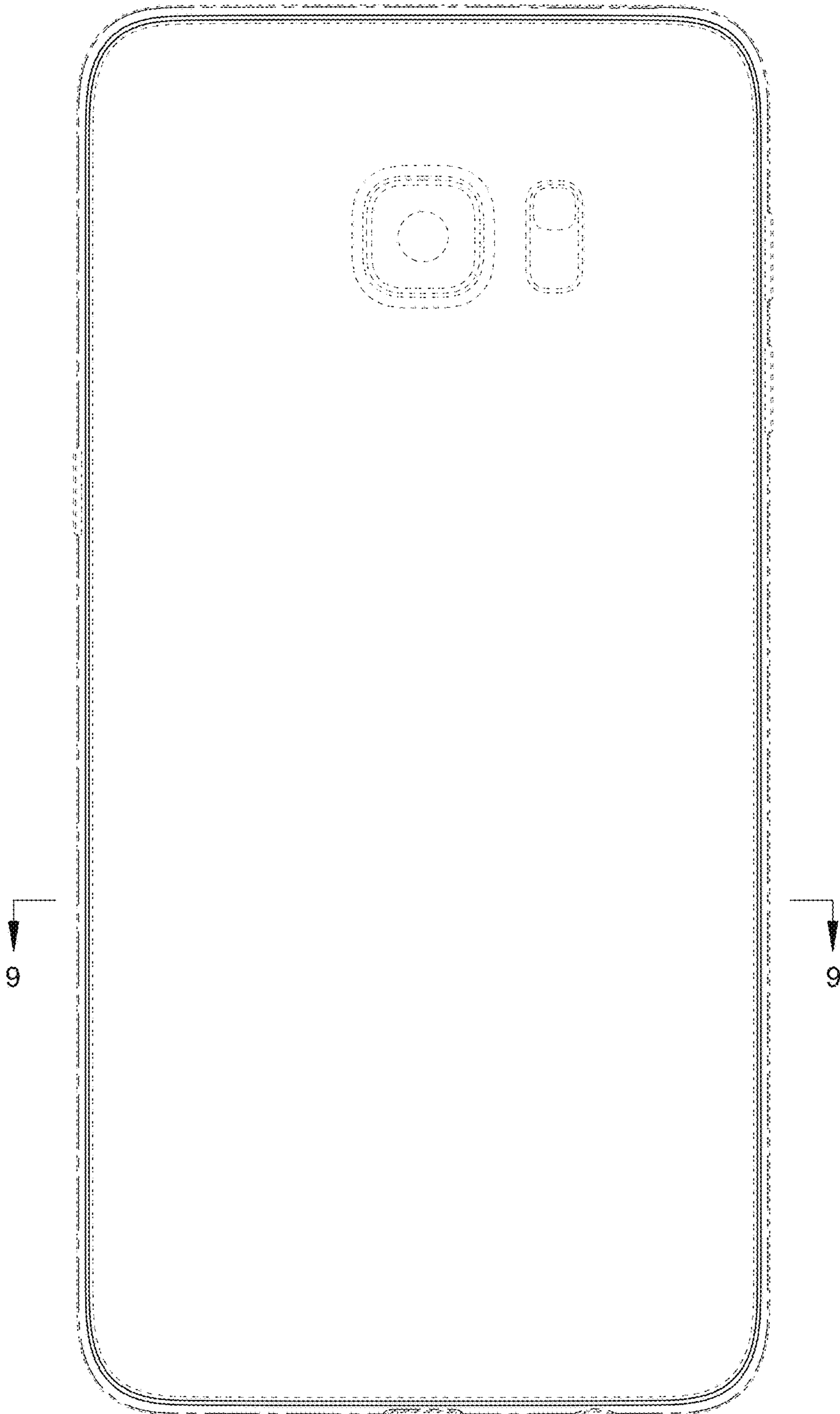


FIG.4

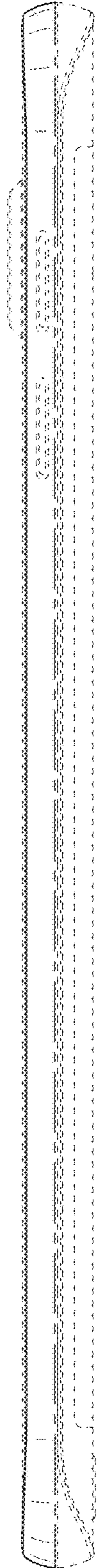


FIG.5

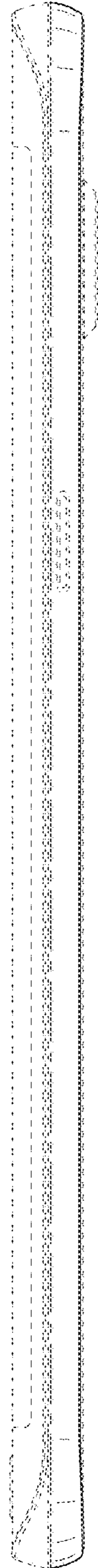


FIG.6

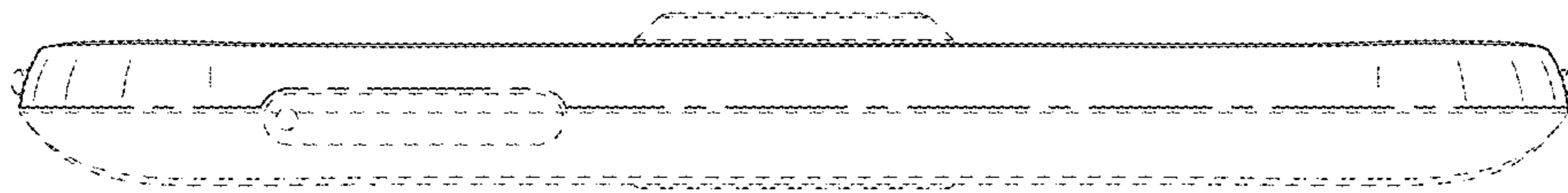


FIG.7

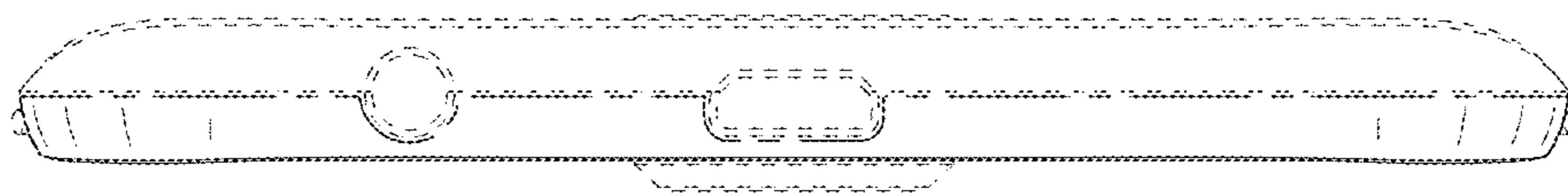


FIG.8

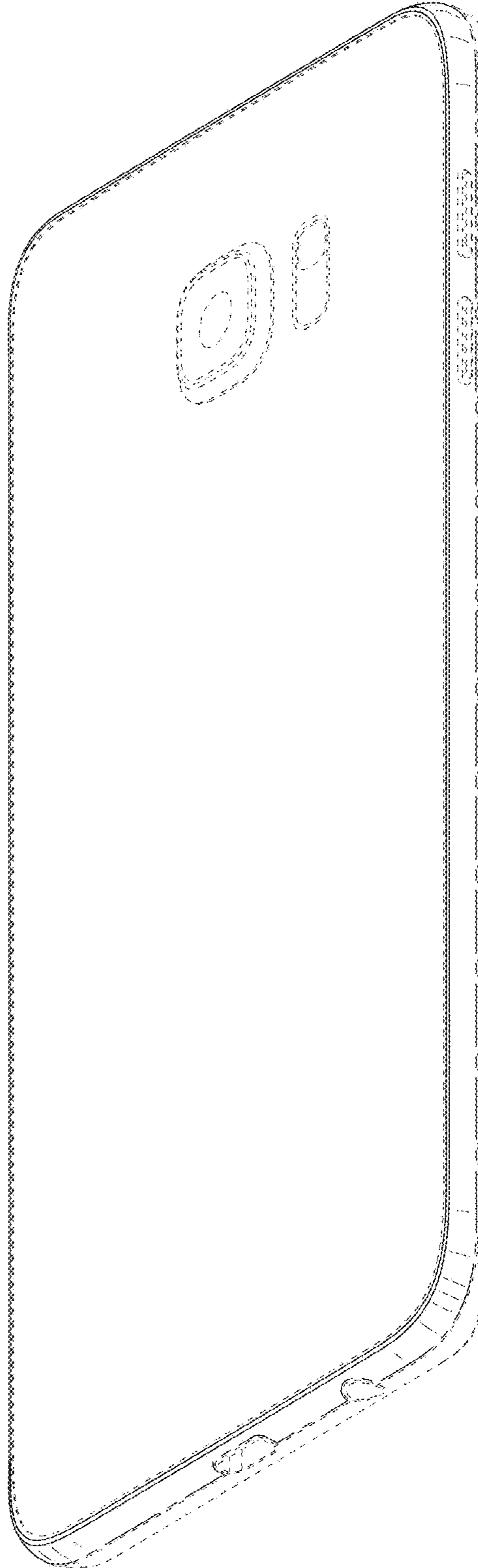


FIG.9



FIG.10

