



US00D784279S

(12) **United States Design Patent**
Lee et al.

(10) **Patent No.:** **US D784,279 S**
(45) **Date of Patent:** **** Apr. 18, 2017**

(54) **ELECTRONIC DEVICE**

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(**) Term: **14 Years**

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(22) Filed: **Mar. 26, 2015**

(30) **Foreign Application Priority Data**

Dec. 31, 2014 (KR) 30-2014-0064579

(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/138 G**

(58) **Field of Classification Search**
USPC D14/138 R, 138 AA, 138 AB, 138 AC,
D14/138 AD, 138 G, 248, 341-347
CPC H04M 1/0202; H04M 1/0235; H04M
1/0237; H04M 1/0239; H04M 2250/52
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a top right front perspective view of a first embodiment of an electronic device showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a rear view thereof;

FIG. 4 is a left-side elevation view thereof;

FIG. 5 is a right-side elevation view thereof;

FIG. 6 is a top plan view thereof;

FIG. 7 is a bottom plan view thereof;

FIG. 8 is a bottom left rear perspective view thereof;

FIG. 9 is a top right front perspective view of a second embodiment of an electronic device showing our new design;

FIG. 10 is a front view thereof;

FIG. 11 is a rear view thereof;

FIG. 12 is a left-side elevation view thereof;

FIG. 13 is a right-side elevation view thereof;

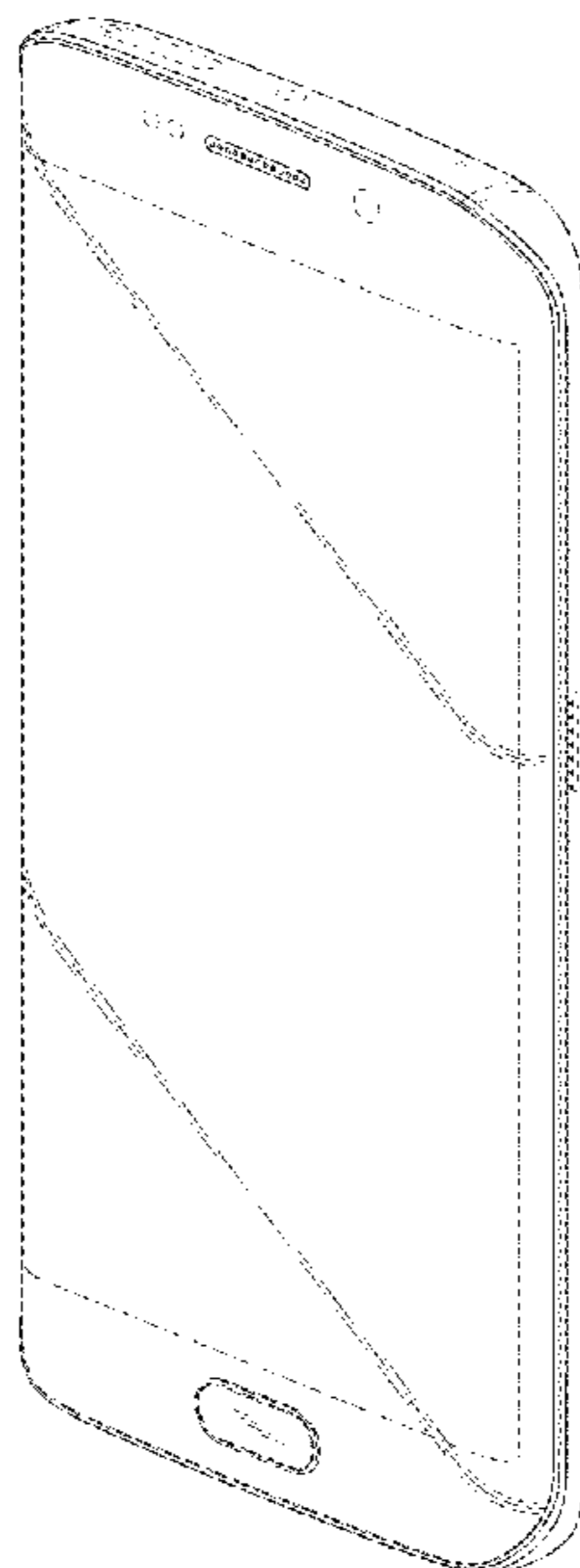
FIG. 14 is a top plan view thereof;

FIG. 15 is a bottom plan view thereof; and,

FIG. 16 is a bottom left rear perspective view thereof.

The broken lines are for environmental purposes only and form no part of the claimed design.

1 Claim, 12 Drawing Sheets



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FIG. 1

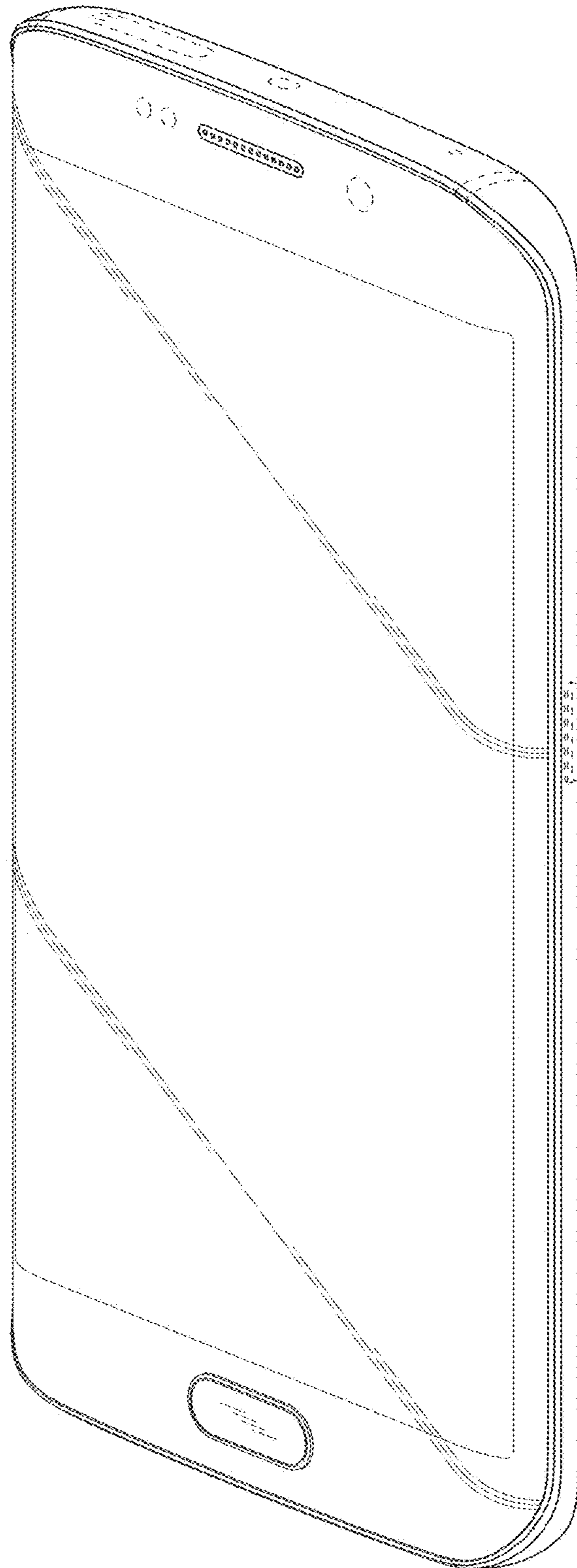


FIG. 2

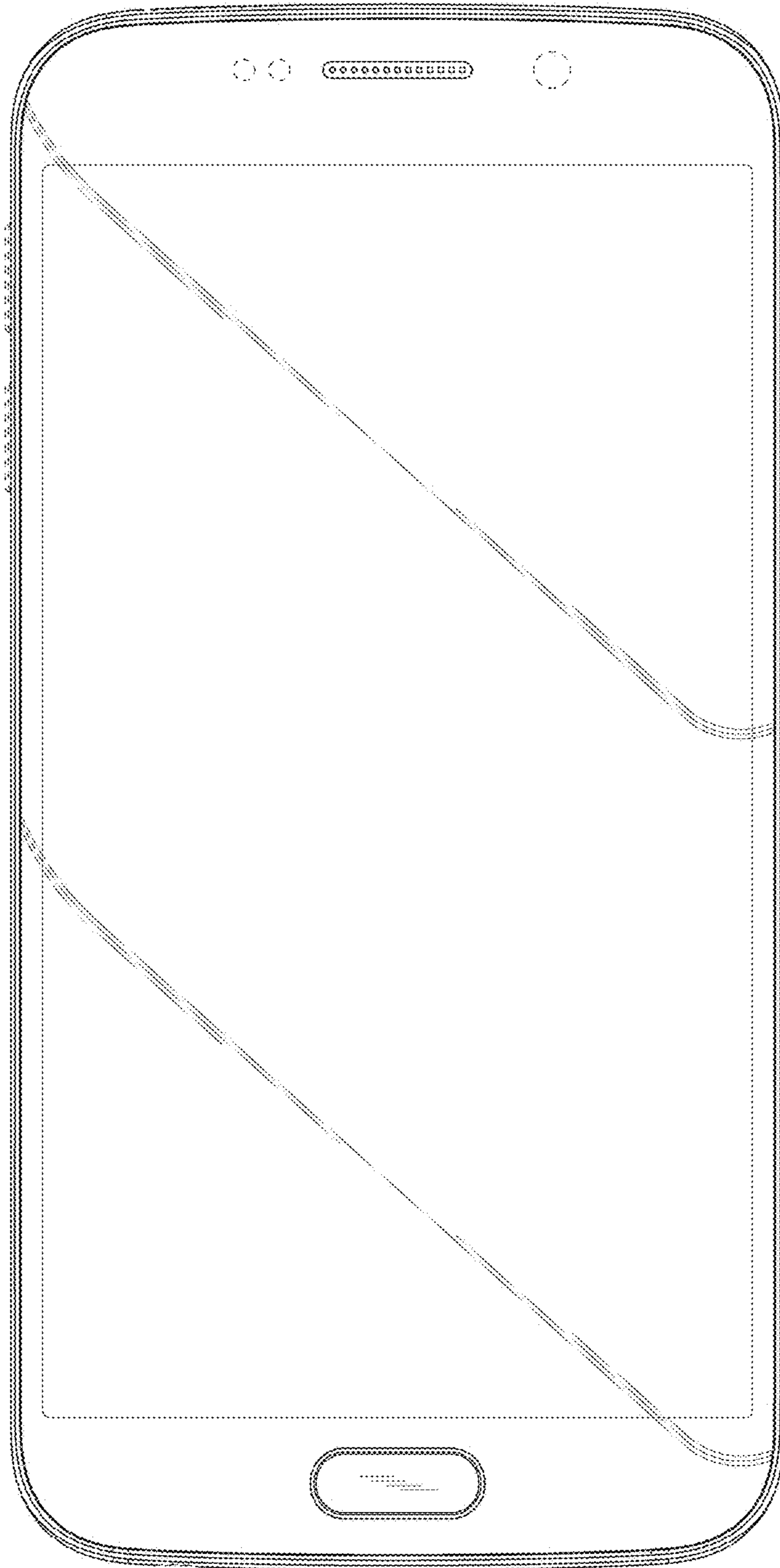


FIG. 3

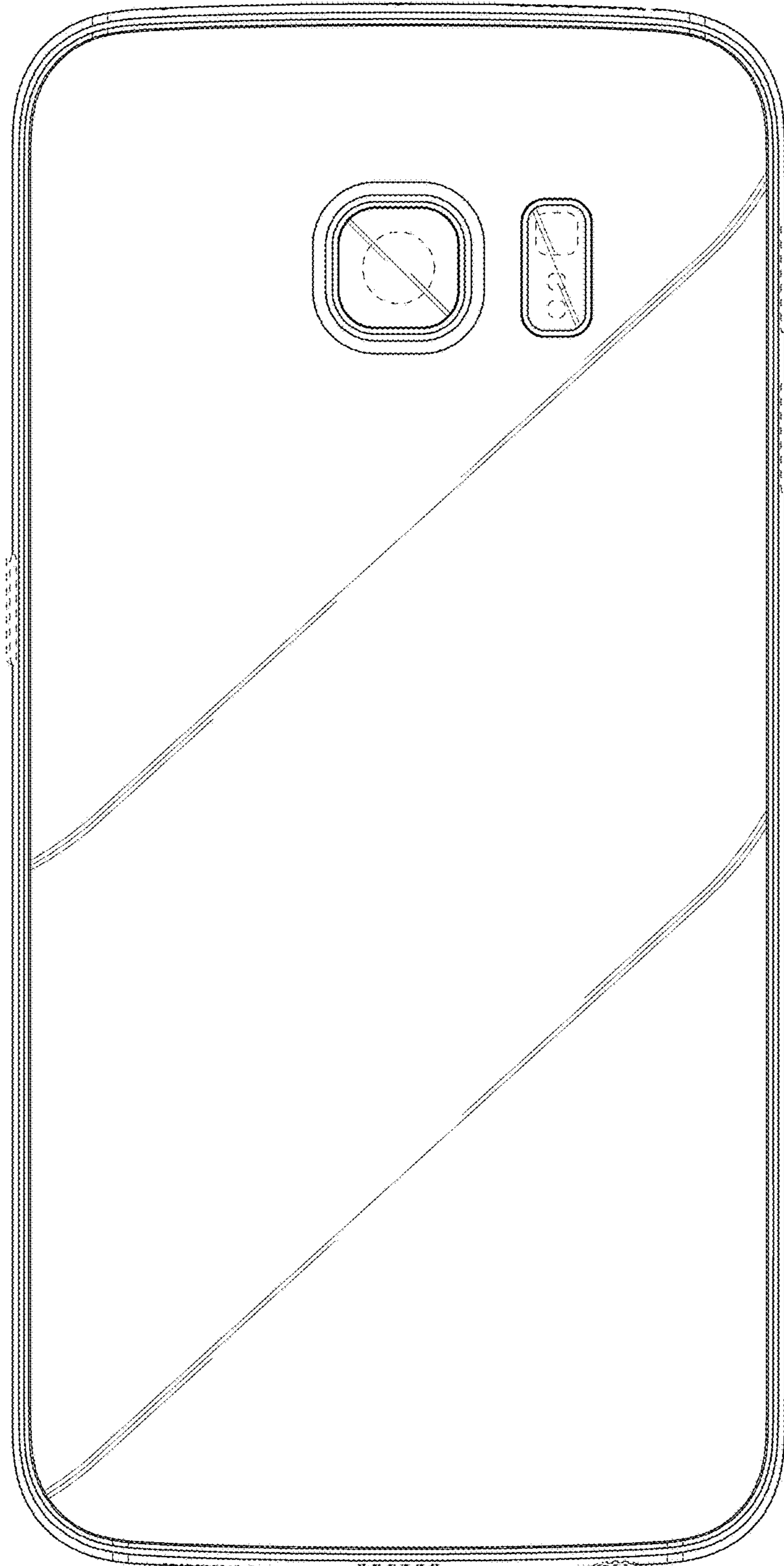


FIG. 4

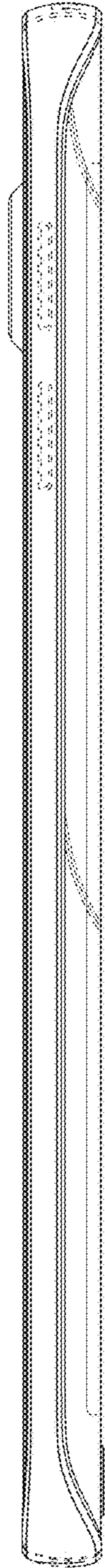


FIG. 5

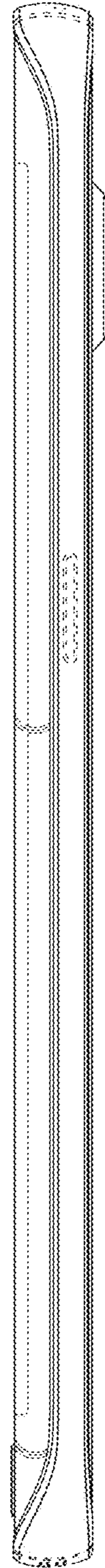


FIG. 6



FIG. 7



FIG. 8

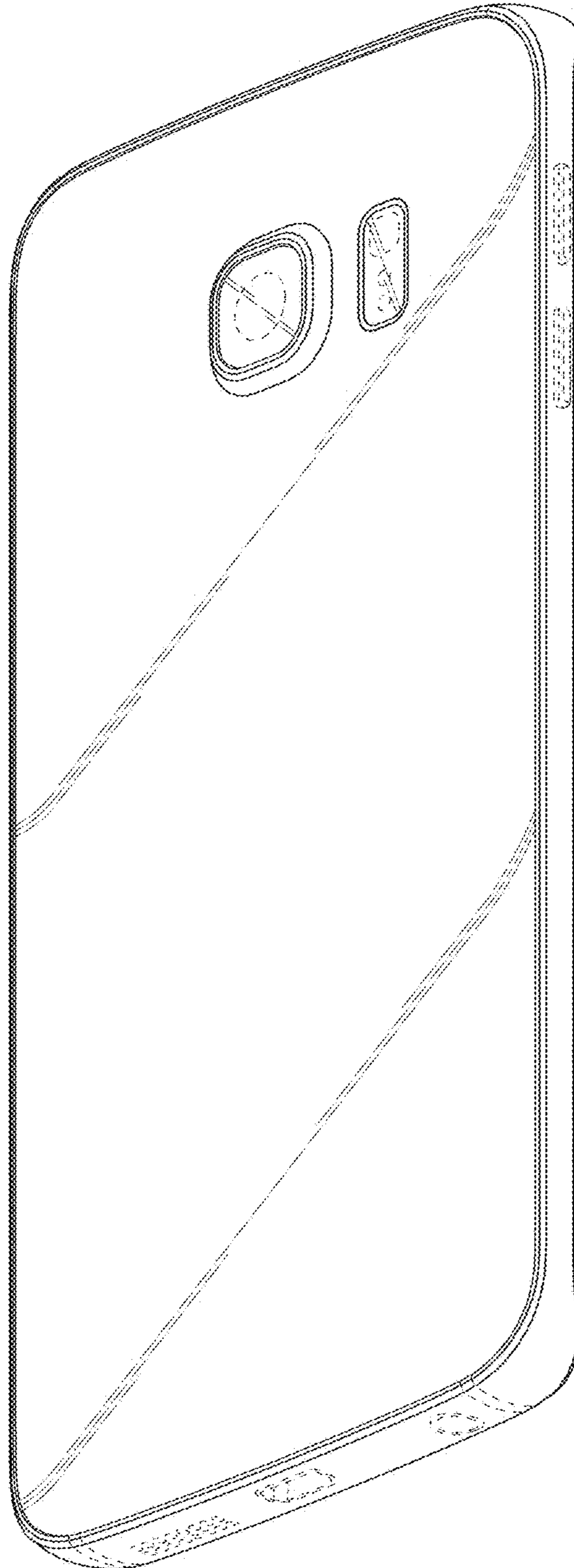


FIG. 9

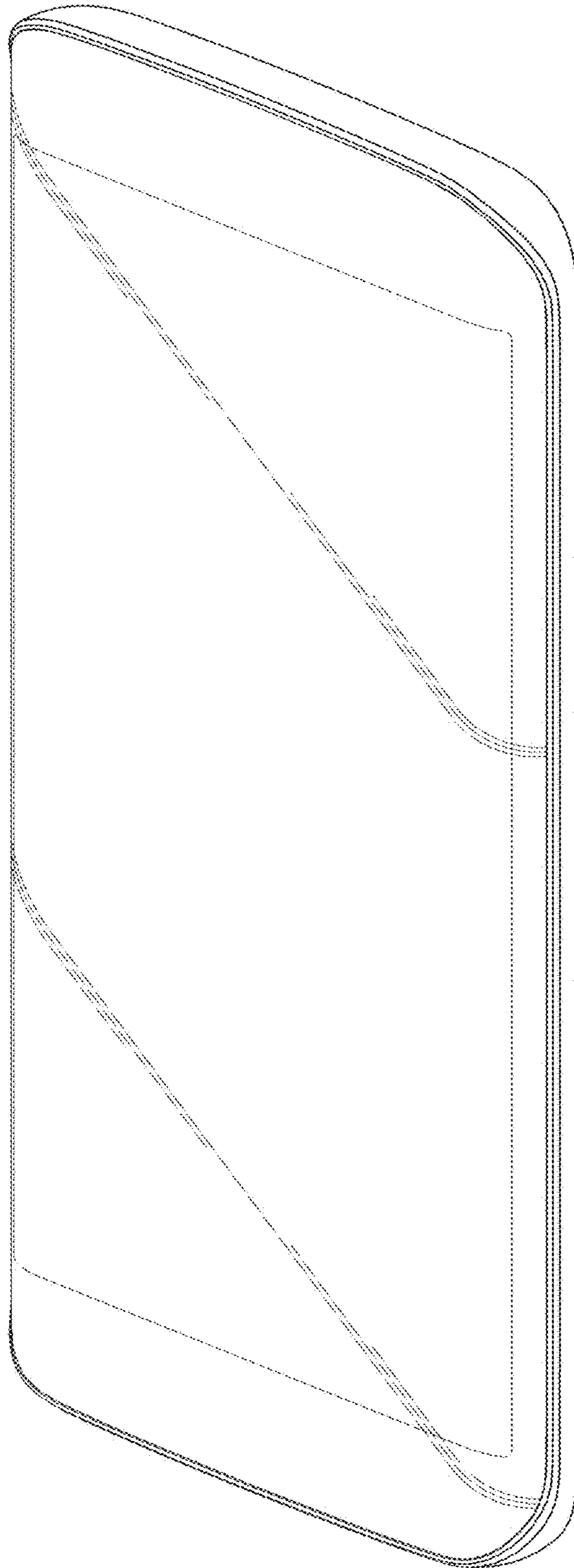


FIG. 10

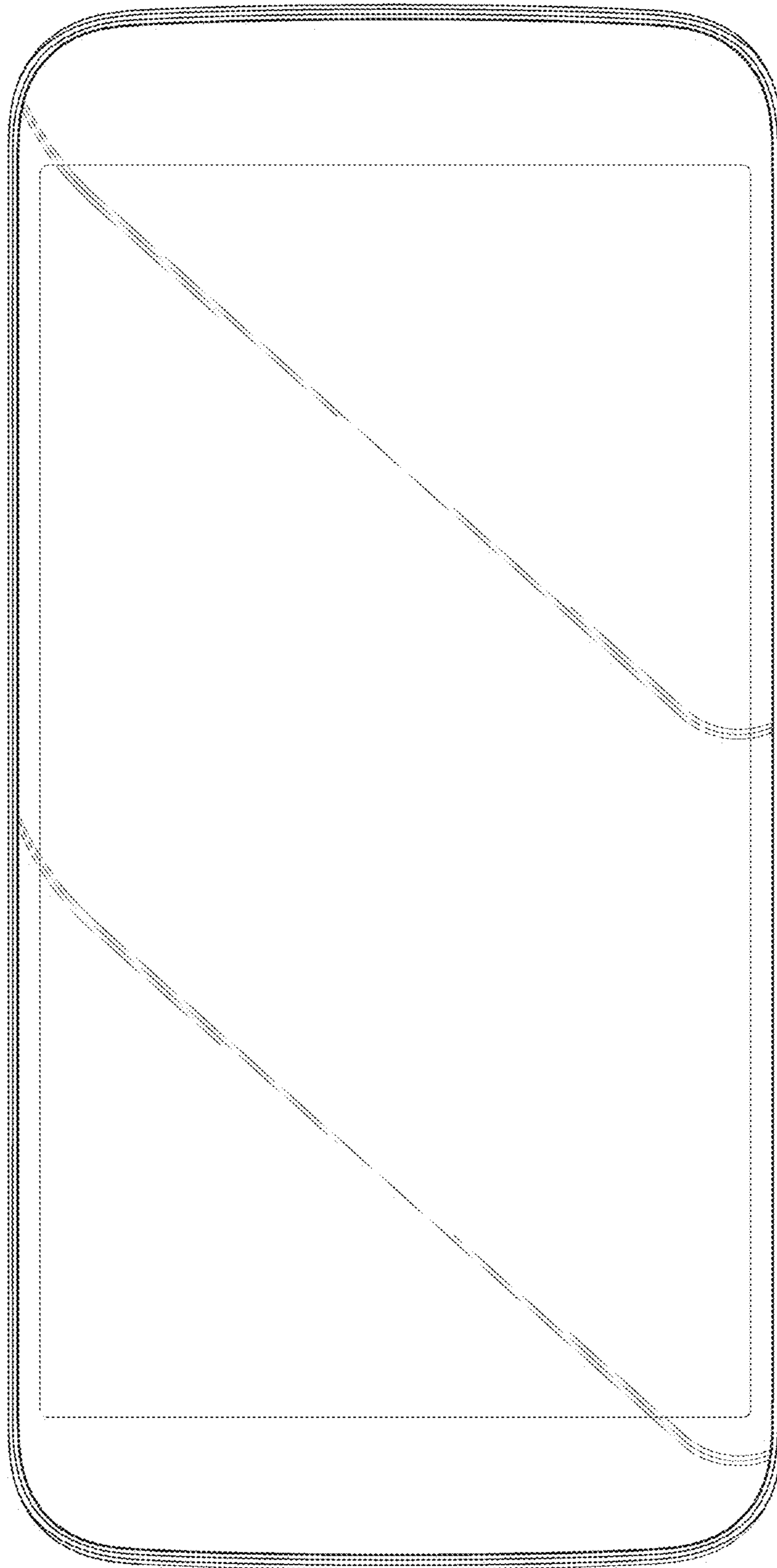


FIG. 11

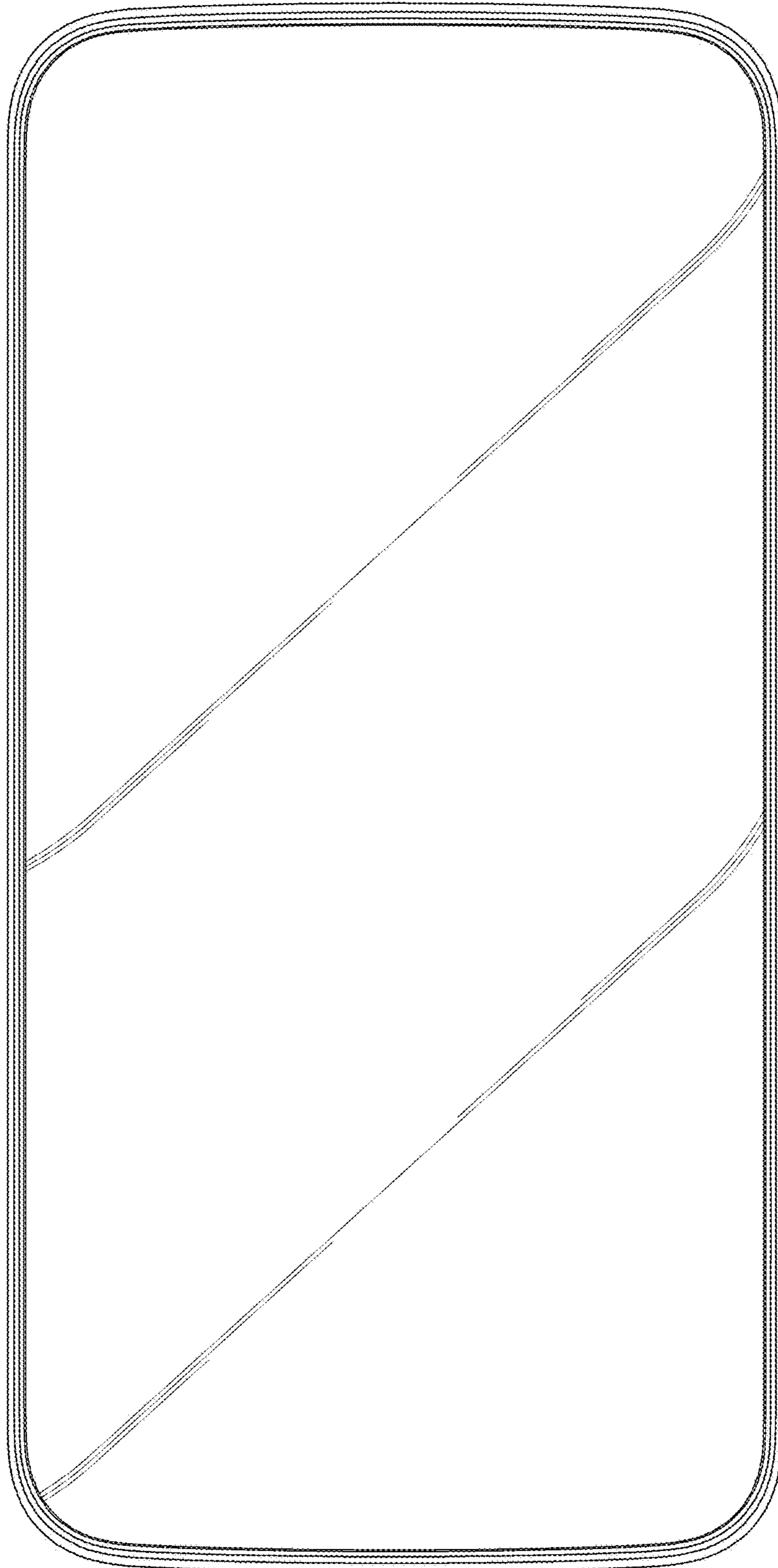


FIG. 12

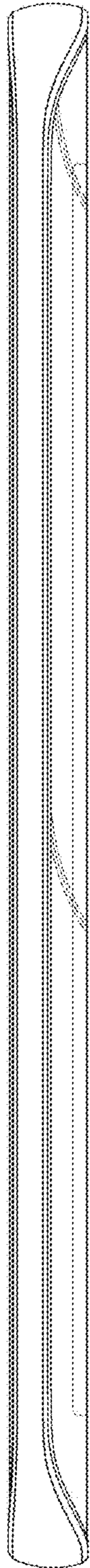


FIG. 13

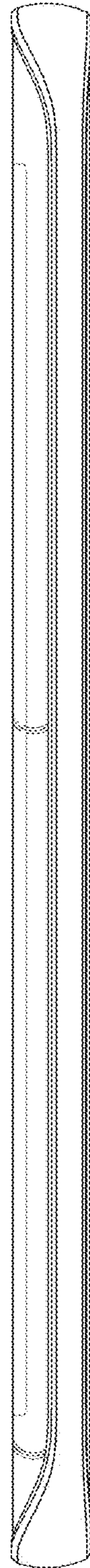


FIG. 14



FIG. 15



FIG. 16

