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(12) **United States Design Patent** (10) **Patent No.:** **US D783,613 S**
Maus et al. (45) **Date of Patent:** **** Apr. 11, 2017**

(54) **UNIVERSAL SERIAL BUS DATA KEY WITH CHIP INTERFACE**

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D718,307 S * 11/2014 Ozolins D14/383

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(Continued)

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Primary Examiner — Austin Murphy

(**) Term: **14 Years**

(74) *Attorney, Agent, or Firm* — Mehrman Law Office; Michael J. Mehrman

(21) Appl. No.: **29/518,154**

(57) **CLAIM**

(22) Filed: **Feb. 20, 2015**

The ornamental design for a universal serial bus data key with chip interface, as shown and described herein.

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**

USPC **D14/383**

(58) **Field of Classification Search**

DESCRIPTION

USPC D14/300–302, 313, 314, 341, 348–370, D14/383, 385, 432, 436, 496, 125, 135, D14/134, 155, 167, 168, 230, 231, 233, D14/235, 237, 240–242, 299; D10/65, D10/75, 78; D13/103, 149, 162, 184, D13/199; D18/4.4–4.6, 12.1, 56; 361/679.31–679.45, 752; 711/100, 115; 382/115, 124

CPC G06F 21/32; G06F 21/35; G06F 21/445; G06F 21/6209; G06K 9/00885

See application file for complete search history.

FIG. 1 is a perspective view of a universal serial bus data key with chip interface.
FIG. 2 is a front view of the universal serial bus data key with chip interface.
FIG. 3 is a rear view of the universal serial bus data key with chip interface.
FIG. 4 is a right side view of the universal serial bus data key with chip interface.
FIG. 5 is a left side of the universal serial bus data key with chip interface.
FIG. 6 is a top view of the universal serial bus data key with chip interface; and,
FIG. 7 is a bottom view of the universal serial bus data key with chip interface.

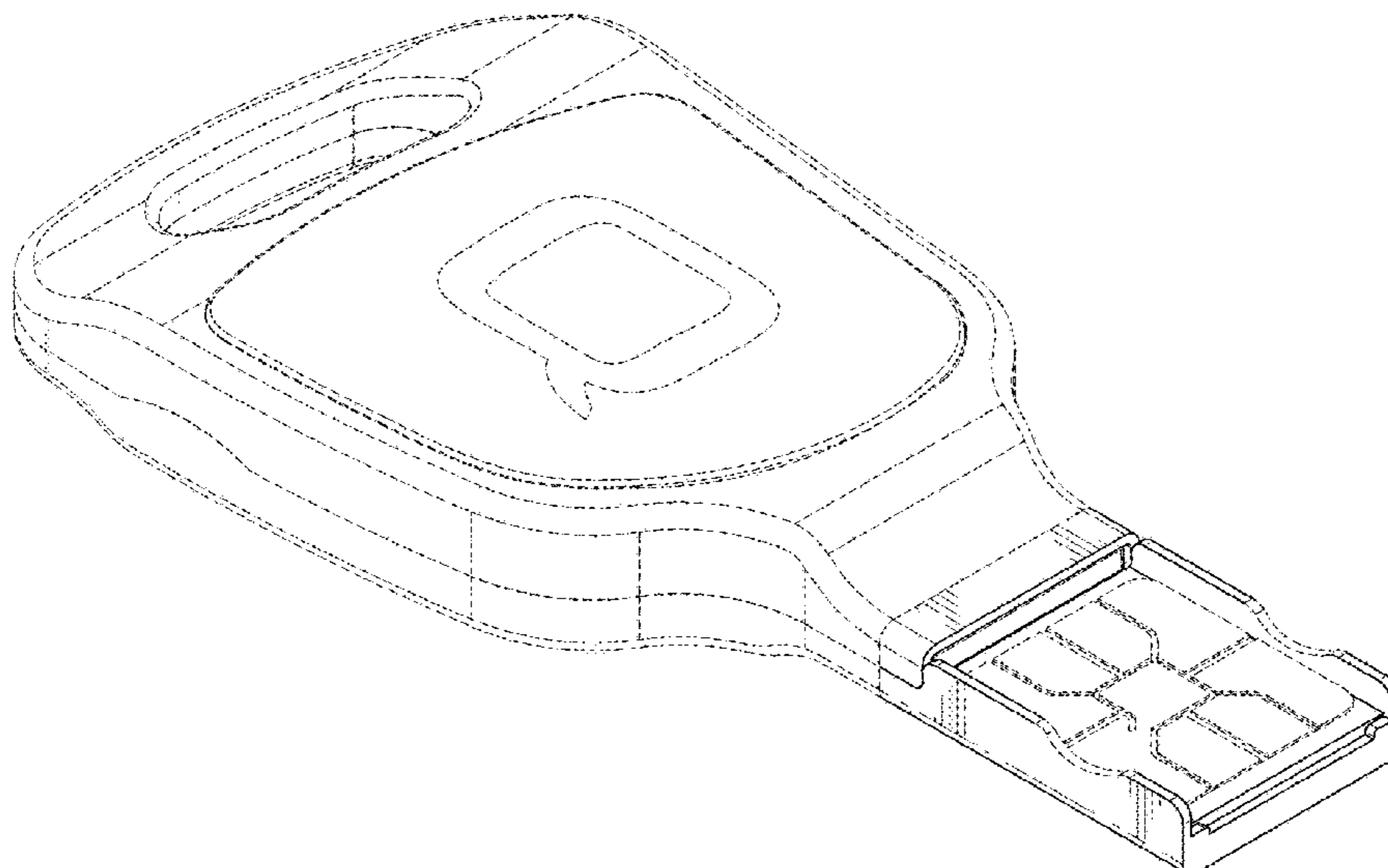
The broken lines shown in the drawings represent portions of the universal serial bus data key with chip interface that form no part of the claimed design.

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1 Claim, 5 Drawing Sheets



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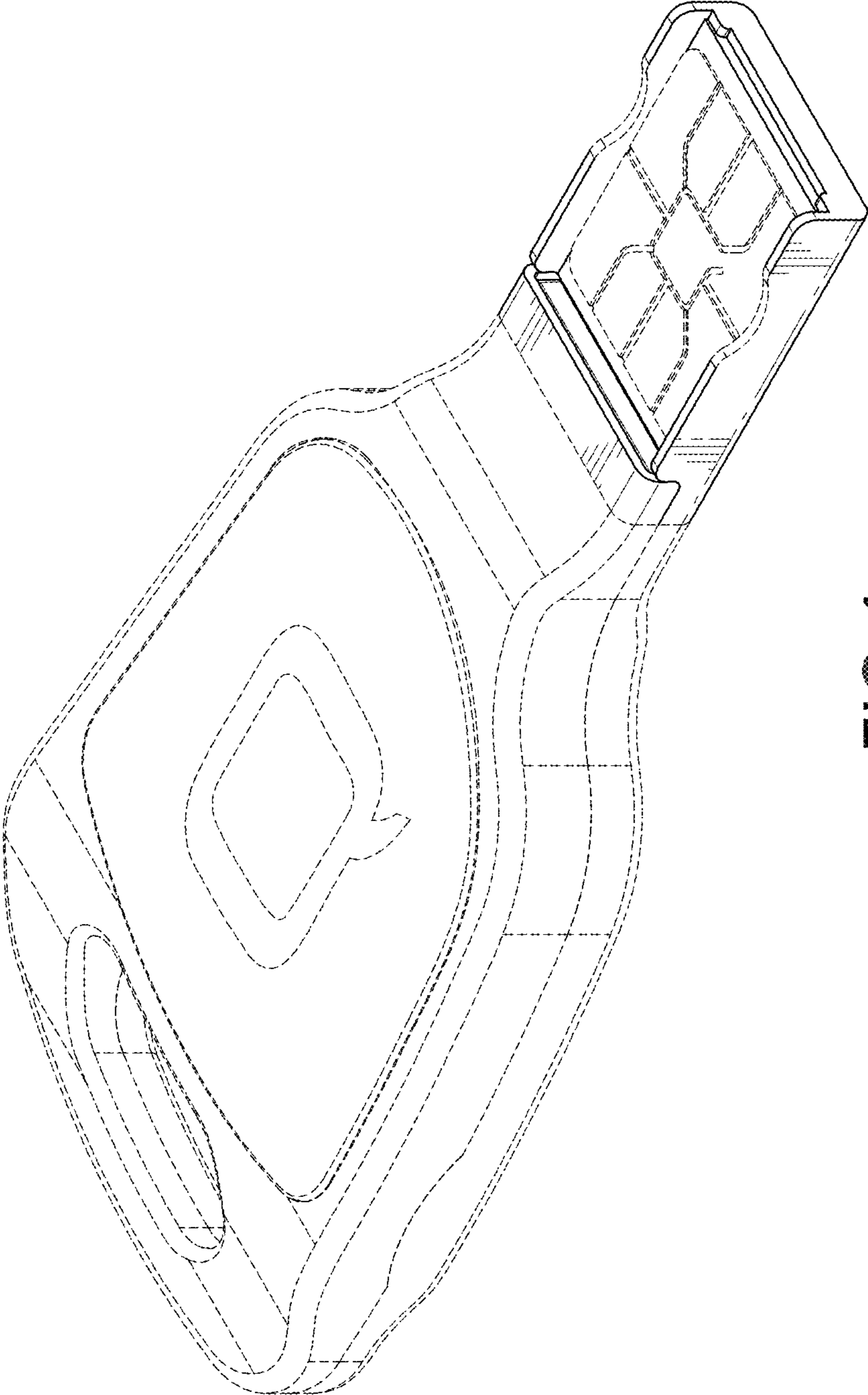


FIG. 1

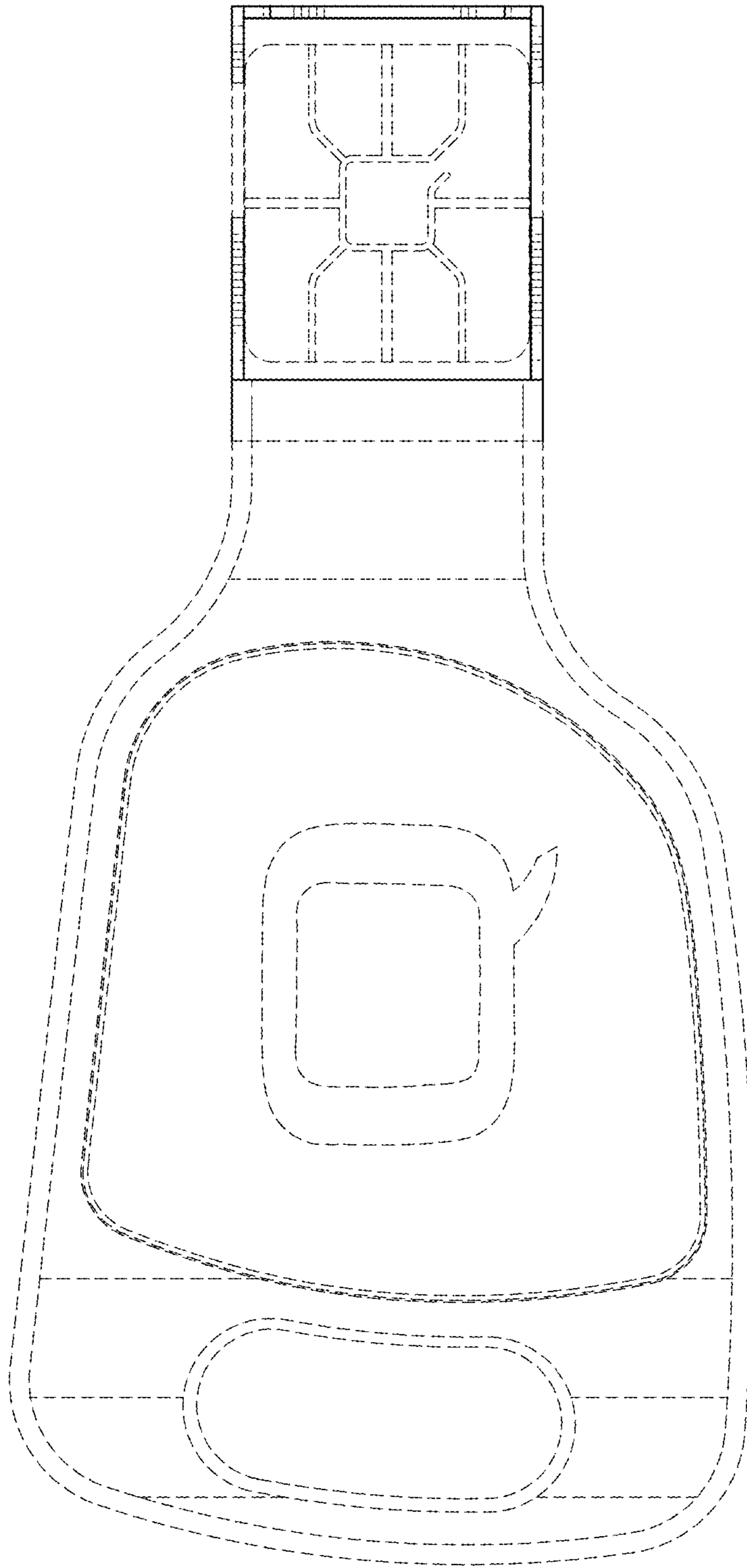


FIG. 2

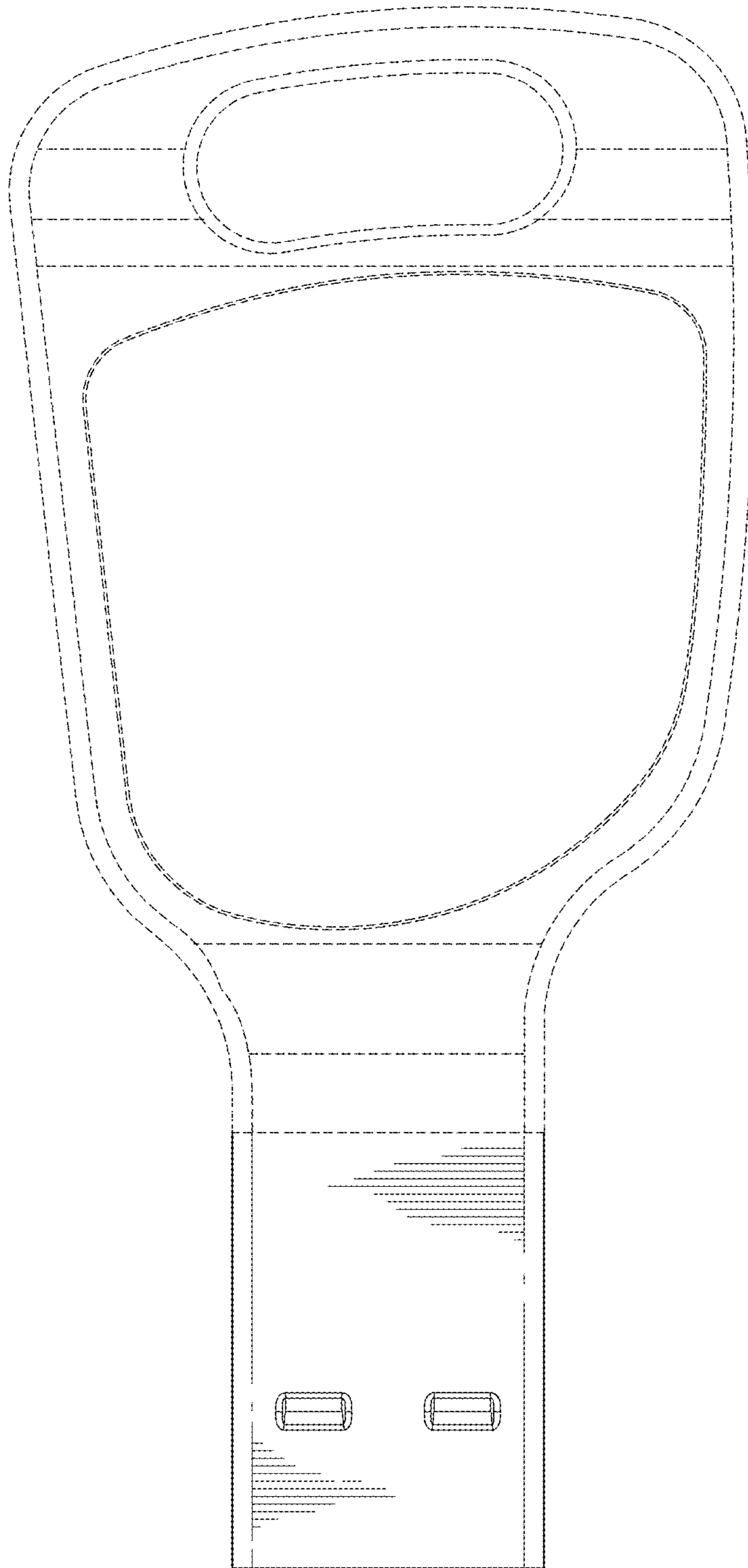


FIG. 3

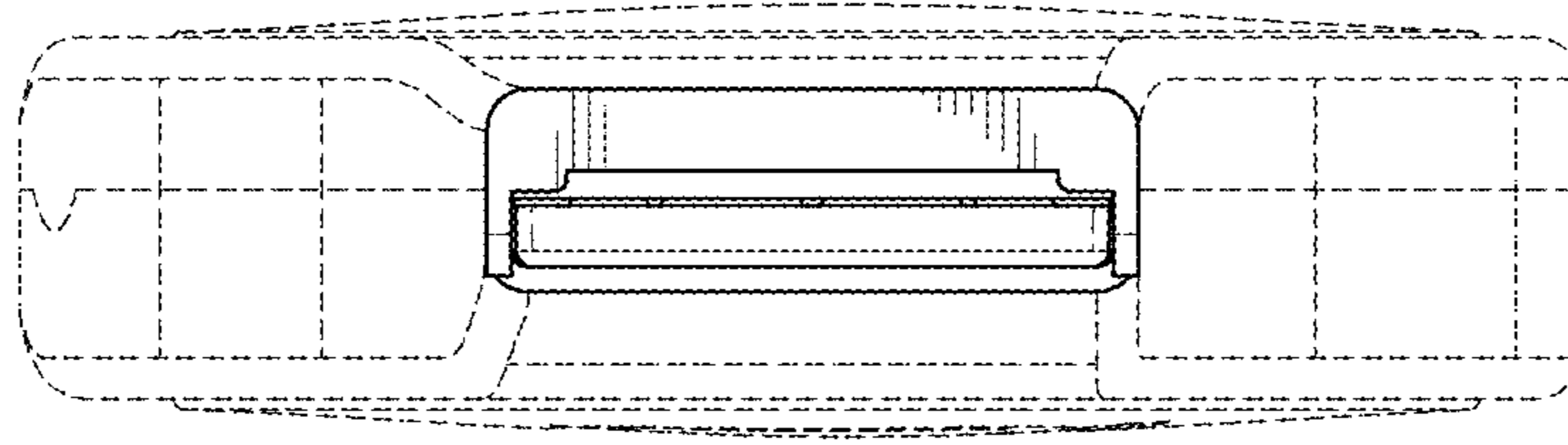


FIG. 5

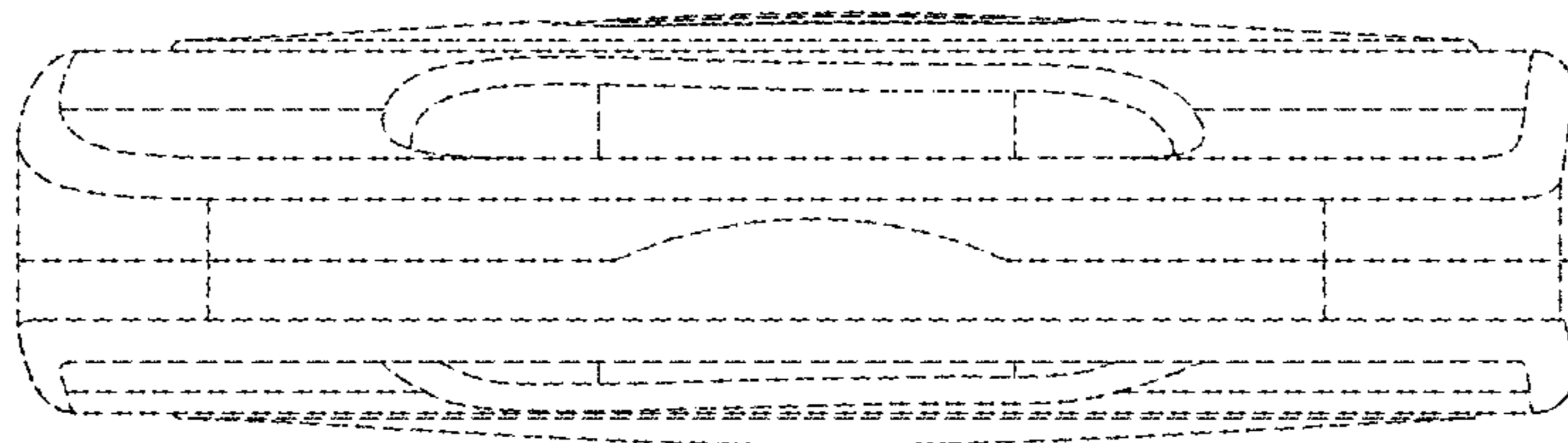


FIG. 4

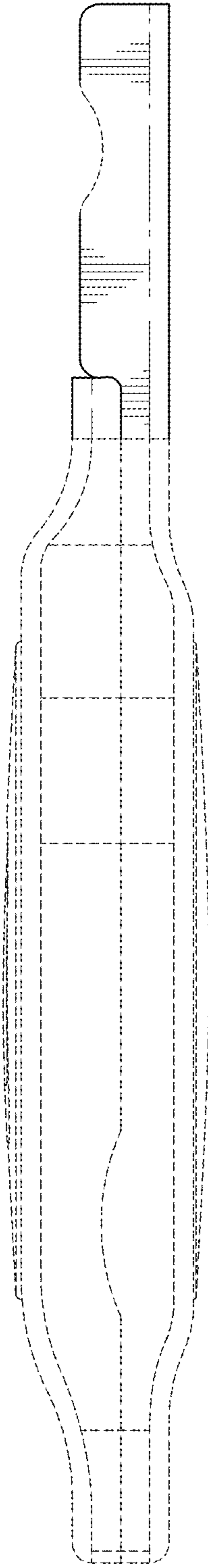


FIG. 6

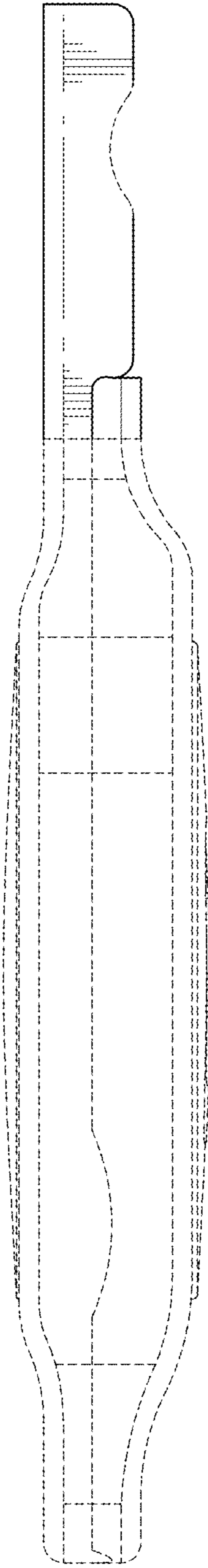


FIG. 7