



US00D783008S

(12) **United States Design Patent**
Kim et al.

(10) **Patent No.:** **US D783,008 S**
(45) **Date of Patent:** **** Apr. 4, 2017**

(54) **GATEWAY TERMINAL**

(71) Applicant: **LG ELECTRONICS INC.**, Seoul (KR)

(72) Inventors: **Taeho Kim**, Seoul (KR); **Inyoung Hwang**, Seoul (KR); **Kangmin Kim**, Seoul (KR); **Kyoungha Lee**, Seoul (KR); **Sungil Cho**, Seoul (KR)

(73) Assignee: **LG ELECTRONICS INC.**, Seoul (KR)

(**) Term: **15 Years**

(21) Appl. No.: **29/540,391**

(22) Filed: **Sep. 24, 2015**

(30) **Foreign Application Priority Data**

Mar. 27, 2015 (KR) 30-2015-0015853

(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/242**

(58) **Field of Classification Search**
USPC D14/242, 240, 357, 358, 125, 140-140.9, D14/155, 137, 139, 243, 239, 433, 496, D14/188, 348, 351, 356, 349, 353, 354
CPC H04L 12/00; H03K 17/00; H04W 88/00; H04W 88/005; H04W 88/02; H04W 88/08; H04W 88/085; H04W 88/10; H04W 88/12; H04W 88/14; H04B 1/38
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D275,673 S * 9/1984 Iino D14/230
D346,598 S * 5/1994 McCay D14/137

D393,468 S * 4/1998 Dalby D13/134
D499,719 S * 12/2004 Peersmann D14/240
D501,848 S * 2/2005 Uehara D14/240
D508,039 S * 8/2005 Cutler D14/125
D694,740 S * 12/2013 Apostolakis D14/240
D704,177 S * 5/2014 Chun D14/240
D725,088 S * 3/2015 Kwak D14/240
D733,107 S * 6/2015 Porter D14/240
D752,566 S * 3/2016 Hinman D14/242
D770,430 S * 11/2016 Lee D14/240
D771,023 S * 11/2016 Zhang D14/240

FOREIGN PATENT DOCUMENTS

CN 303579254 * 2/2016
WO D087429-005 * 3/2016

* cited by examiner

Primary Examiner — Bridget L Eland
Assistant Examiner — Lauren McVey

(74) *Attorney, Agent, or Firm* — Ked & Associates LLP

(57) **CLAIM**

We claim the ornamental design for a gateway terminal, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a gateway terminal showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left-side elevation view thereof;
FIG. 5 is a right-side elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.
The broken lines immediately adjacent the claim represent the bounds of the claimed design; the broken lines form no part of the claimed design.

1 Claim, 5 Drawing Sheets

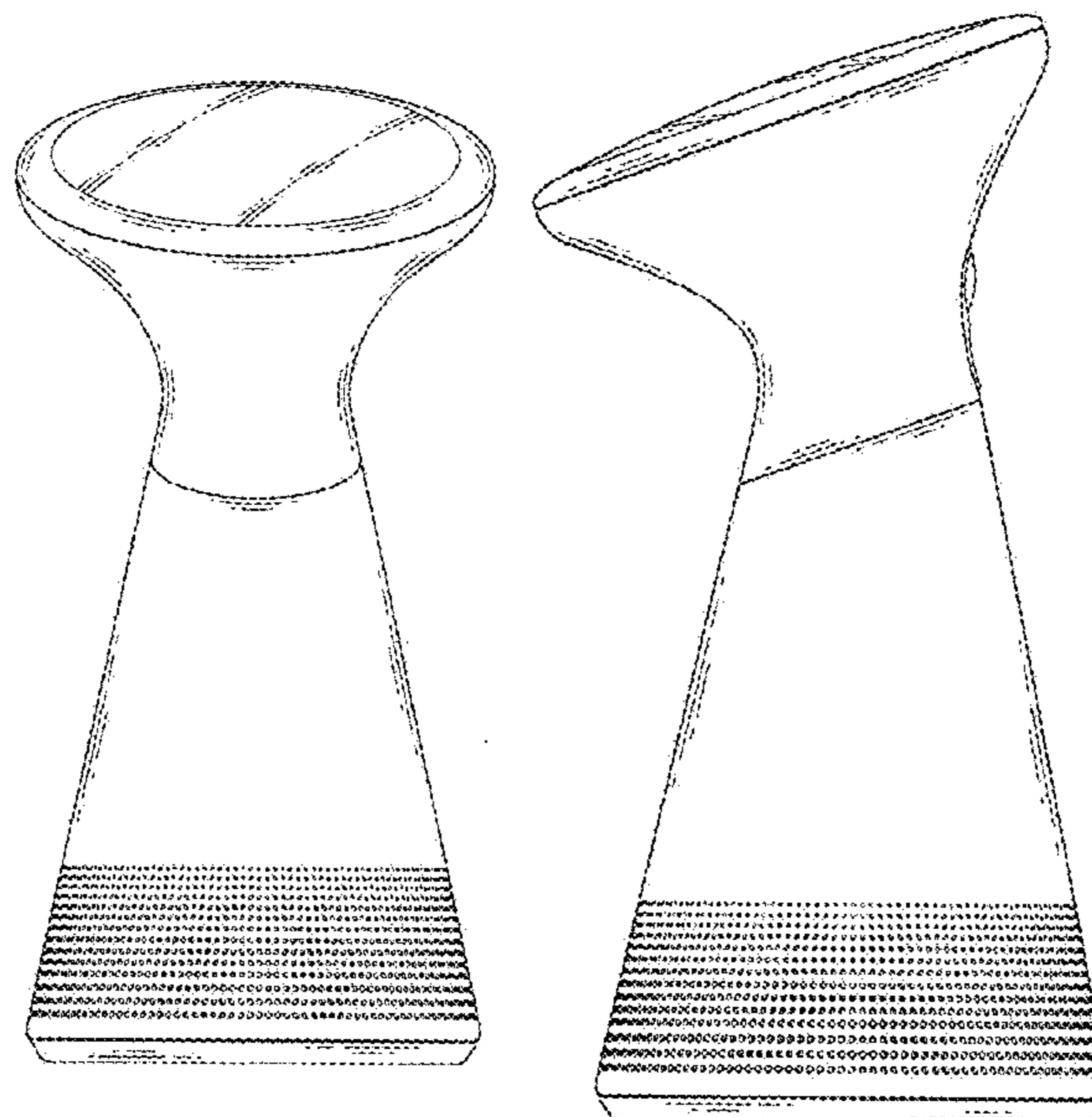


FIG. 1

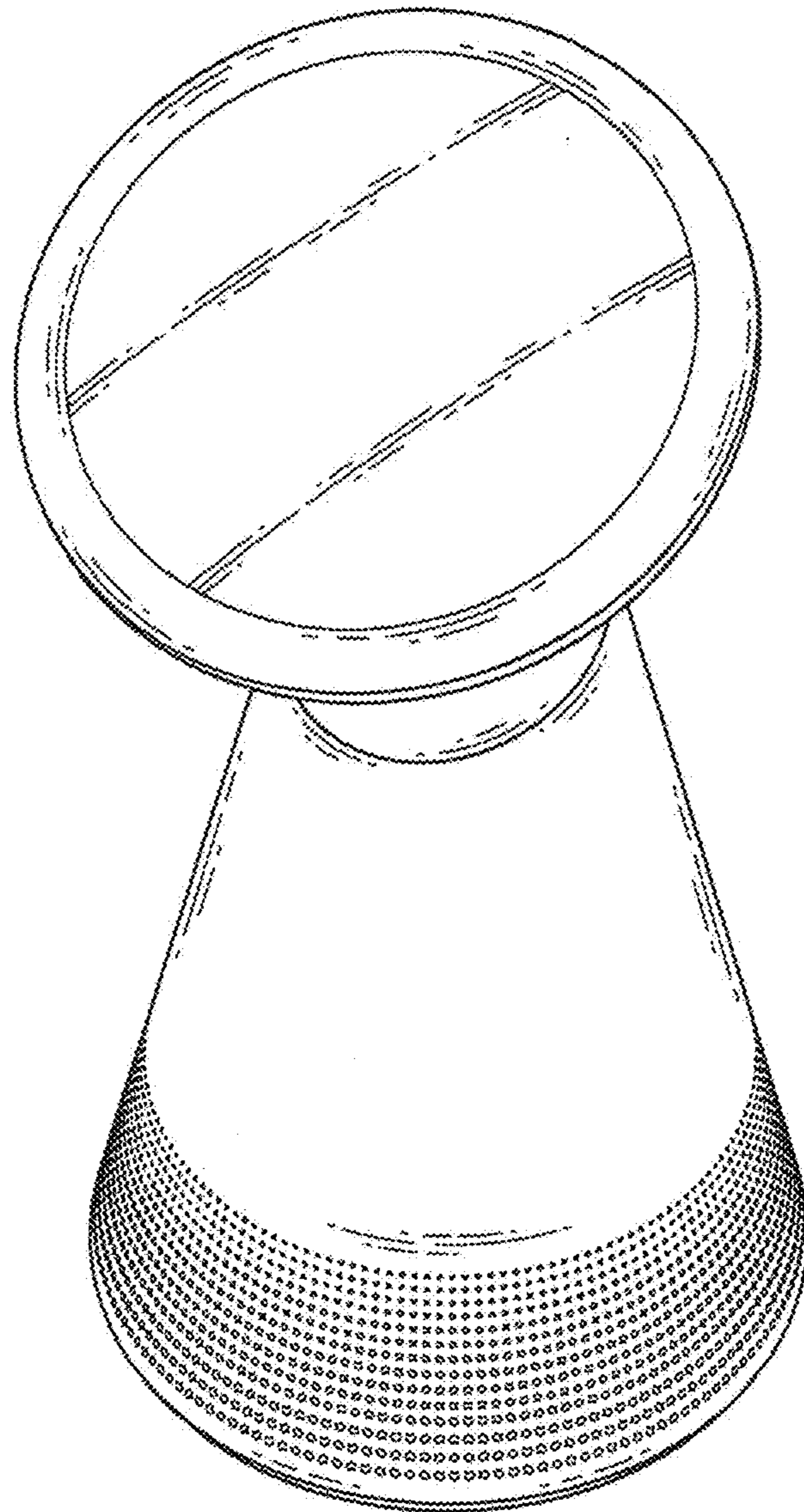


FIG.2

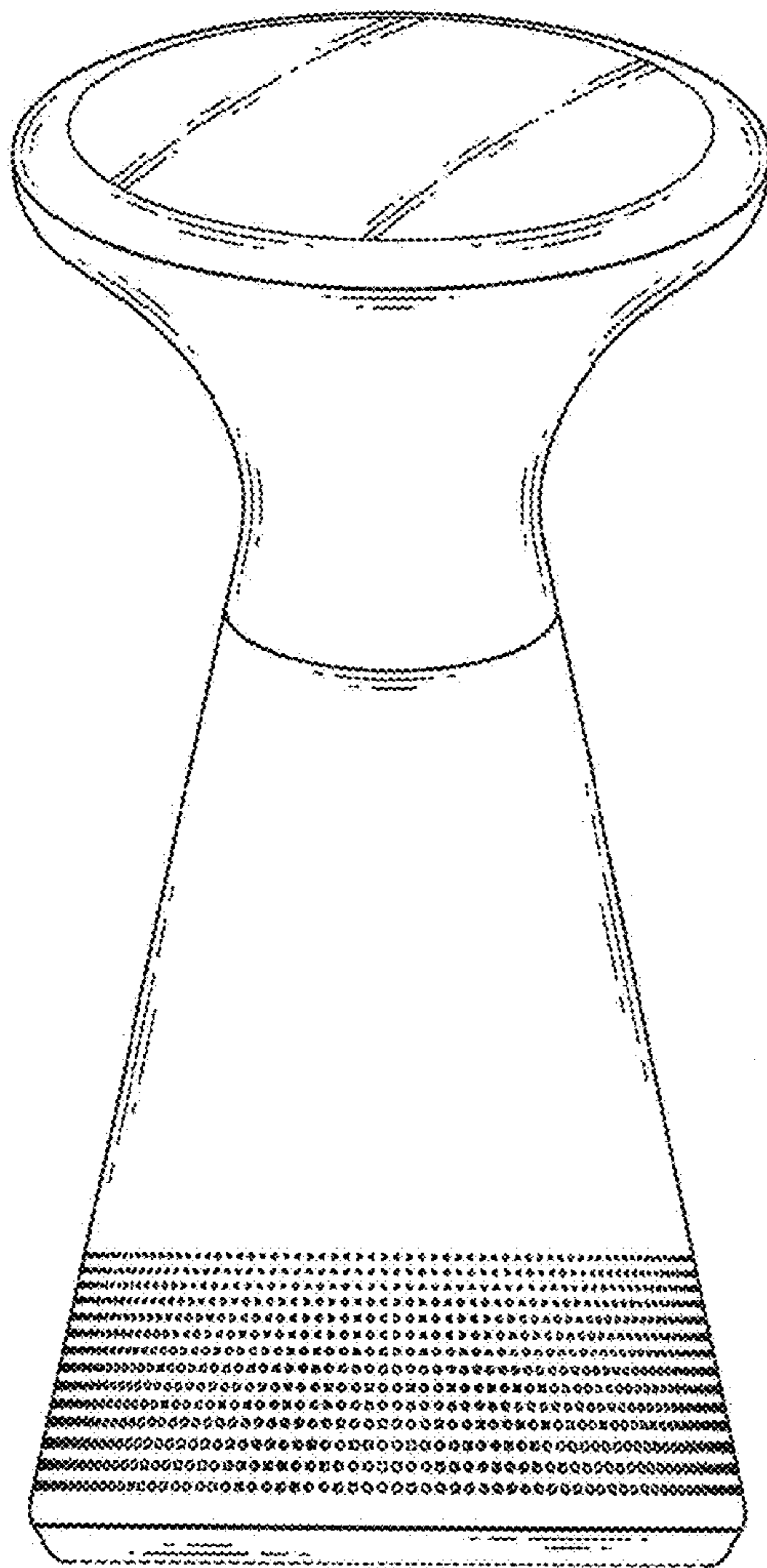


FIG.3

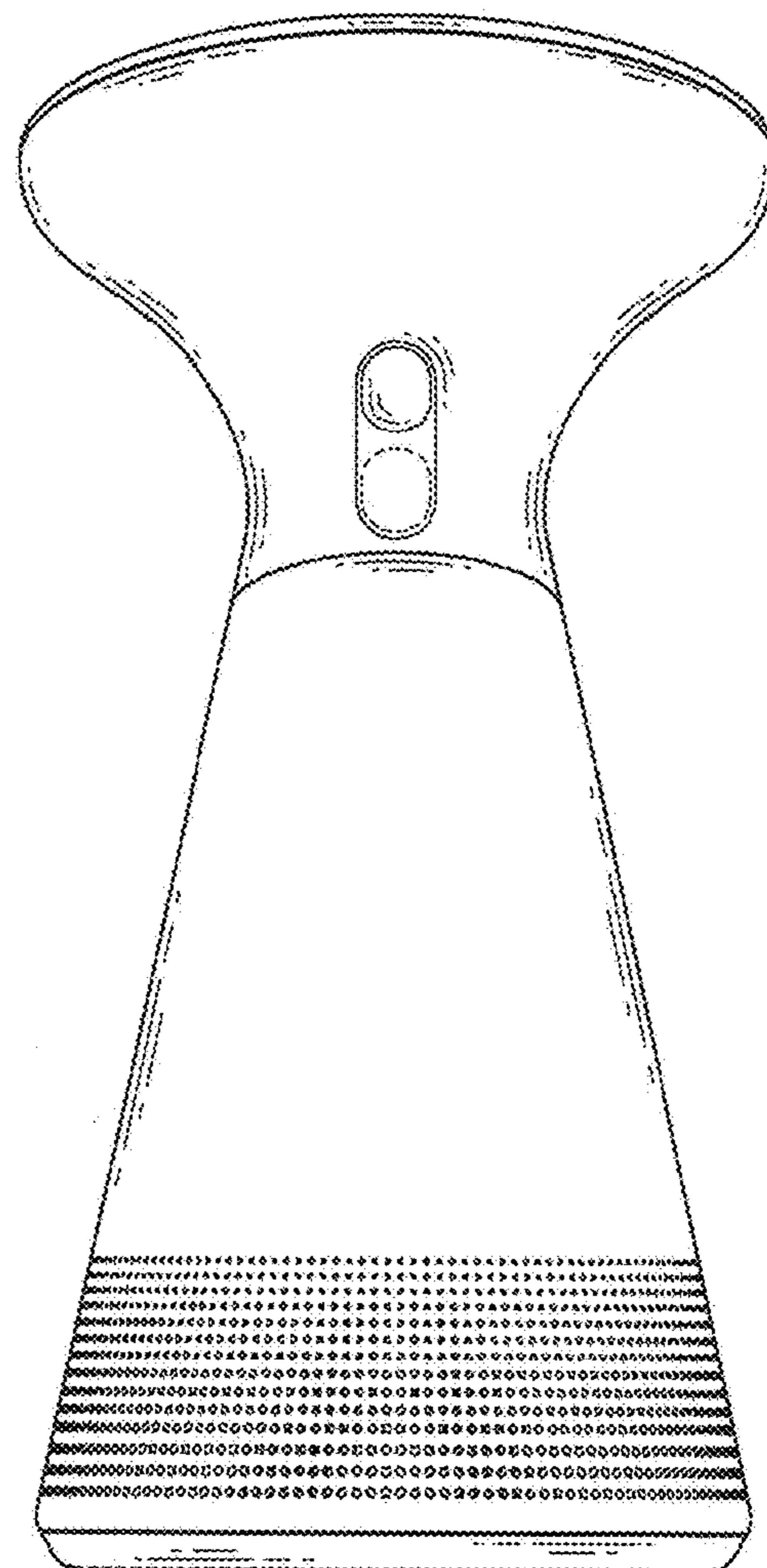


FIG. 4

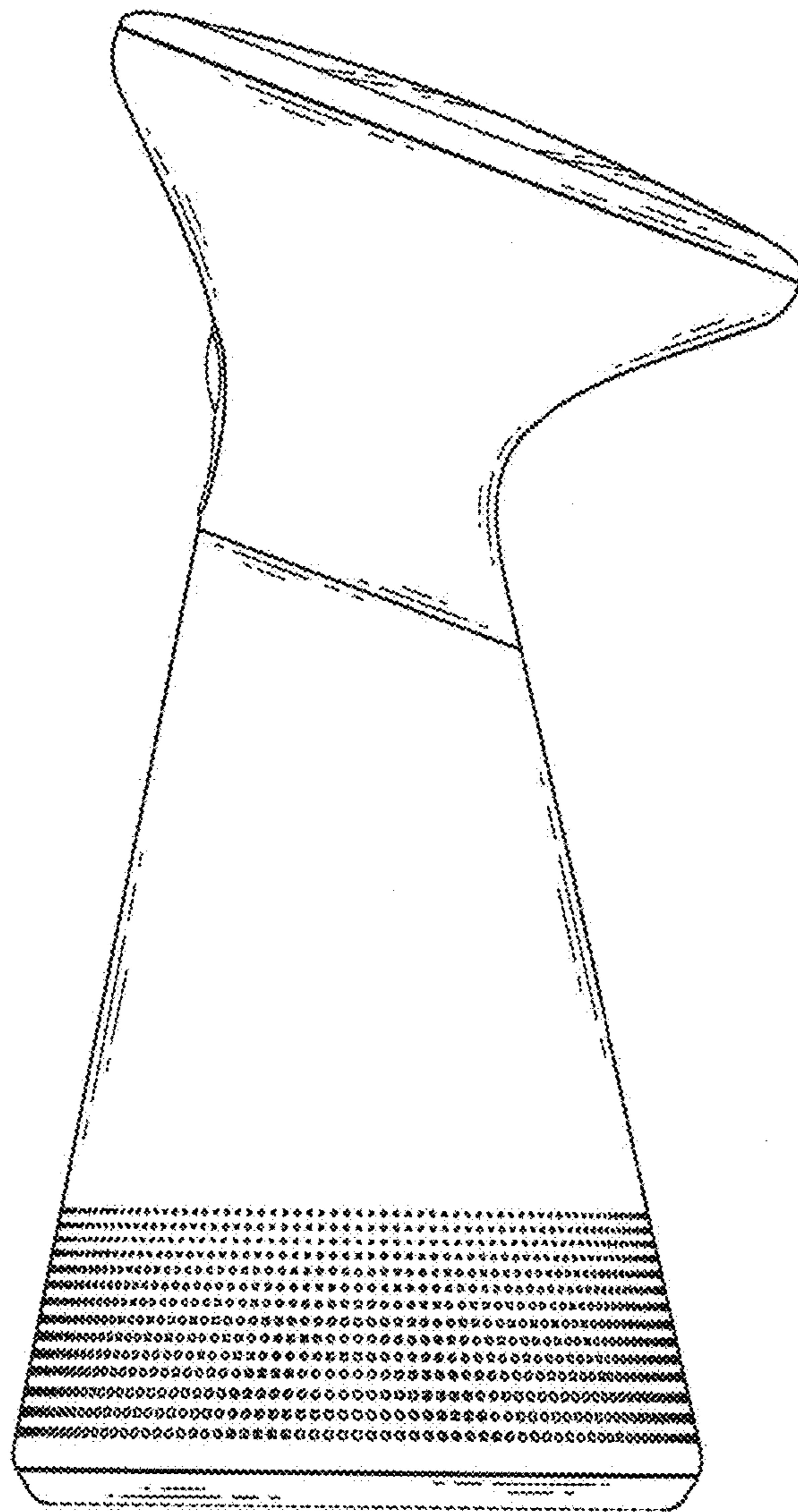


FIG. 5

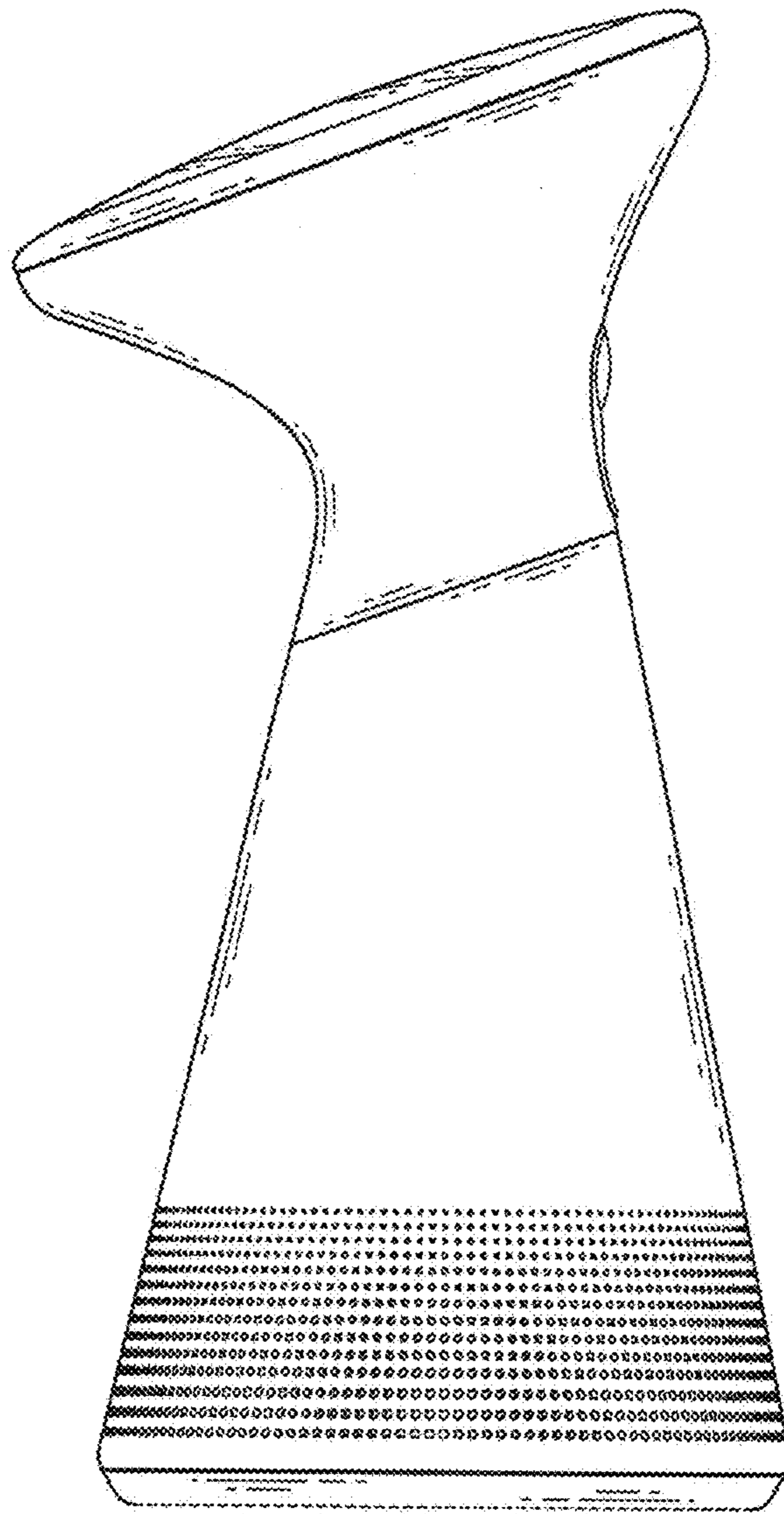


FIG.6

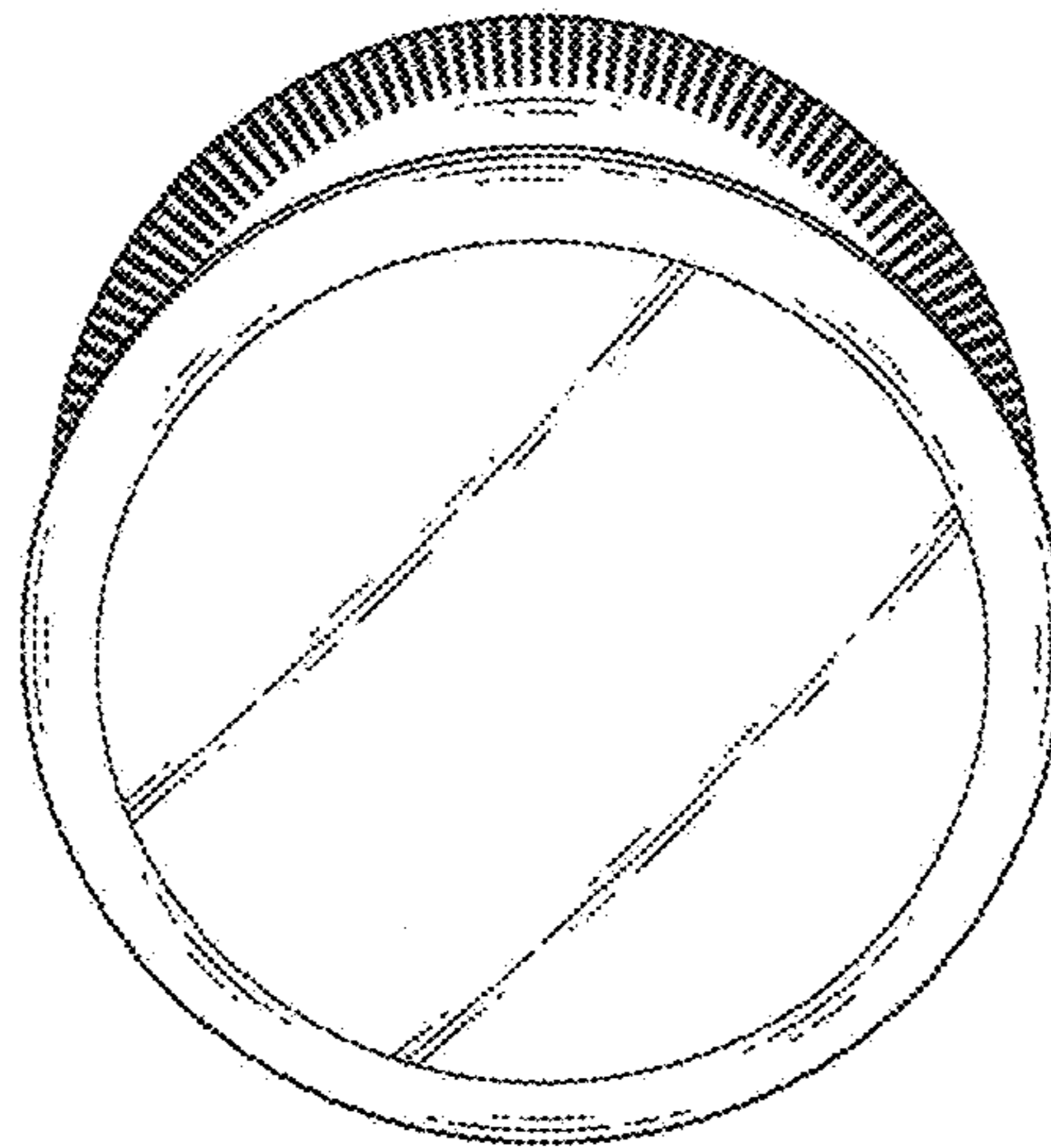


FIG.7

