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(12) **United States Design Patent**
Liu et al.

(10) **Patent No.:** **US D782,421 S**
(45) **Date of Patent:** **** Mar. 28, 2017**

(54) **RIGHT EXPANSION MODULE FOR RTU IO
MODULE AND EXPANSION END PLATE**

D691,608 S * 10/2013 Samuels D14/434
D693,775 S * 11/2013 Cech D13/162
D703,141 S * 4/2014 Andersson D13/147

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* cited by examiner

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(**) Term: **14 Years**

(57) **CLAIM**

(21) Appl. No.: **29/522,146**

The ornamental design for a right expansion module for an
RTU IO module and expansion end plate, as shown and
described.

(22) Filed: **Mar. 27, 2015**

(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/147**

(58) **Field of Classification Search**
USPC D13/123, 147, 154, 158, 162, 184, 199;
D14/358, 363, 383, 388, 464
CPC H01R 9/031; H01R 9/032; H01R 9/09;
H01R 9/091; H01R 9/16; H01R 9/0232;
H01R 9/2408; H01R 11/05; H01R 12/51;
H01R 12/52; H01R 12/53; H01R 12/55;
H01R 12/61; H01R 12/616; H01R 12/62;
H01R 12/7076; H01R 13/2492; H01R
13/28; H01R 13/506; H01R 13/516
See application file for complete search history.

DESCRIPTION

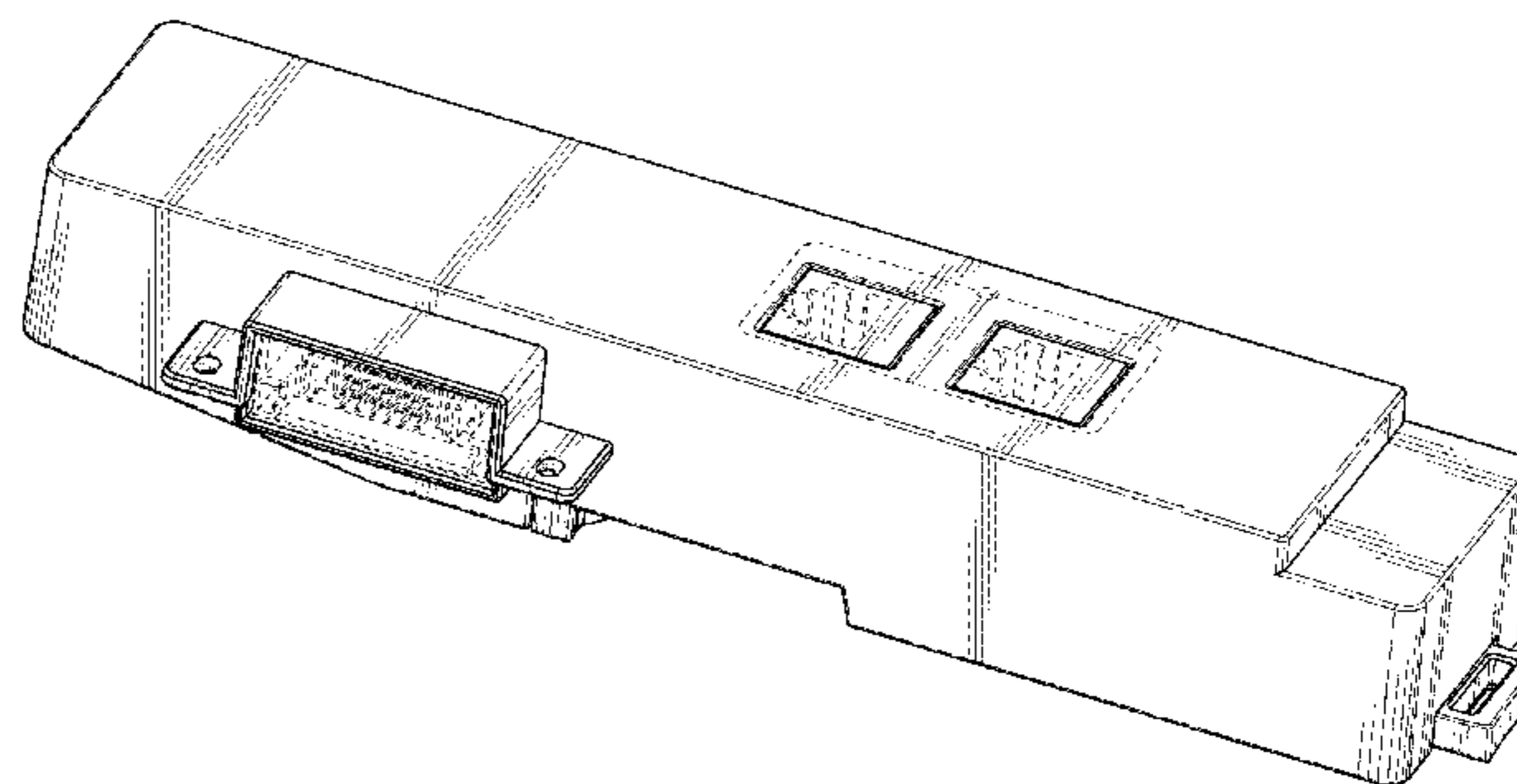
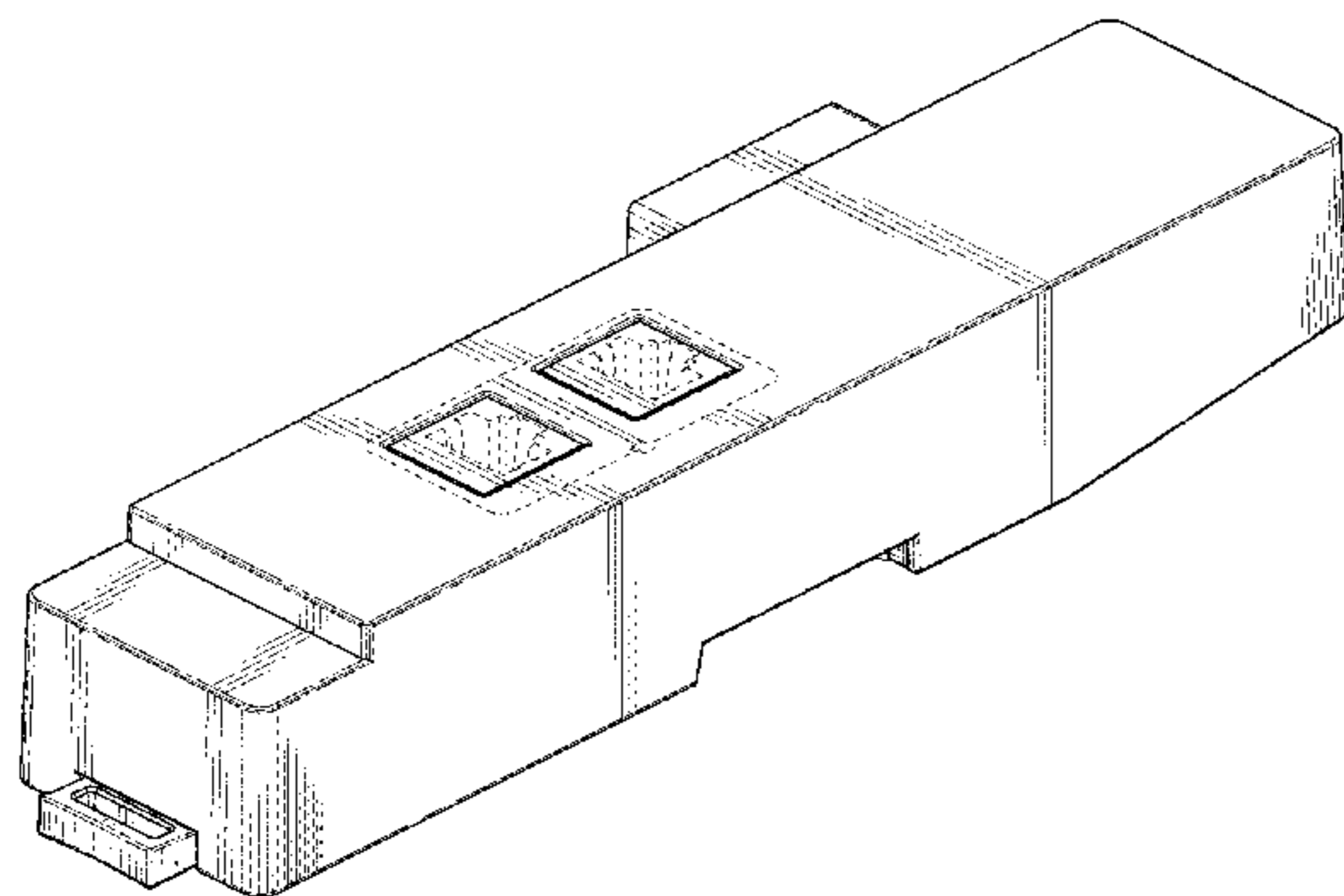
FIG. 1 is a top plan view of a right expansion module for an
RTU IO module and expansion end plate, showing our new
design;
FIG. 2 is a right side elevation view thereof;
FIG. 3 is a left side elevation view thereof;
FIG. 4 is a front elevation view thereof;
FIG. 5 is a rear elevation view thereof;
FIG. 6 is a bottom plan view thereof;
FIG. 7 is a top right front perspective view thereof; and,
FIG. 8 is a top left front perspective view thereof.
The broken line in the figure drawing views is included to
show unclaimed environment only and forms no part of the
claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D431,526 S * 10/2000 Nishio D13/123
D520,958 S * 5/2006 Yoneyama D13/158

1 Claim, 5 Drawing Sheets



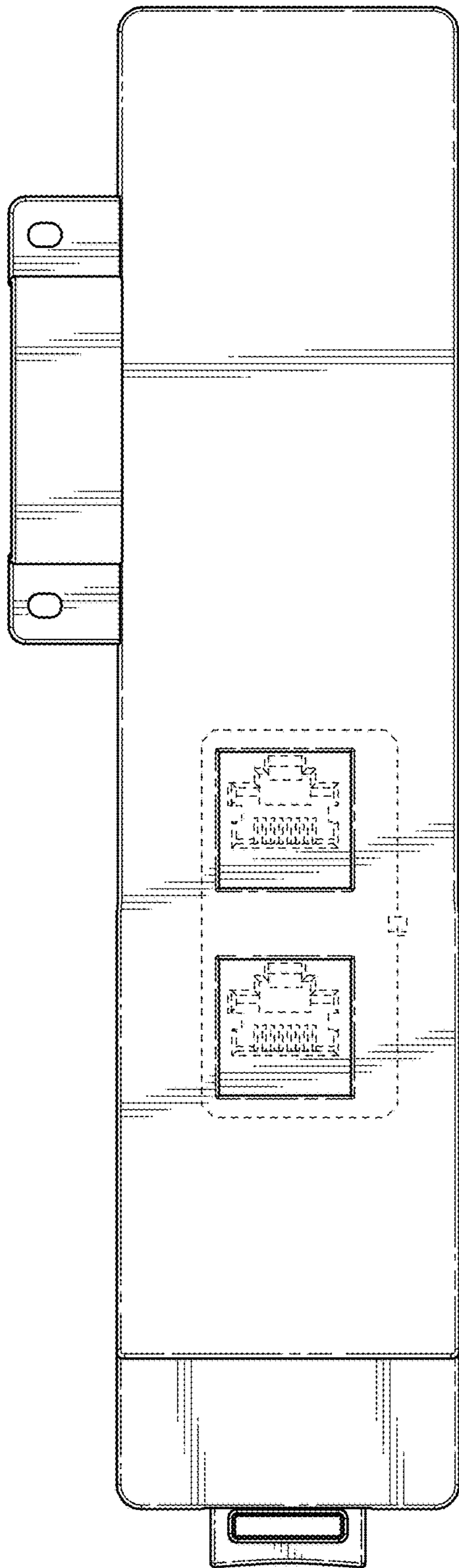


FIG. 1

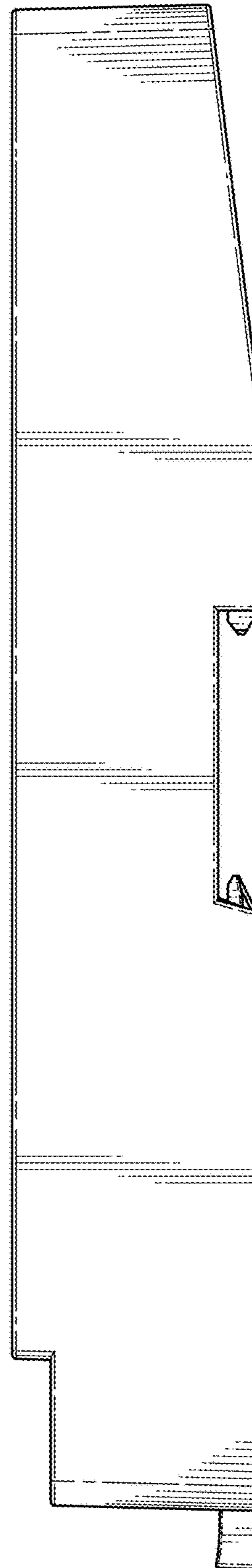


FIG. 2

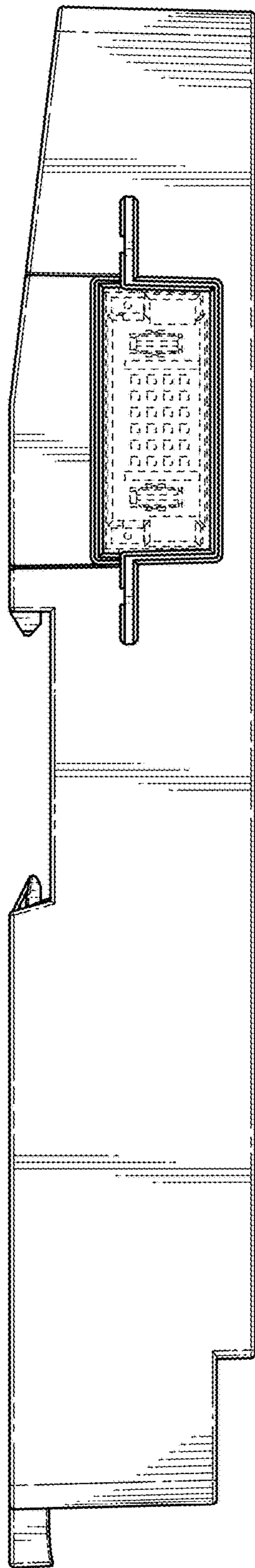


FIG. 3

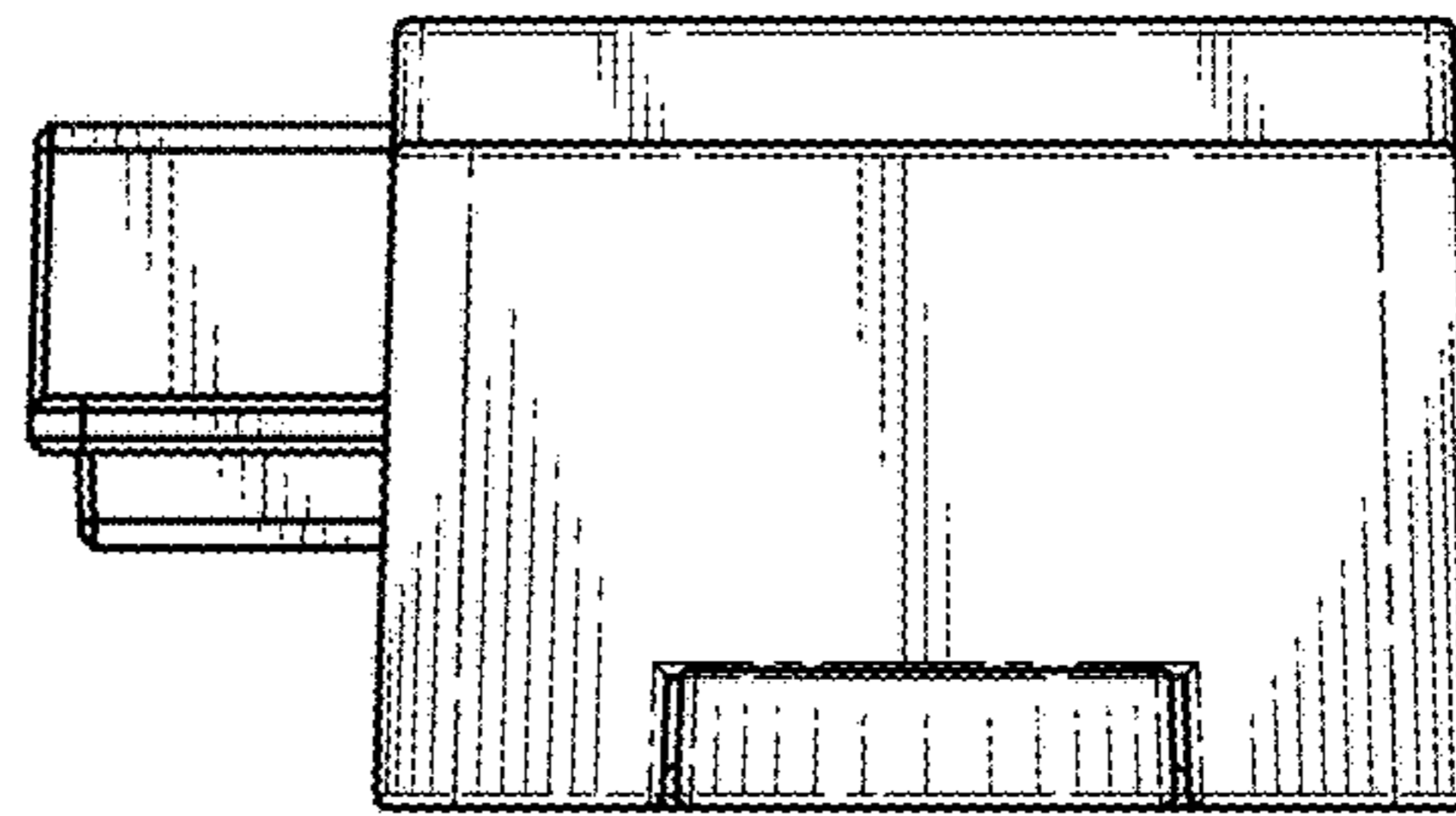


FIG. 4

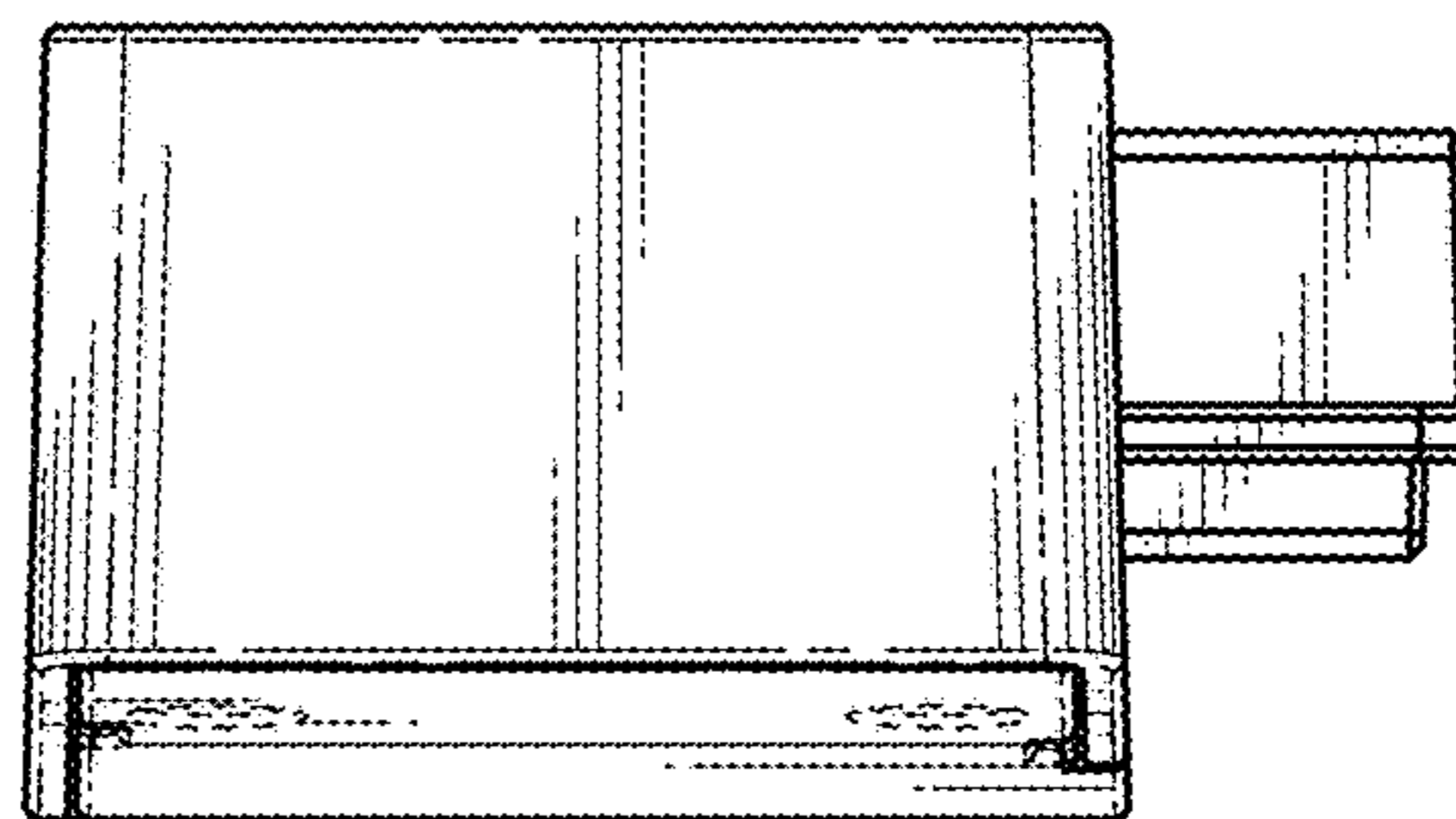


FIG. 5

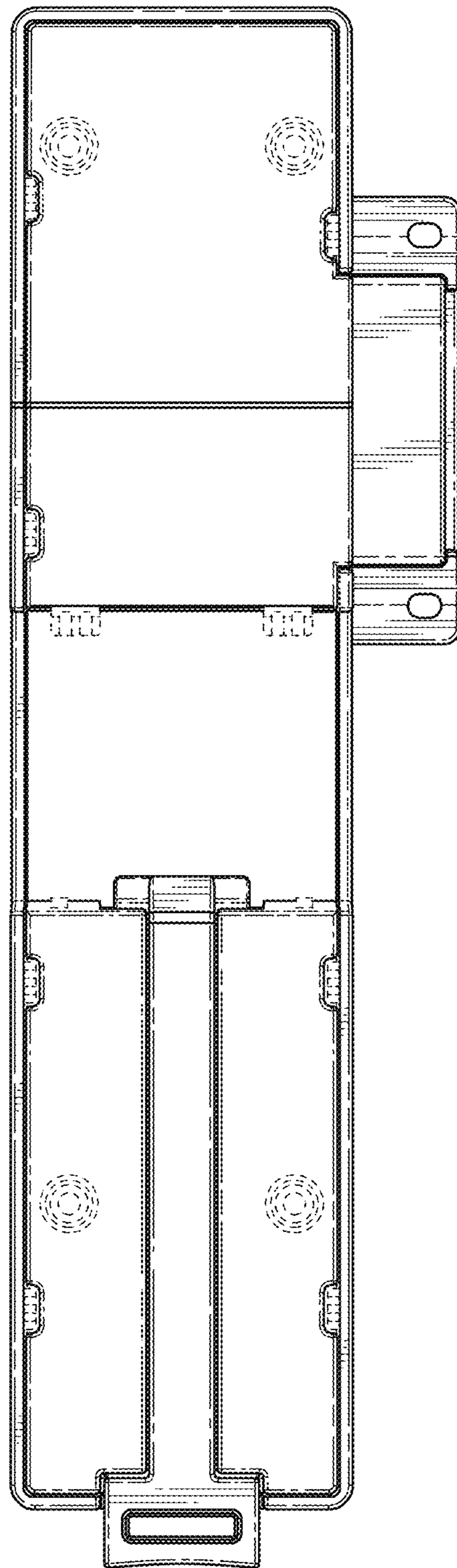


FIG. 6

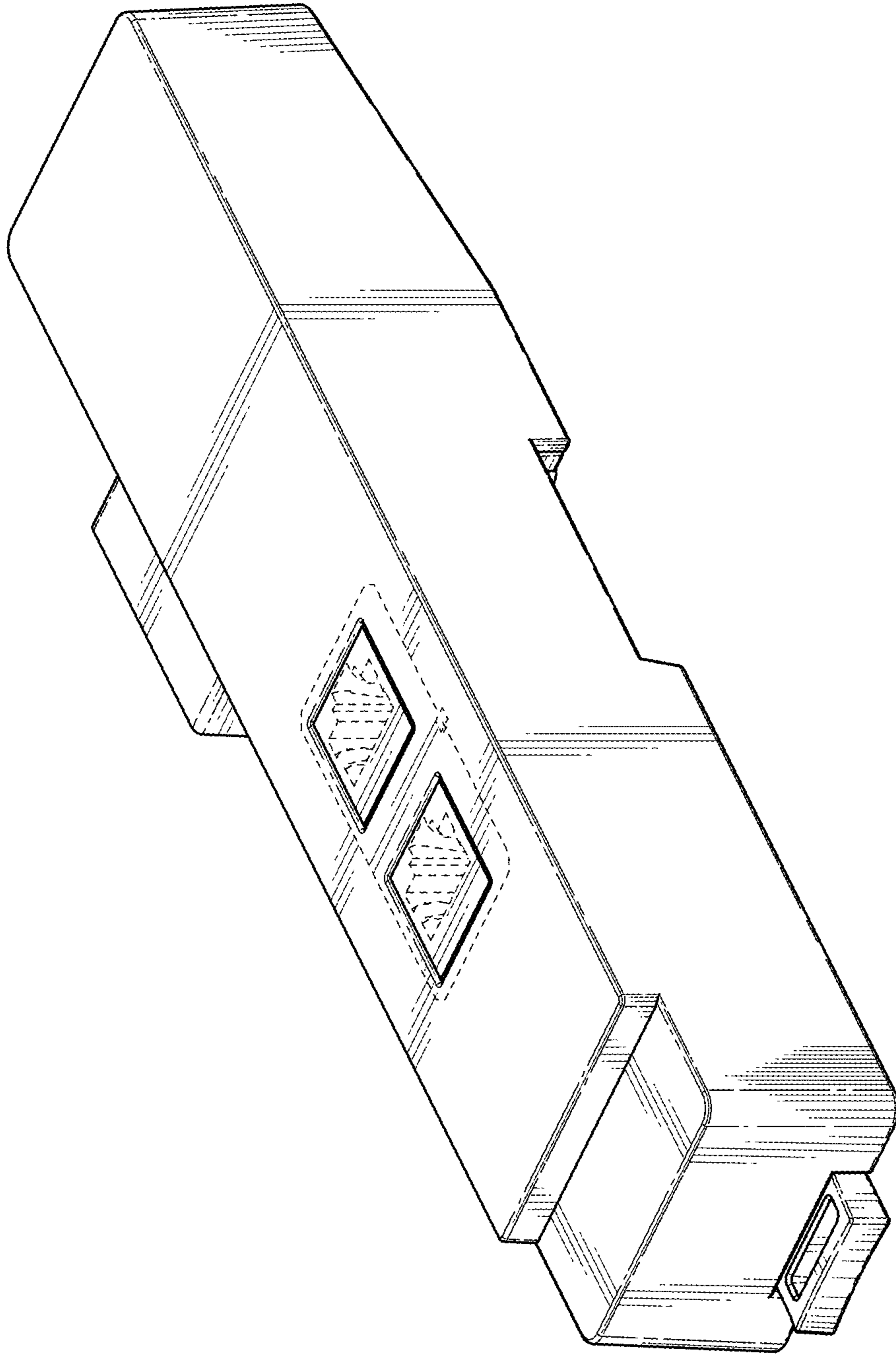


FIG. 7

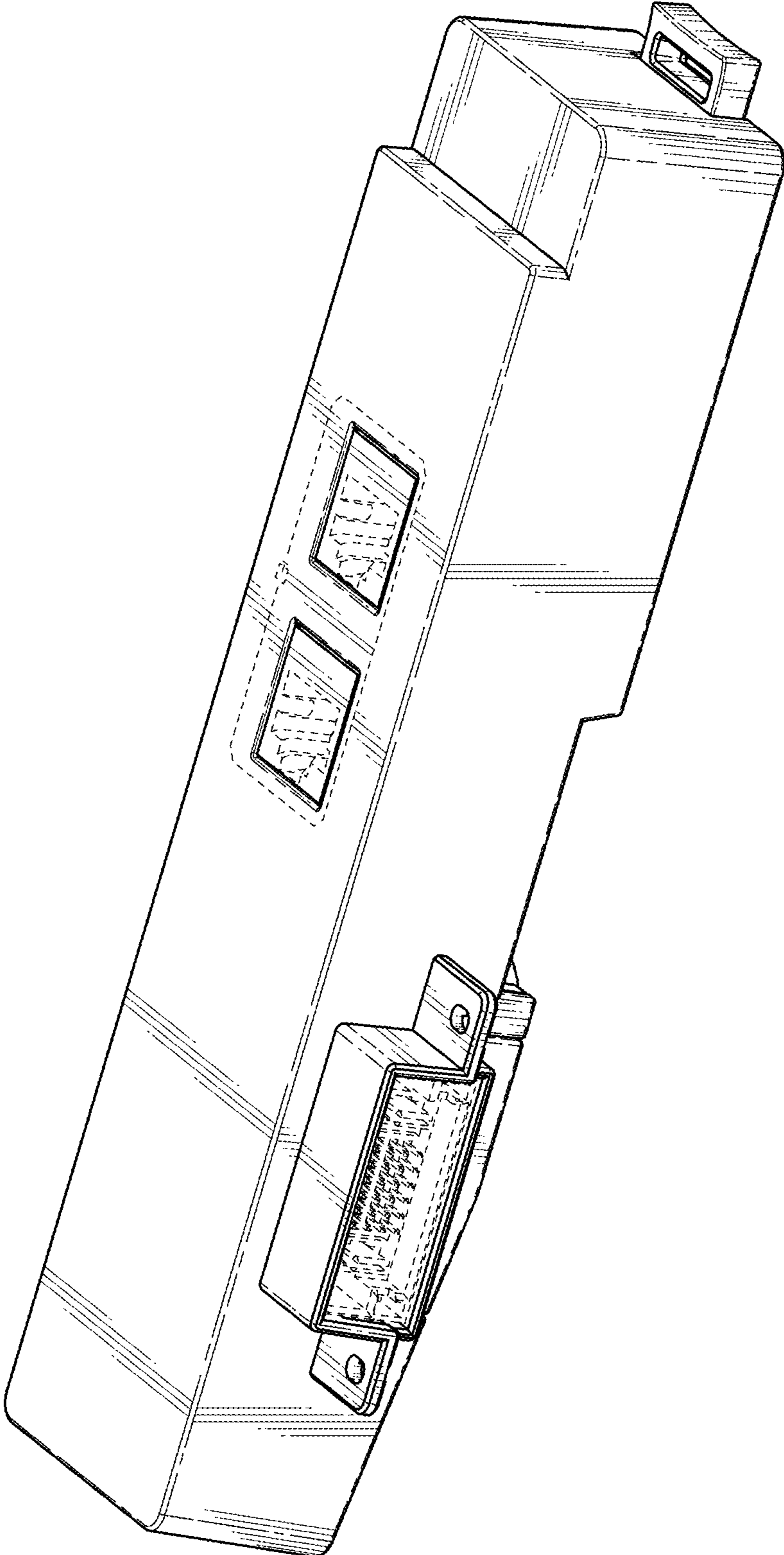


FIG. 8