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(12) **United States Design Patent** (10) **Patent No.:** **US D781,801 S**  
**Tao et al.** (45) **Date of Patent:** **\*\* \*Mar. 21, 2017**

(54) **ELECTRONIC DEVICE**

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(\*) Notice: This patent is subject to a terminal disclaimer.

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/497,445**

(22) Filed: **Jul. 24, 2014**

**Related U.S. Application Data**

(63) Continuation-in-part of application No. 29/482,800, filed on Feb. 21, 2014, now Pat. No. Des. 760,181.

(51) **LOC (10) Cl.** ..... **14-03**

(52) **U.S. Cl.**  
USPC ..... **D14/138 AA; D14/346**

(58) **Field of Classification Search**  
USPC .... D14/138 AA, 138 R, 346, 248, 218, 137, D14/147, 148; D13/168; 455/575.1, 455/556.1, 556.2, 566; D21/517; 361/679.3, 679.56; 379/433.01, 433.04  
CPC ..... H04M 1/202-1/035; G06F 1/1613; H01H 13/85

See application file for complete search history.

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(57) **CLAIM**

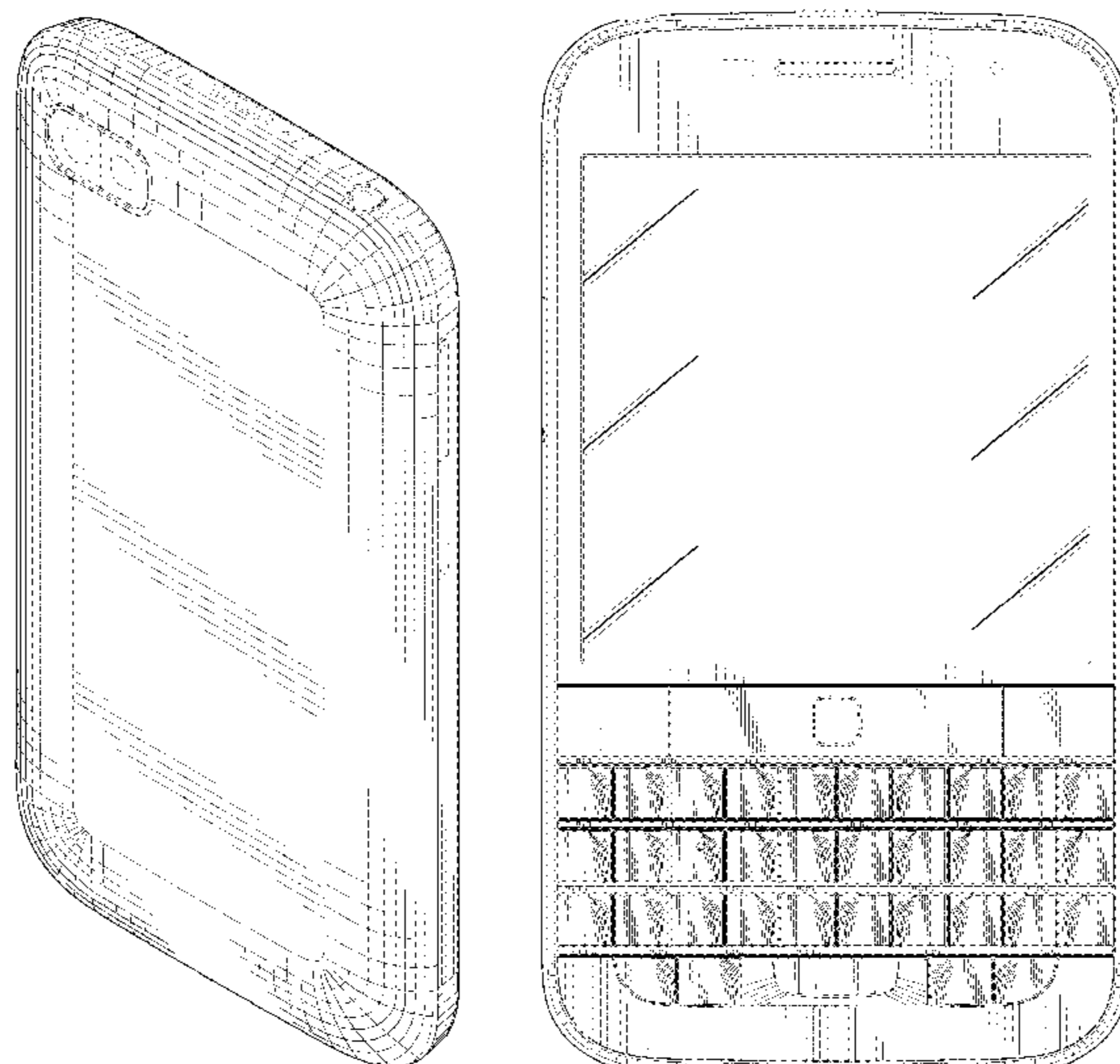
The ornamental design for an electronic device, as shown and described.

**DESCRIPTION**

FIG. 1 is a front perspective view of an electronic device; FIG. 2 is a rear perspective view thereof; FIG. 3 is a front elevation view thereof; FIG. 4 is a rear elevation view thereof; FIG. 5 is a left side elevation view thereof; FIG. 6 is a right side elevation view thereof; FIG. 7 is a top plan view thereof; and, FIG. 8 is a bottom plan view thereof.

The broken lines shown throughout the views, which are immediately adjacent to the shaded areas and define unshaded regions, represent the bounds of the claimed design, while all other broken lines are directed to environment; the broken lines form no part of the claimed design.

**1 Claim, 6 Drawing Sheets**



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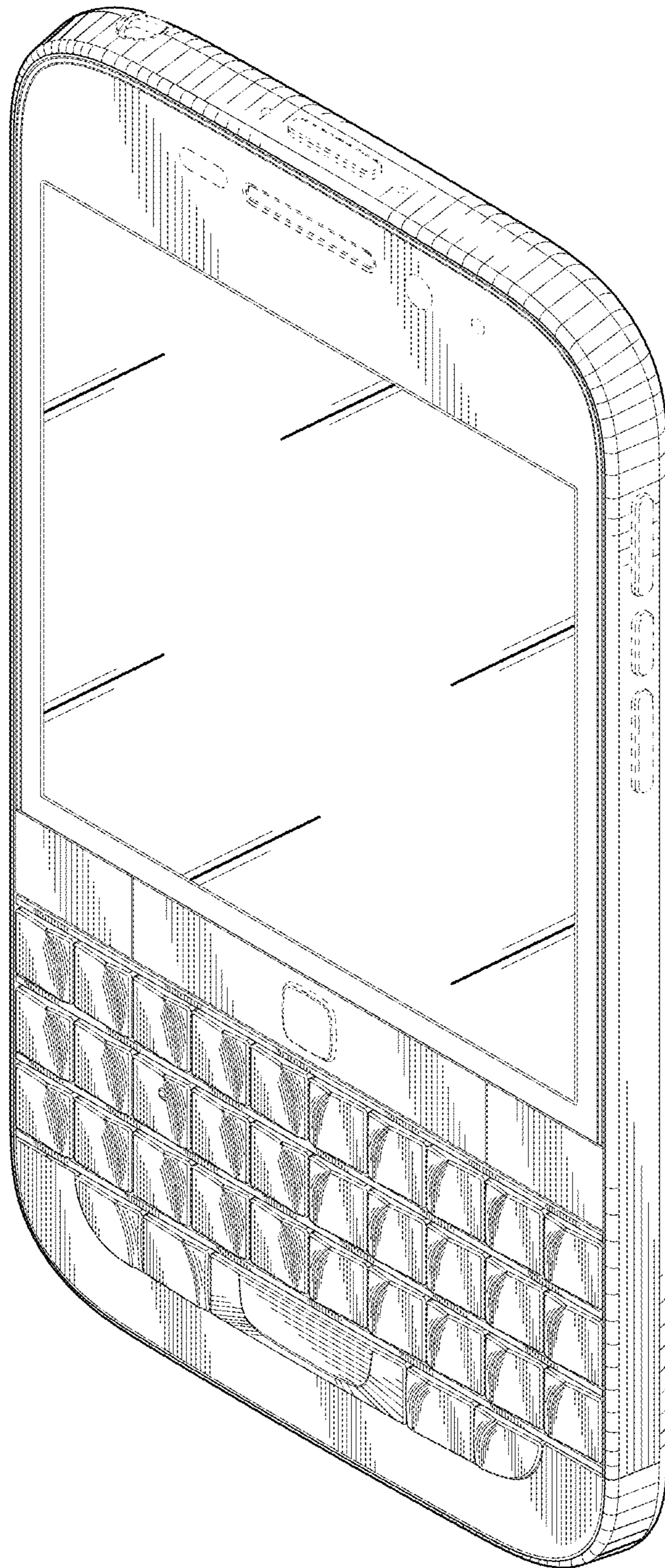


FIG.1

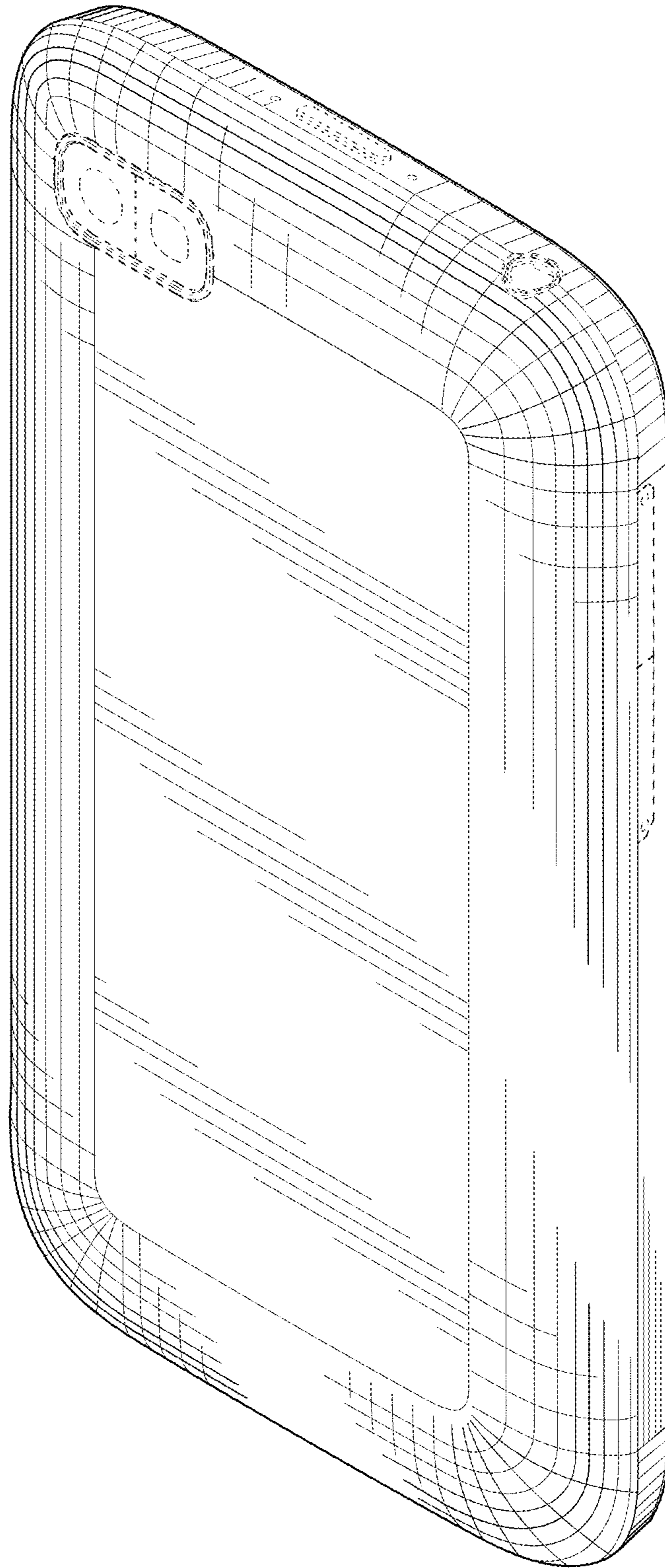


FIG.2

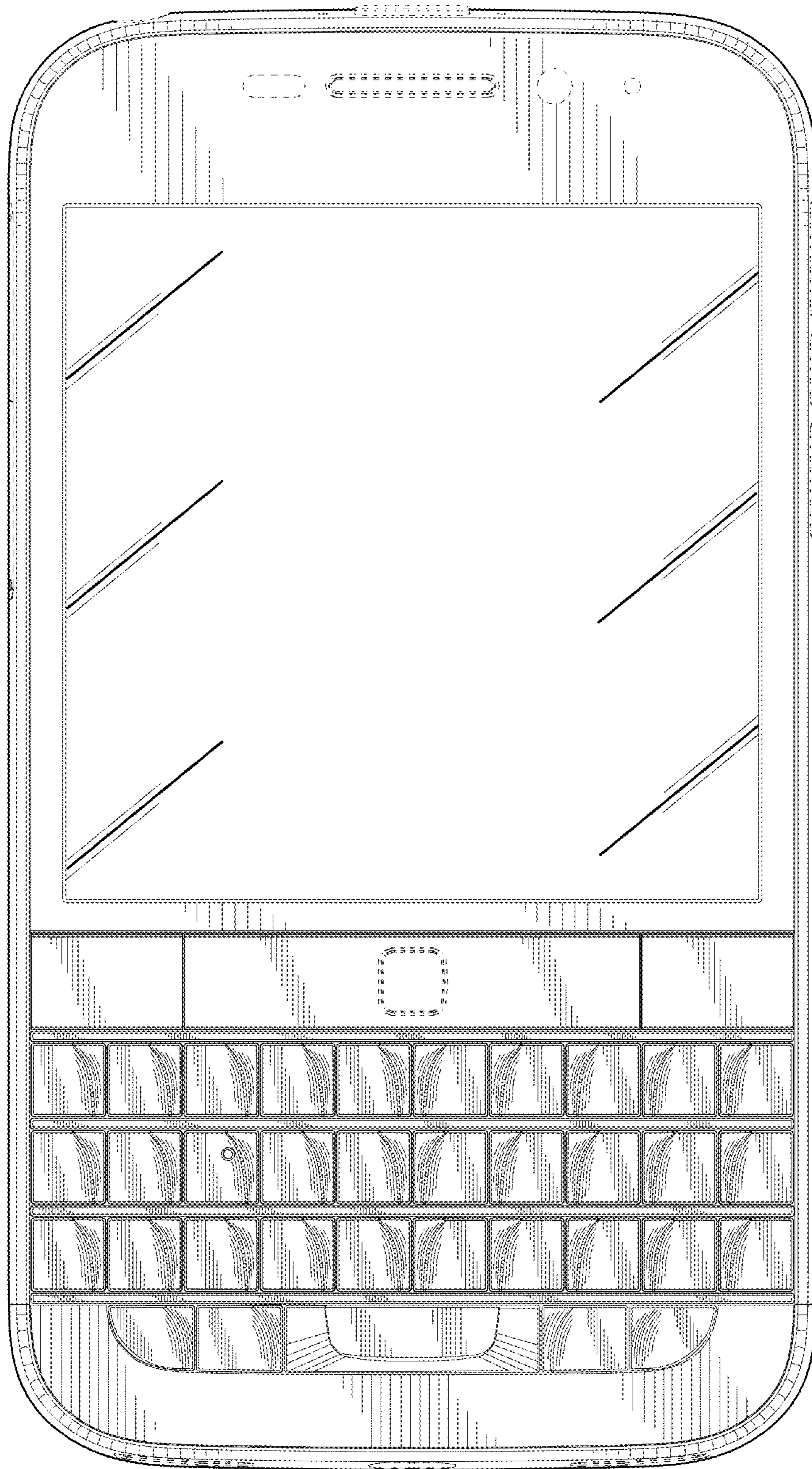


FIG.3

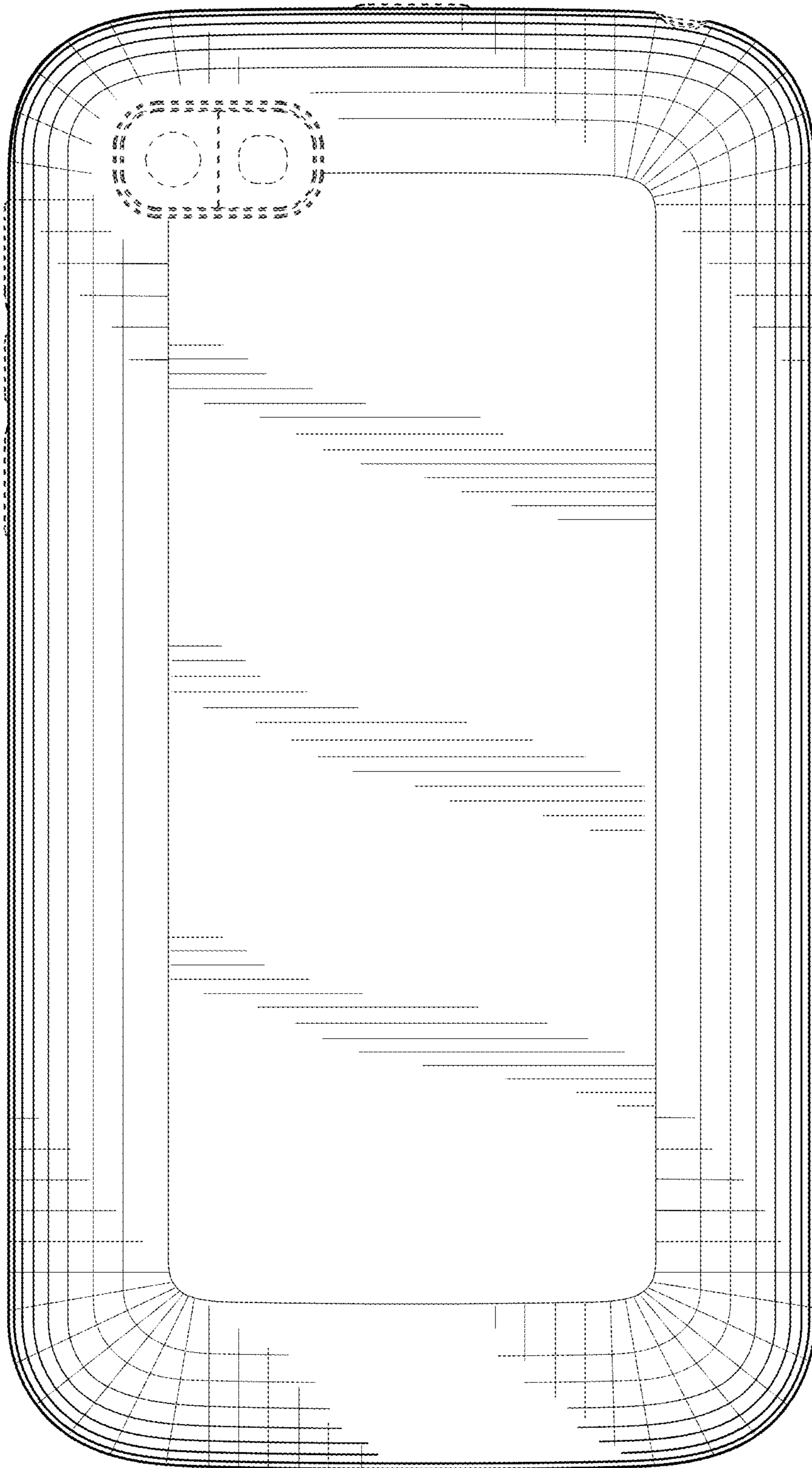


FIG.4

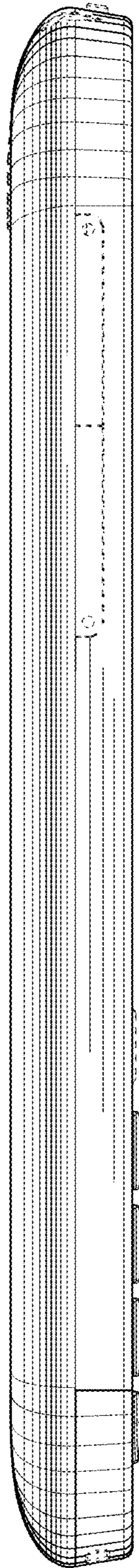


FIG. 5

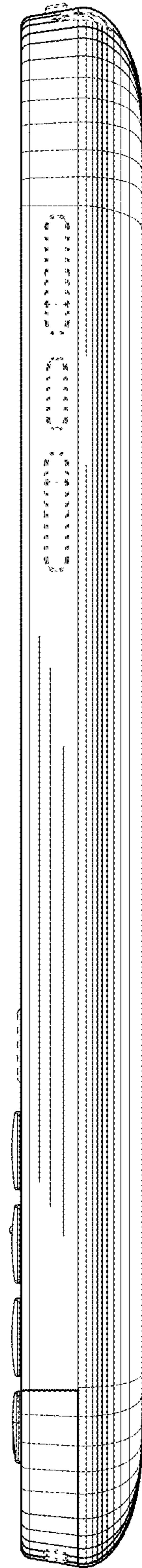


FIG. 6

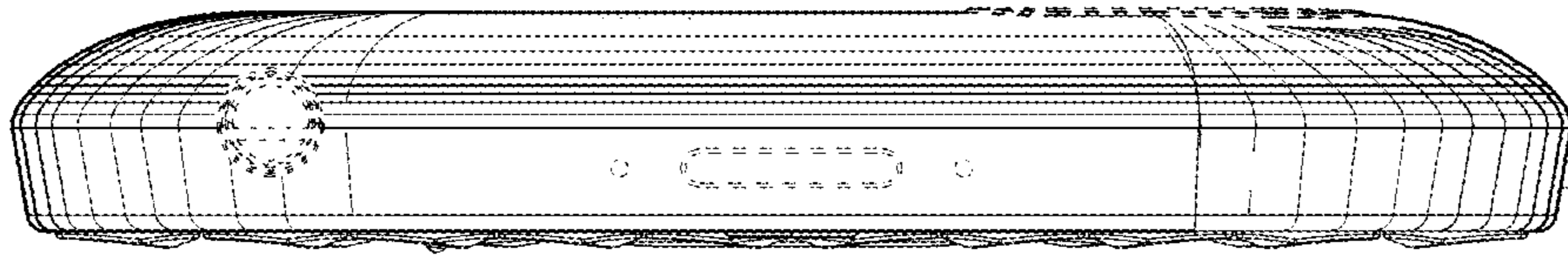


FIG. 7

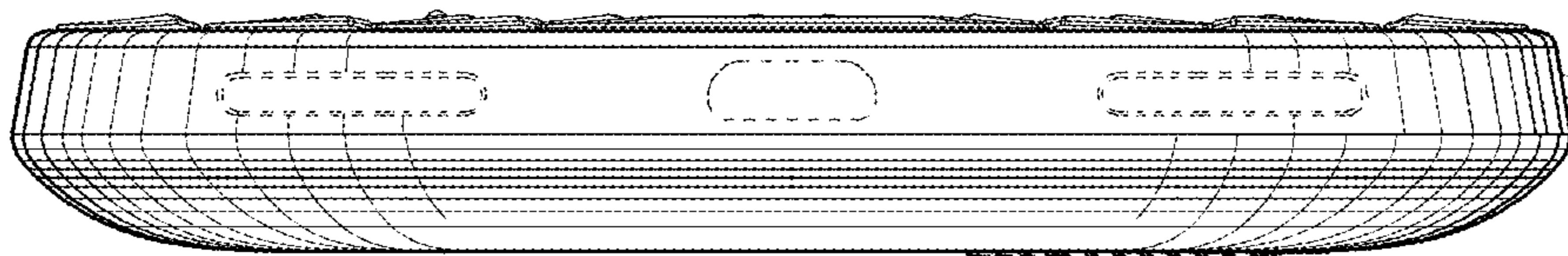


FIG. 8