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(12) **United States Design Patent**  
**Leveque et al.**

(10) **Patent No.:** **US D781,790 S**  
(45) **Date of Patent:** **\*\* Mar. 21, 2017**

- (54) **ELECTRICAL CIRCUIT BREAKER**
- (71) Applicant: **SCHNEIDER ELECTRIC INDUSTRIES SAS**, Rueil-Malmaison (FR)
- (72) Inventors: **Yves Leveque**, Voiron (FR); **Guillaume Reiner**, Grenoble (FR)
- (73) Assignee: **SCHNEIDER ELECTRIC INDUSTRIES SAS**, Rueil-Malmaison (FR)
- (\*\*) Term: **15 Years**
- (21) Appl. No.: **29/548,127**
- (22) Filed: **Dec. 10, 2015**
- (30) **Foreign Application Priority Data**

- Jun. 12, 2015 (EP) ..... 002717736-0001
- (51) **LOC (10) Cl.** ..... **13-03**
- (52) **U.S. Cl.**  
USPC ..... **D13/160**
- (58) **Field of Classification Search**  
USPC ..... D13/110, 112, 123, 158-160, 184, 199,  
D13/118, 165-173, 177, 178  
CPC .. H01H 9/00; H01H 9/02; H01H 9/20; H01H  
9/22; H01H 9/34; H01H 11/00; H01H  
33/00; H01H 33/08; H01H 77/00  
See application file for complete search history.

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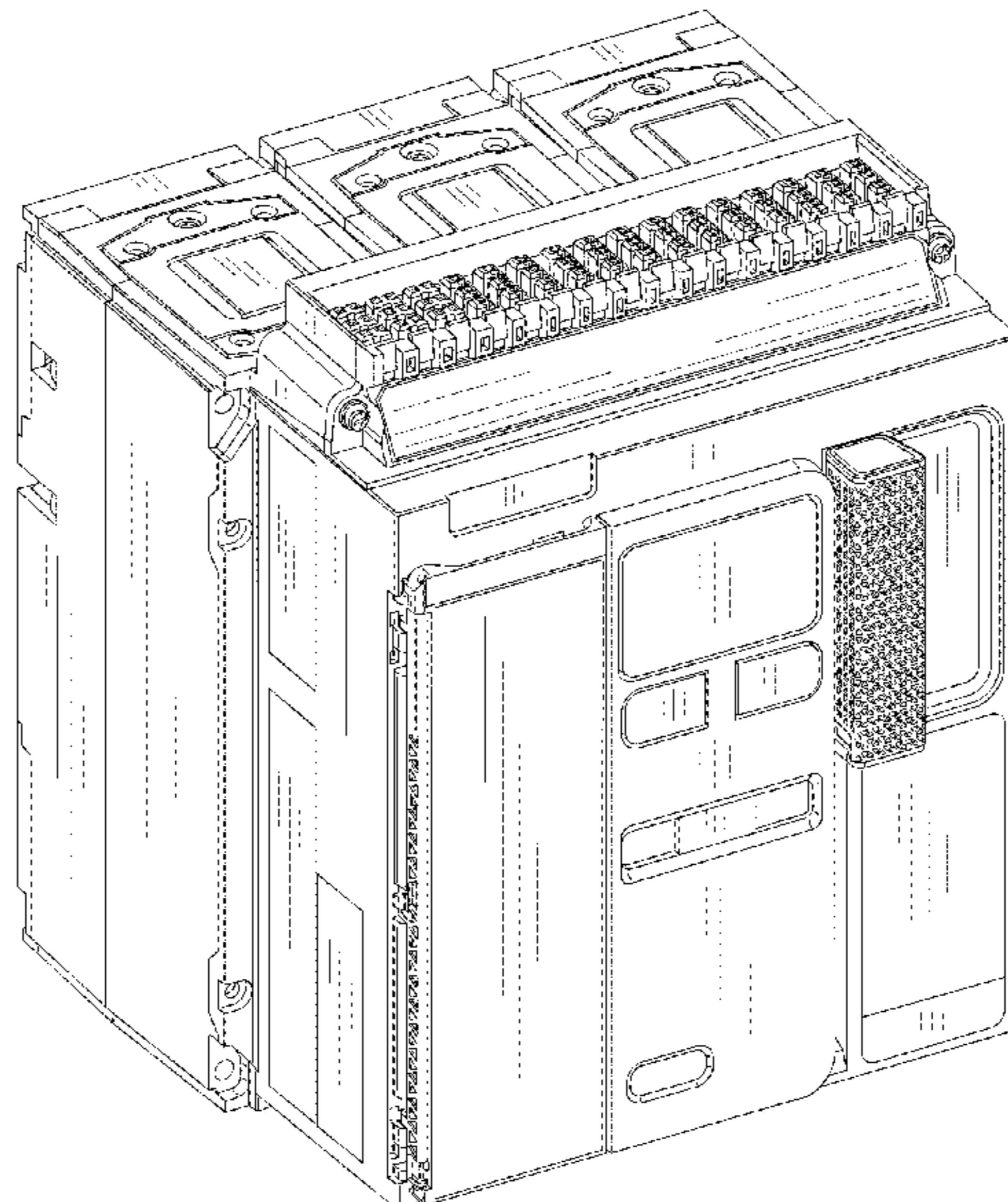
(57) **CLAIM**

The ornamental design for an electrical circuit breaker, as shown and described.

**DESCRIPTION**

FIG. 1 is a front elevation view of the electrical circuit breaker of the present design.  
FIG. 2 is a rear elevation view of the electrical circuit breaker of FIG. 1.  
FIG. 3 is a right elevation view of the electrical circuit breaker of FIG. 1.  
FIG. 4 is a left view of the electrical circuit breaker of FIG. 1.  
FIG. 5 is a top plan view of the electrical circuit breaker of FIG. 1.  
FIG. 6 is a bottom elevation view of the electrical circuit breaker of FIG. 1; and,  
FIG. 7 is a perspective view taken from the front, left side and top of an electrical circuit breaker of in FIG. 1.  
Portions drawn in broken lines form no part of the claimed design.

**1 Claim, 7 Drawing Sheets**



(56)

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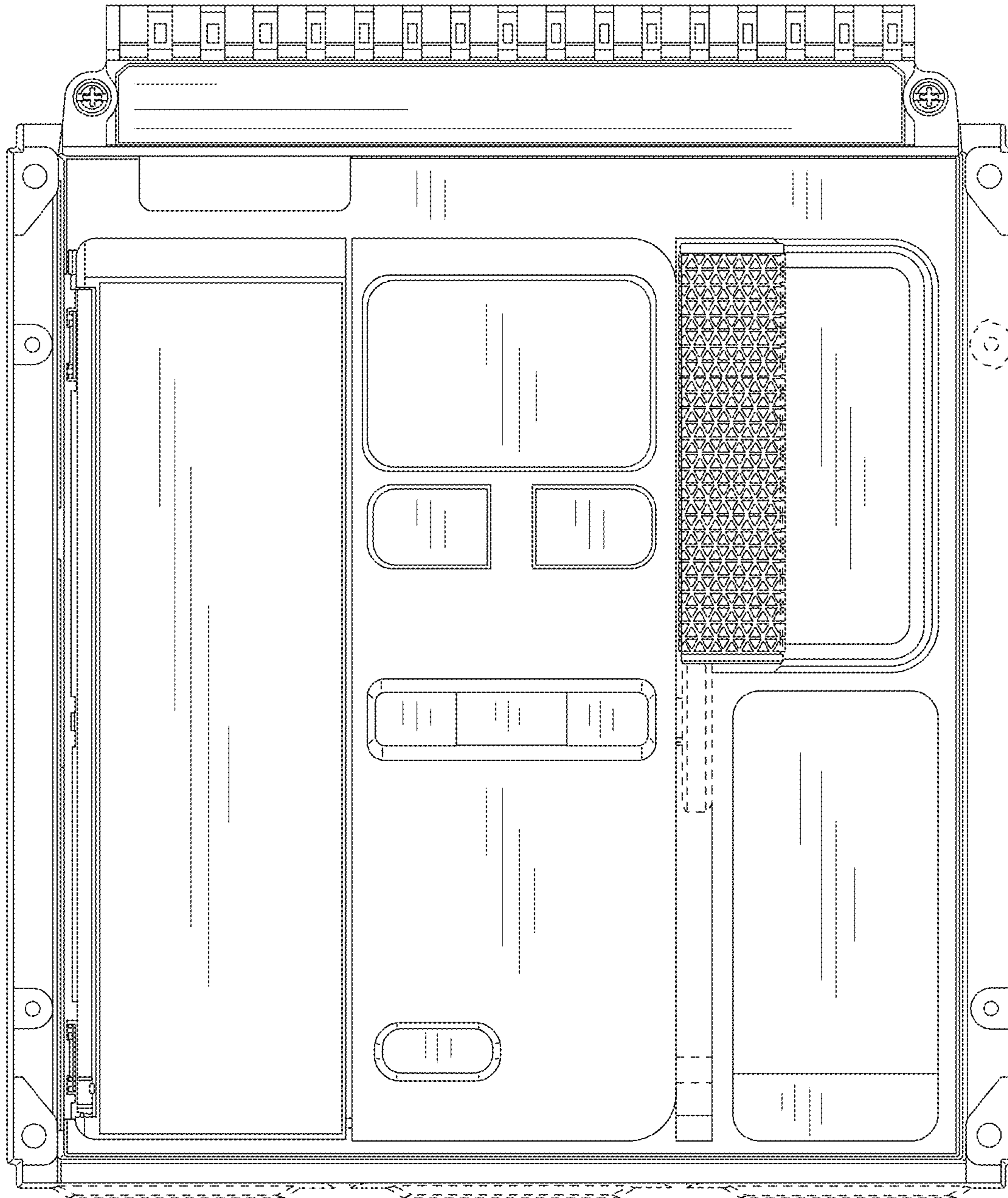


Fig. 1



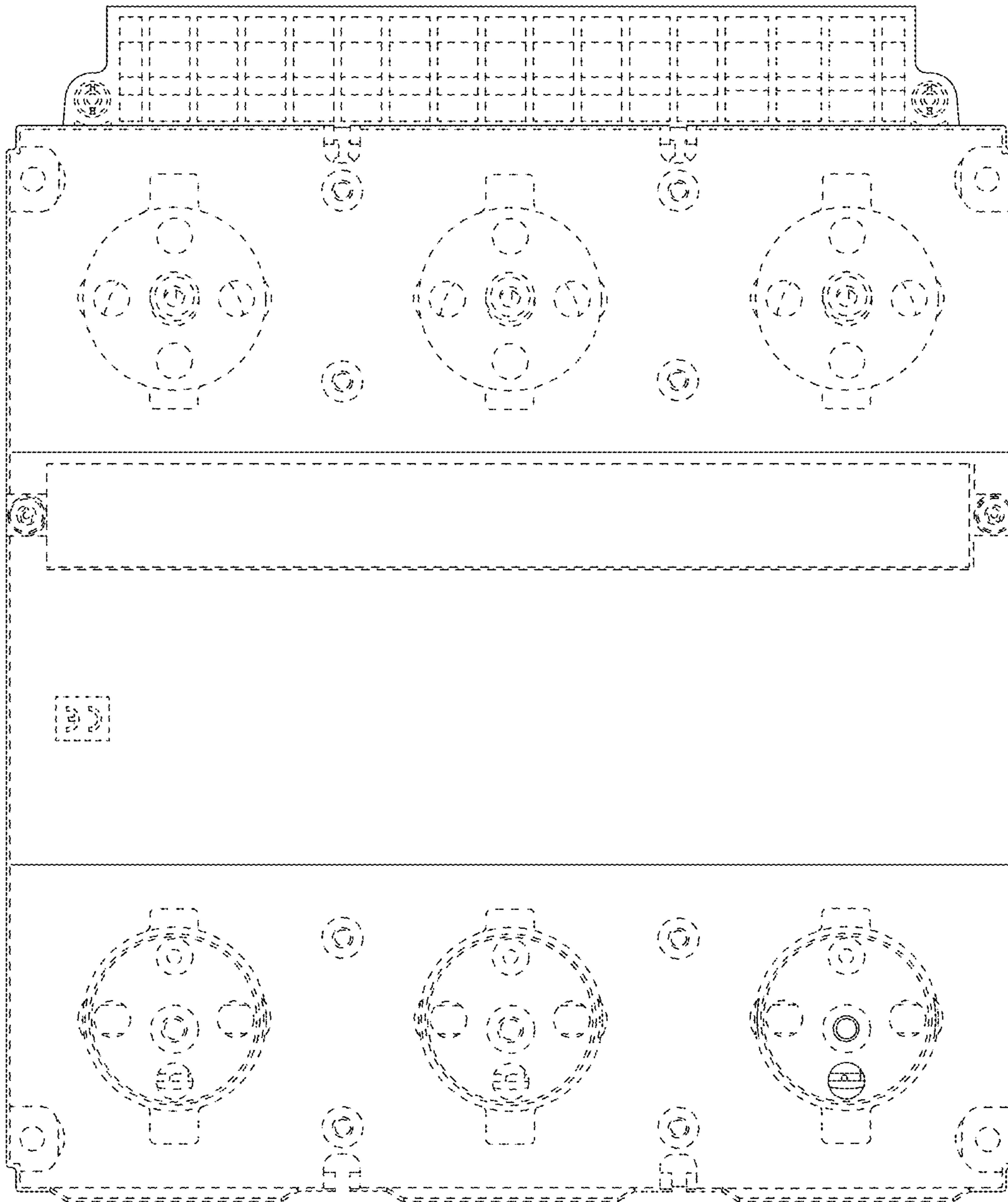


Fig. 2

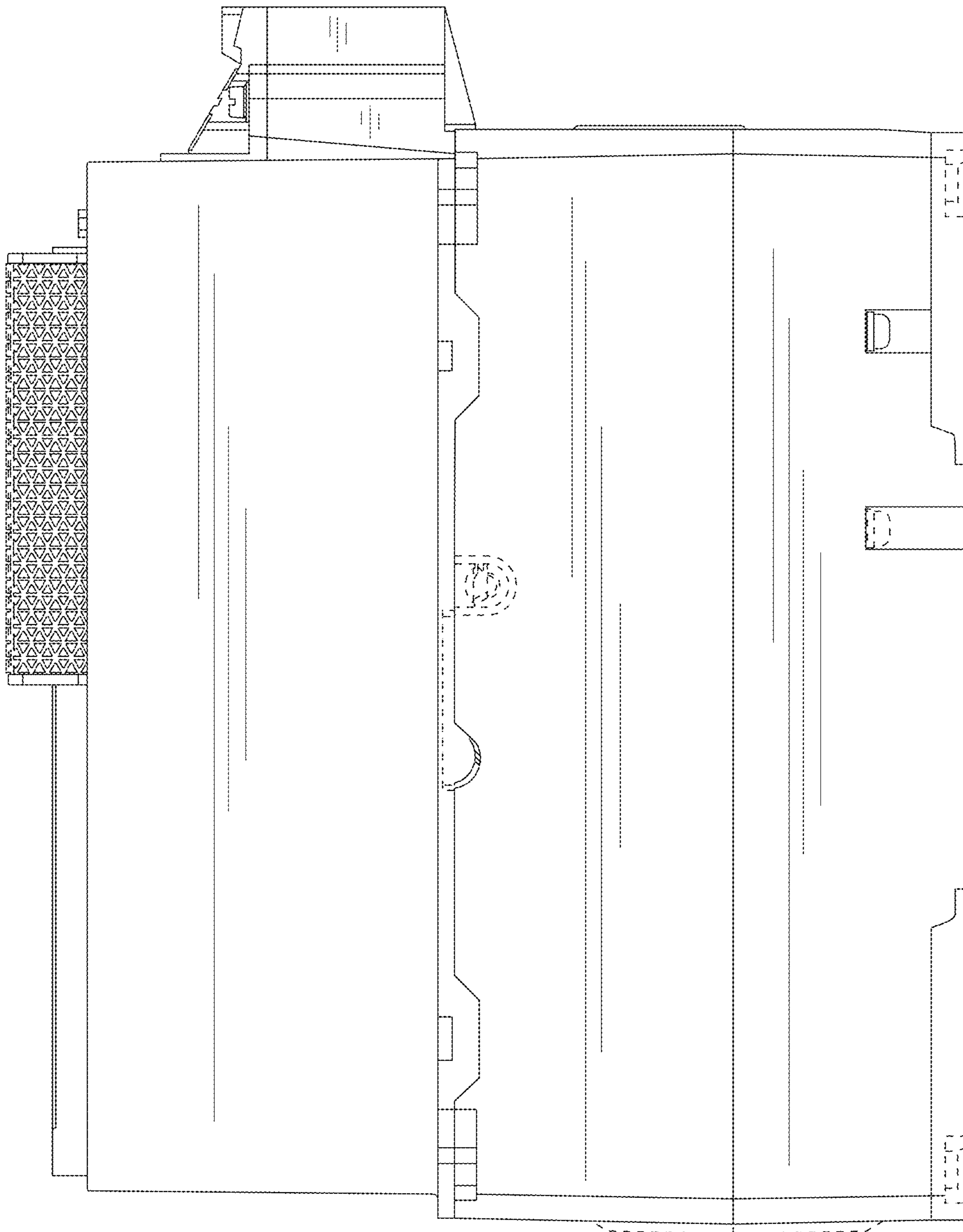


Fig. 3

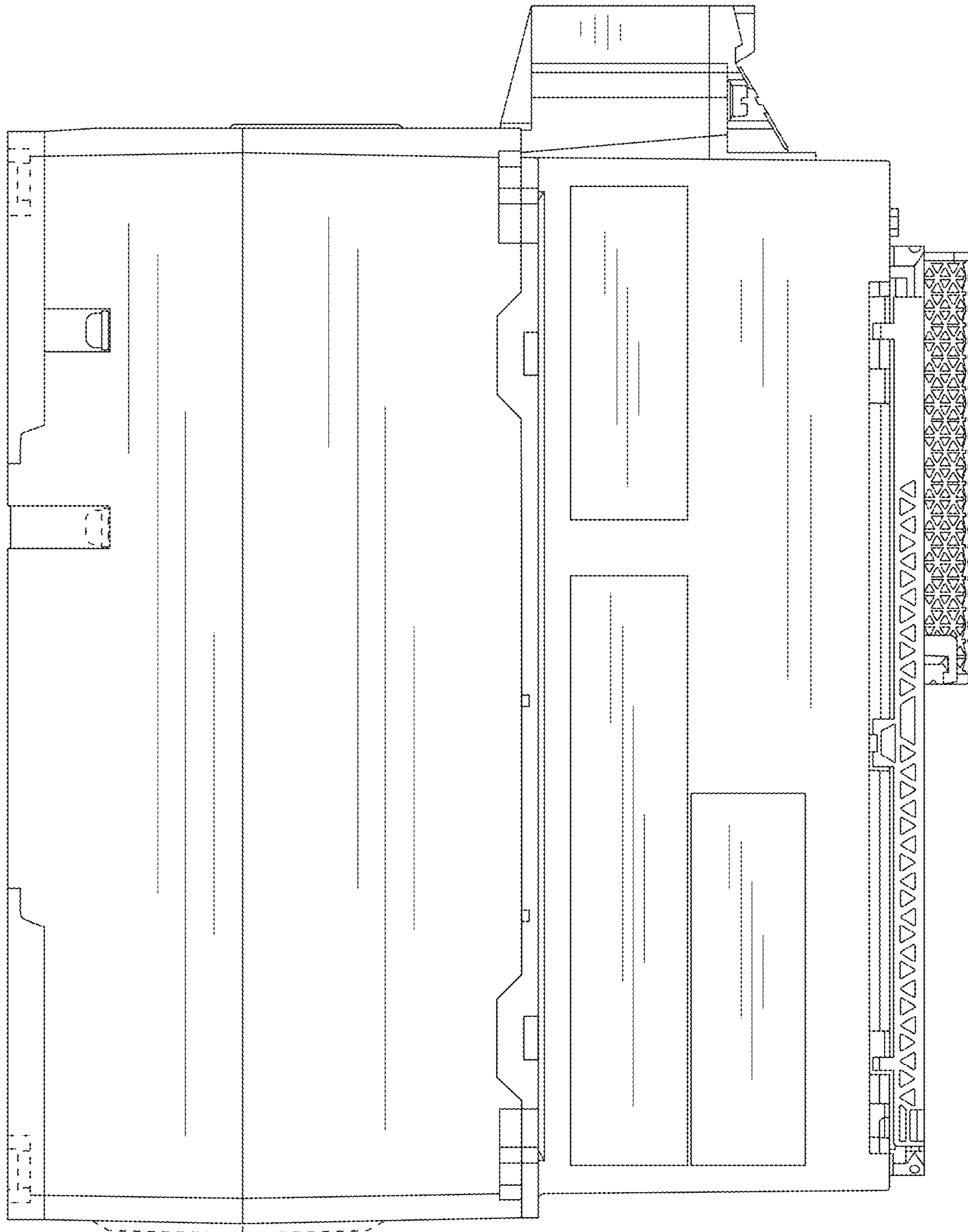


Fig. 4

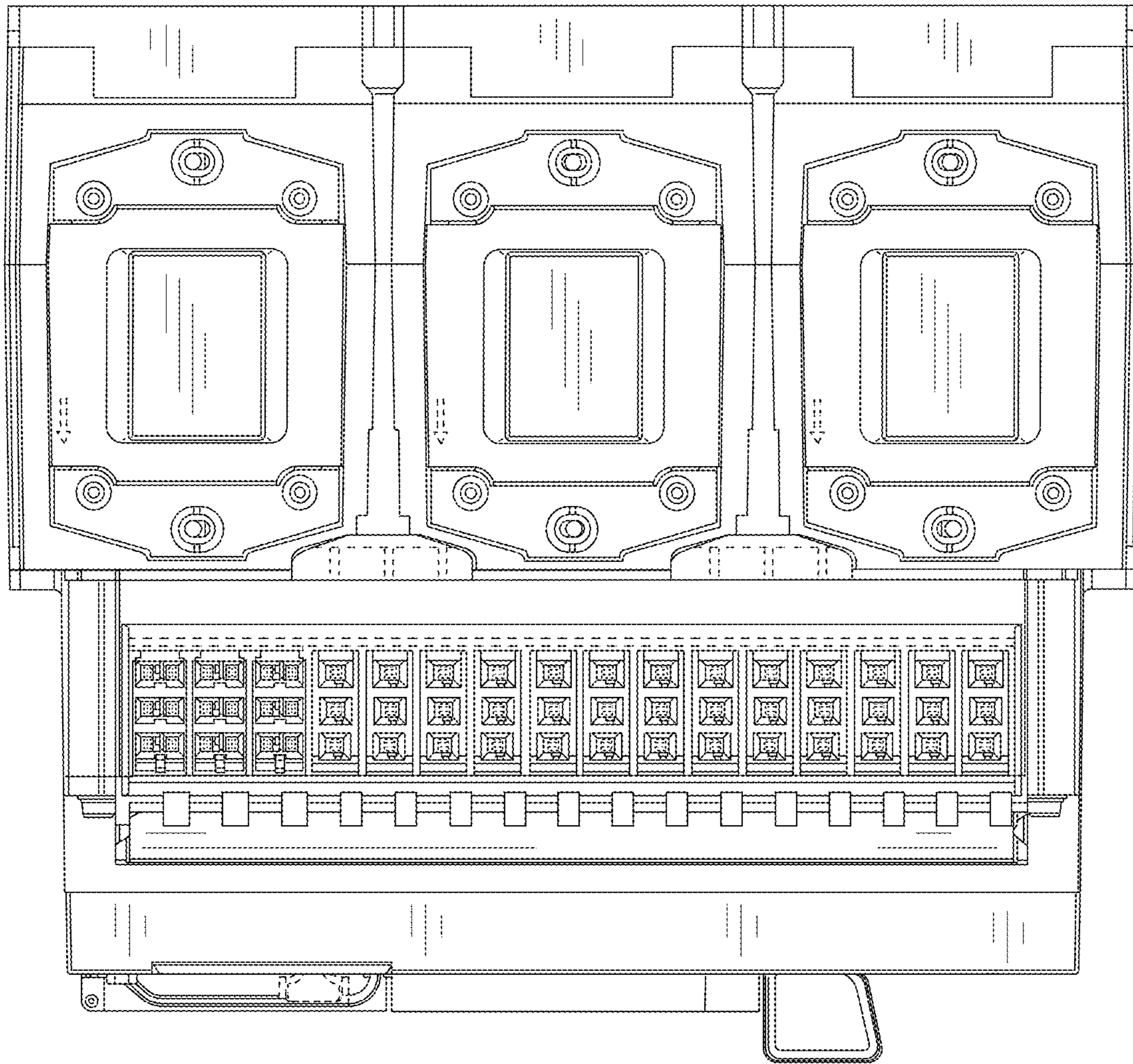


Fig. 5



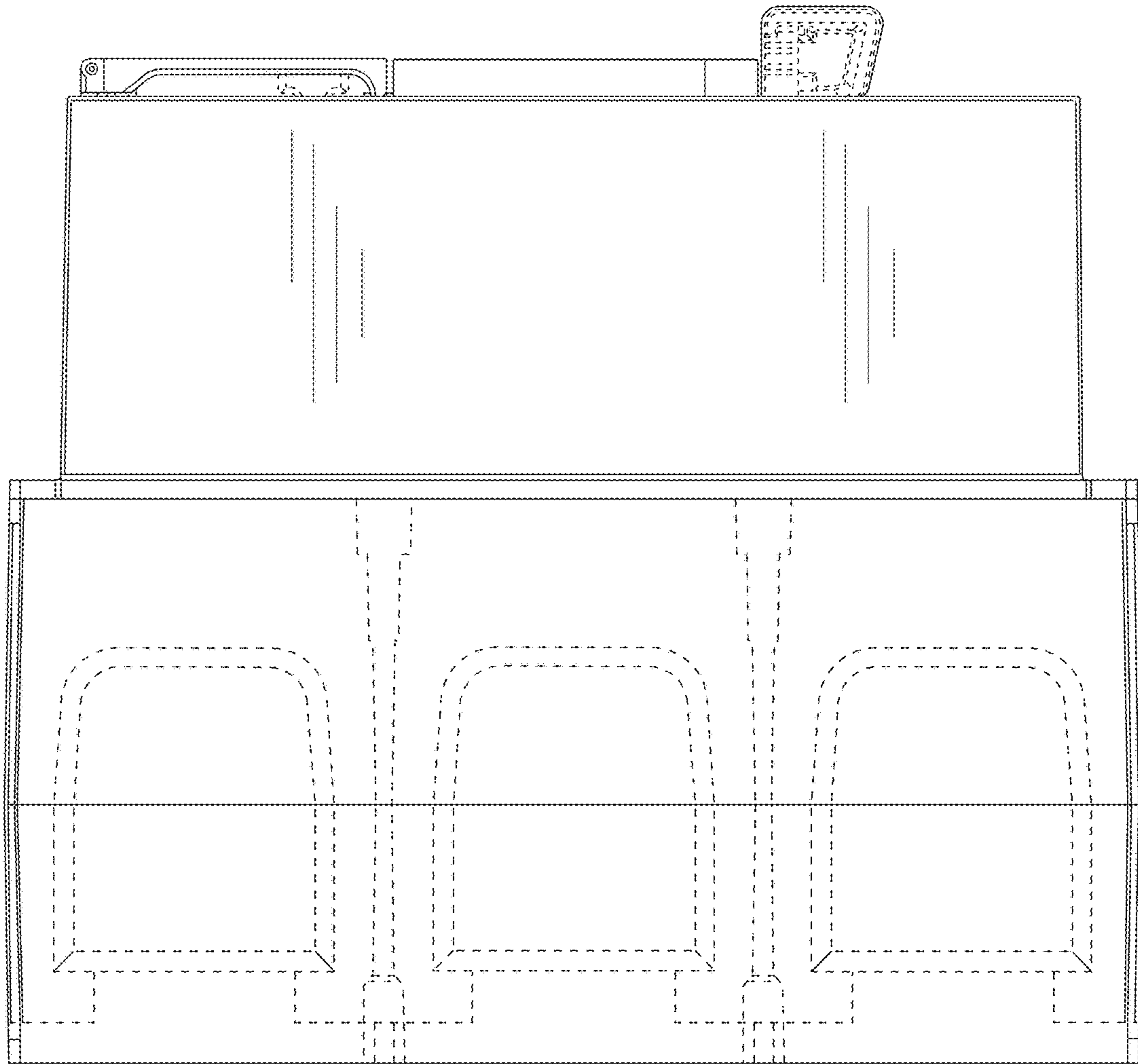


Fig. 6



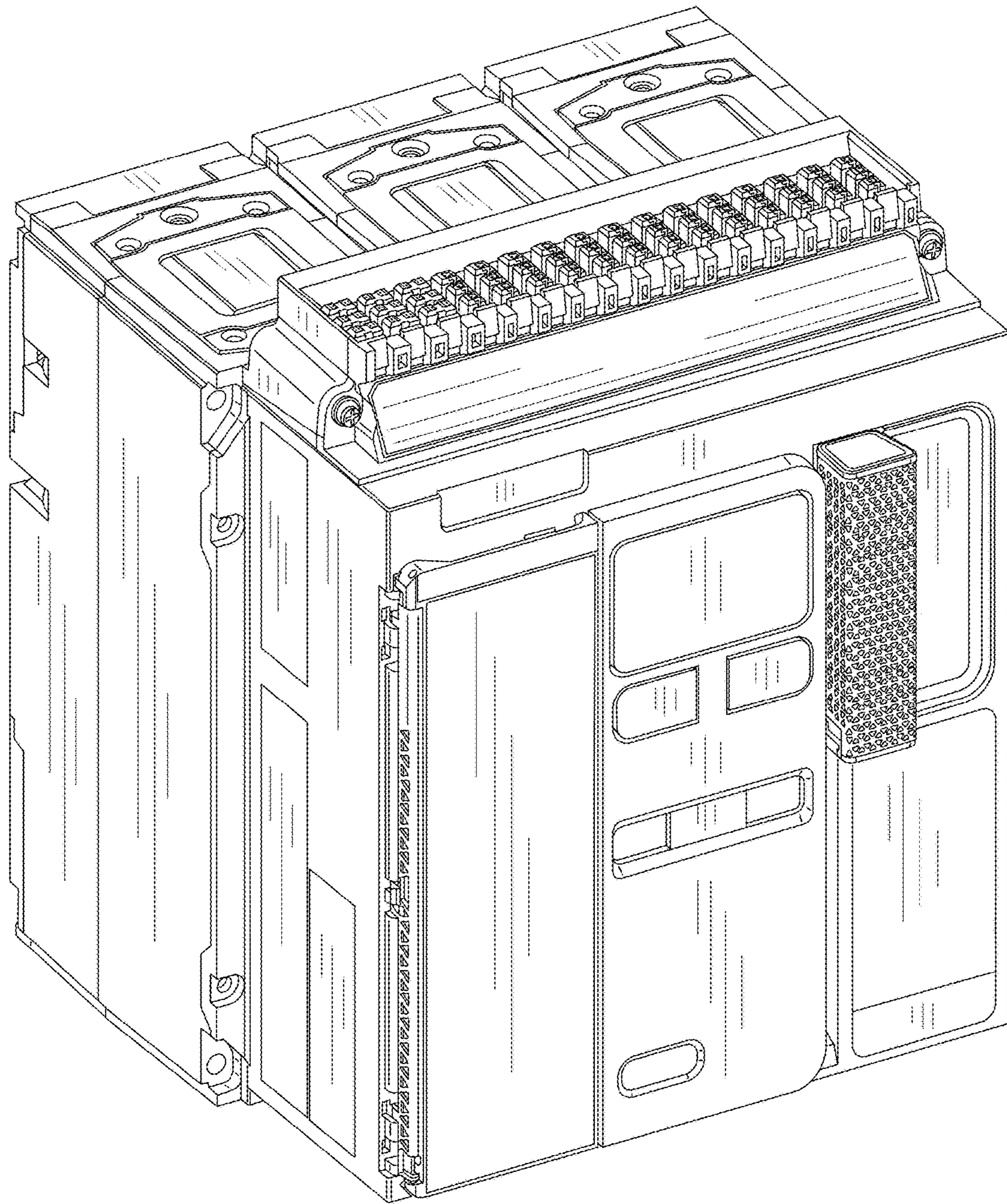


Fig. 7

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : D781,790 S  
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Change:

**\*(30) Foreign Application Priority Data**

June 12, 2015 (EP).....002717736-0001\*

To:

**---(30) Foreign Application Priority Data**

June 12, 2015 (EM).....002717736-0001---

Signed and Sealed this  
Twenty-seventh Day of June, 2017



Joseph Matal

*Performing the Functions and Duties of the  
Under Secretary of Commerce for Intellectual Property and  
Director of the United States Patent and Trademark Office*