



US00D779948S

(12) **United States Design Patent** (10) **Patent No.:** **US D779,948 S**
Hirose et al. (45) **Date of Patent:** **** Feb. 28, 2017**

(54) **CARRYING TRAY FOR SEMICONDUCTORS**

(71) Applicant: **MISHIMA KOSAN CO., LTD.**,
Kitakyushu-shi, Fukuoka (JP)

(72) Inventors: **Yoji Hirose**, Kitakyushu (JP);
Nobuyuki Harada, Kitakyushu (JP)

(73) Assignee: **MISHIMA KOSAN CO., LTD.**,
Kitakyushu-shi (JP)

(**) Term: **14 Years**

(21) Appl. No.: **29/507,517**

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(30) **Foreign Application Priority Data**

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(51) **LOC (10) Cl.** **09-07**

(52) **U.S. Cl.**
USPC **D9/456**

(58) **Field of Classification Search**
USPC D6/555, 704, 707, 707.24; D9/414,
D9/432-436, 438, 440, 443-445, 447,
(Continued)

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Primary Examiner — Robert M Spear

Assistant Examiner — Darcey E Heflin

(74) *Attorney, Agent, or Firm* — Westerman, Hattori,
Daniels & Adrian, LLP

(57) **CLAIM**

The ornamental design for a carrying tray for semiconductors, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of a carrying tray for semiconductors showing our new design.

FIG. 2 is a reference view taken from line 2-2 of FIG. 1.

FIG. 3 is a reference view taken from line 3-3 of FIG. 1.

FIG. 4 is a rear elevational view of the carrying tray for semiconductors.

FIG. 5 is a reference view taken from line 5-5 of FIG. 4.

FIG. 6 is a reference view taken from line 6-6 of FIG. 4.

FIG. 7 is a top plan view of the carrying tray for semiconductors.

FIG. 8 is a reference view taken from line 8-8 of FIG. 7.

FIG. 9 is a reference view taken from line 9-9 of FIG. 7.

FIG. 10 is a bottom plan view of the carrying tray for semiconductors.

FIG. 11 is a reference view taken along line 11-11 of FIG. 10.

FIG. 12 is a reference view taken along line 12-12 of FIG. 10.

FIG. 13 is a left-side view of the carrying tray for semiconductors.

FIG. 14 is a right-side view.

FIG. 15 is a partial enlarged view of FIG. 9, designated by area 15.

FIG. 16 is a partial enlarged view of FIG. 8, designated by area 16.

FIG. 17 is a partial enlarged view of FIG. 11, designated by area 17.

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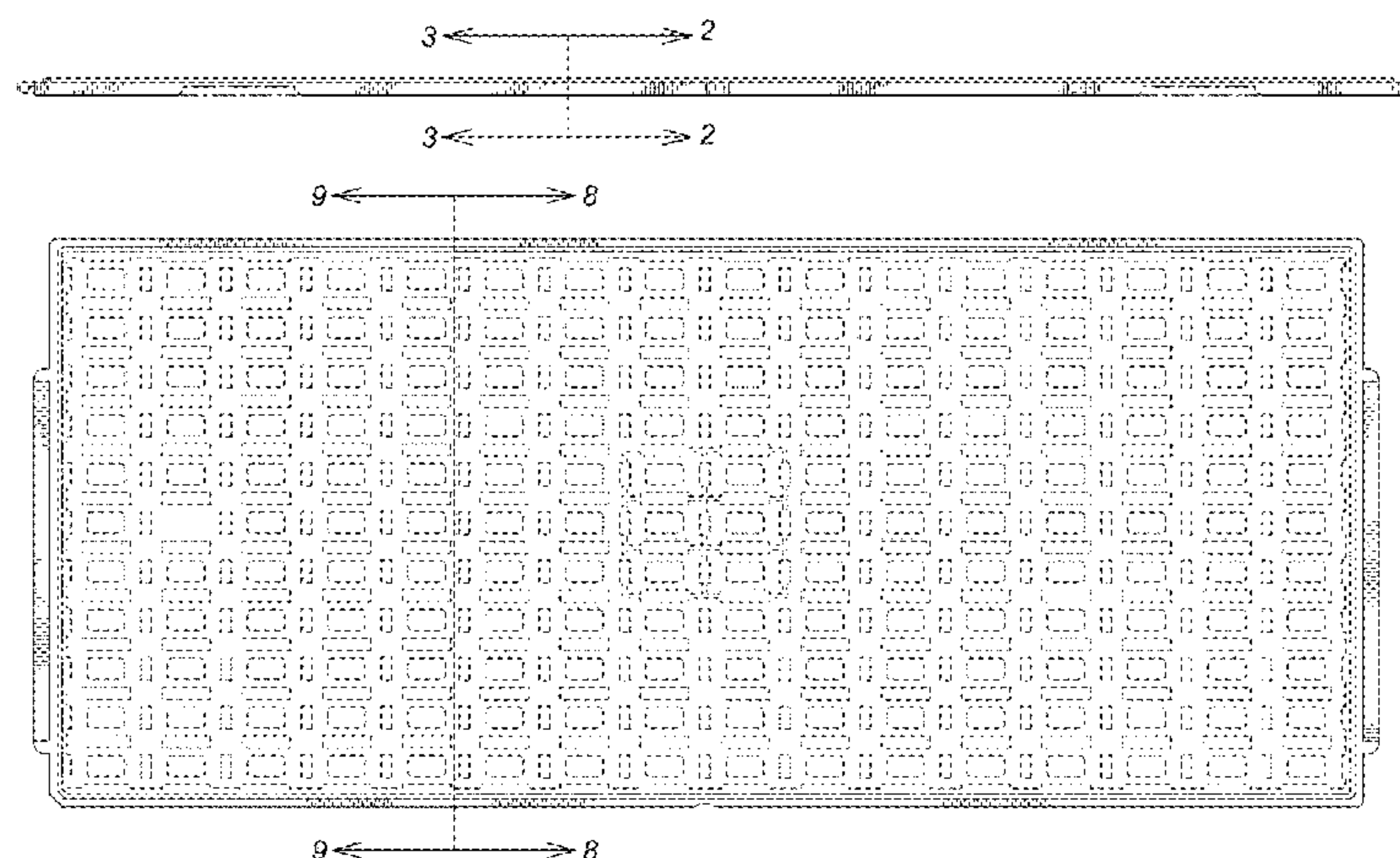


FIG. 18 is a partial enlarged view of FIG. 12, designated by area 18.

FIG. 19 is a partial enlarged view of FIG. 9, designated by area 19.

FIG. 20 is a partial enlarged view of FIG. 8, designated by area 20.

FIG. 21 is a cross sectional view of the carrying tray for semiconductors, taken along line 21-21 of FIG. 10.

FIG. 22 is a reference view taken along line 22-22 of FIG. 21.

FIG. 23 is a reference view taken along line 23-23 of FIG. 21.

FIG. 24 is a cross sectional view of FIG. 8, taken along line 24-24.

FIG. 25 is a partial enlarged view of FIG. 23, designated by area 25.

FIG. 26 is a partial enlarged view of FIG. 22, designated by area 26.

FIG. 27 is a partial enlarged view of FIG. 24, designated by area 27; and,

FIG. 28 is a partial enlarged view of FIG. 24, designated by area 28.

The evenly-spaced broken lines illustrate environmental structure and the dash-dot-dash lines illustrate a boundary of the claimed design for those figures, and both of them form no part of the claimed design.

1 Claim, 28 Drawing Sheets

(58) Field of Classification Search

USPC D9/449, 499, 517, 529, 736, 749, 456,
D9/530, 574, 760; D11/3, 4; 438/67, 78,
438/88, 121, 123, 125; D24/187, 216,
D24/224, 227

CPC H01L 23/552

See application file for complete search history.

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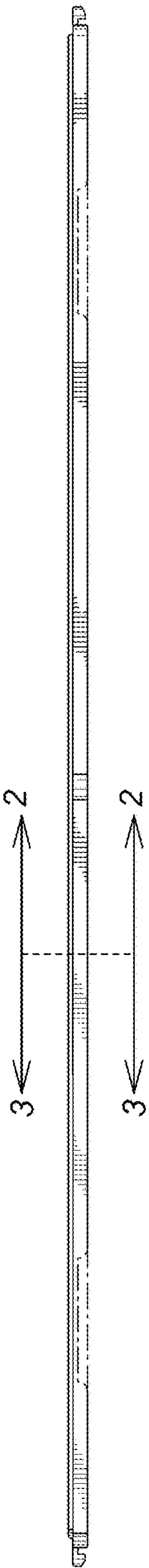


FIG. 1

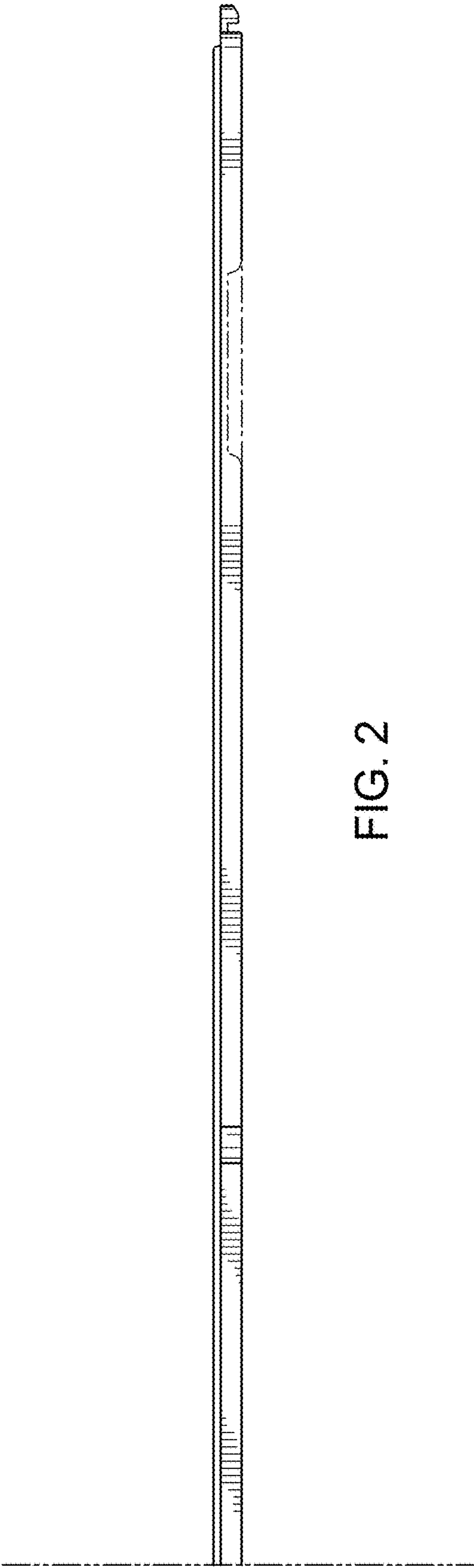


FIG. 2

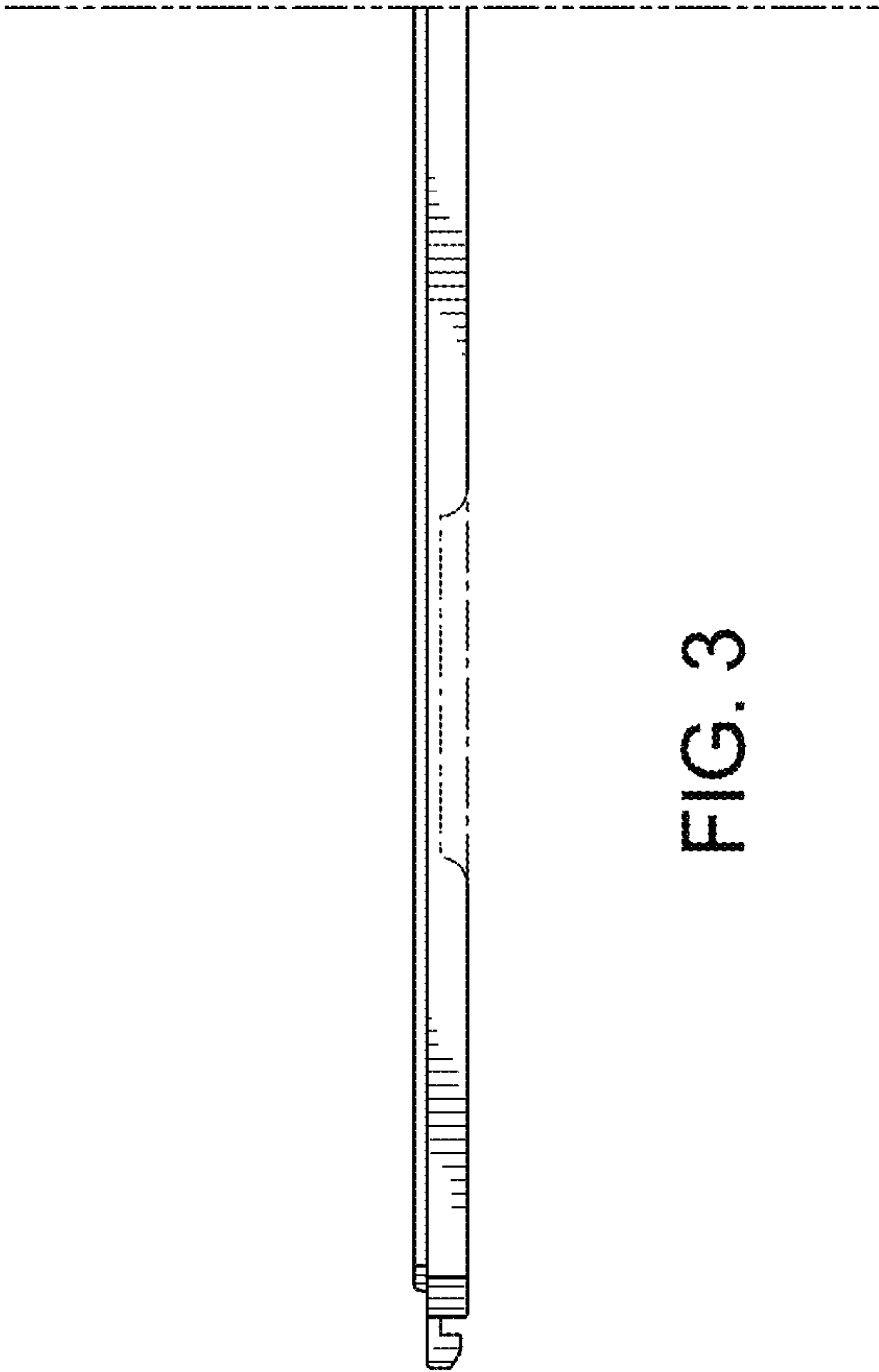


FIG. 3

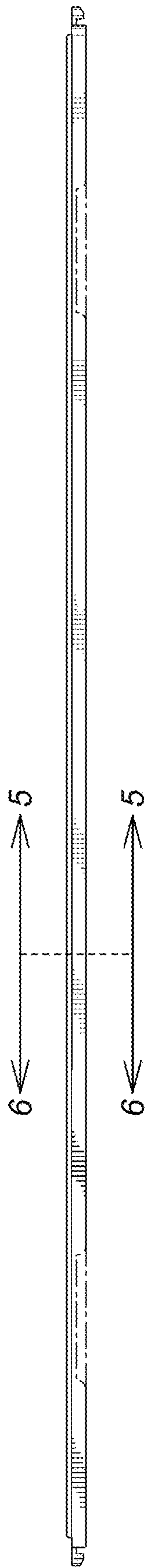


FIG. 4

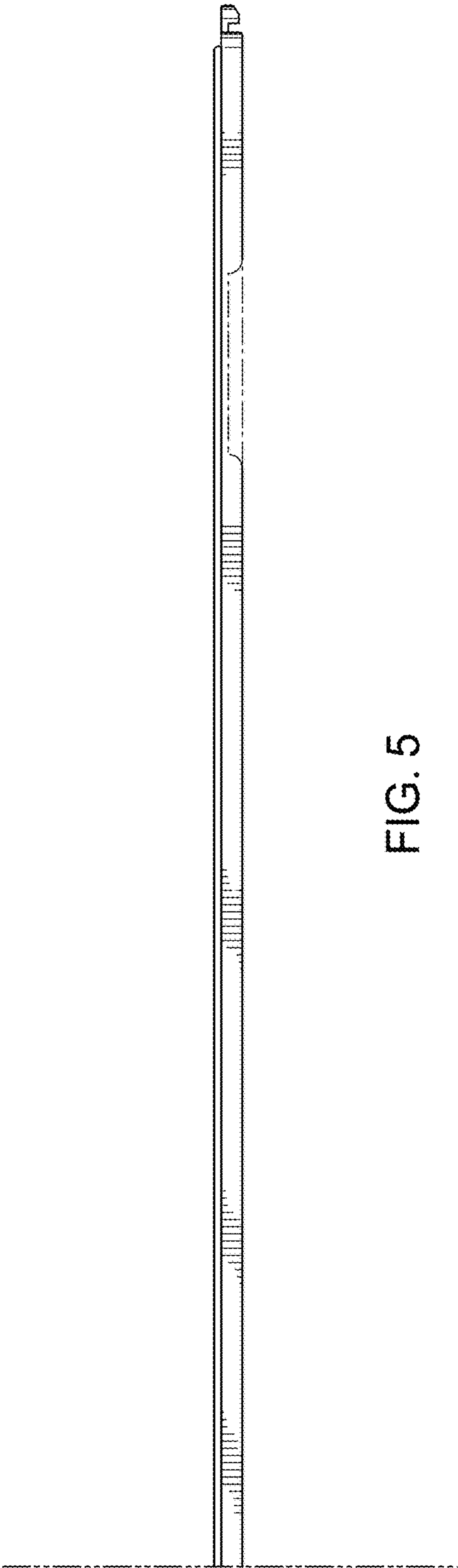


FIG. 5

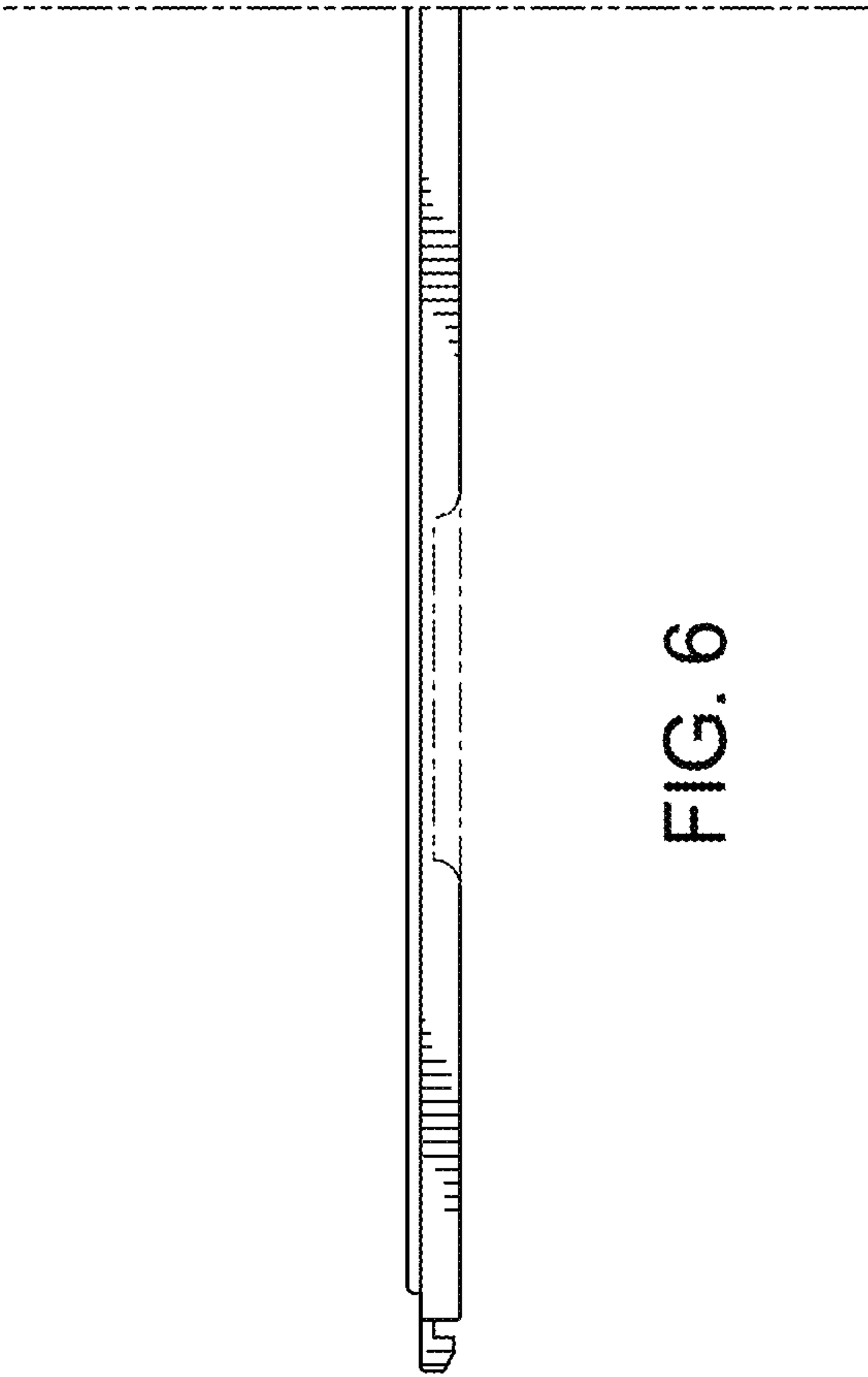


FIG. 6

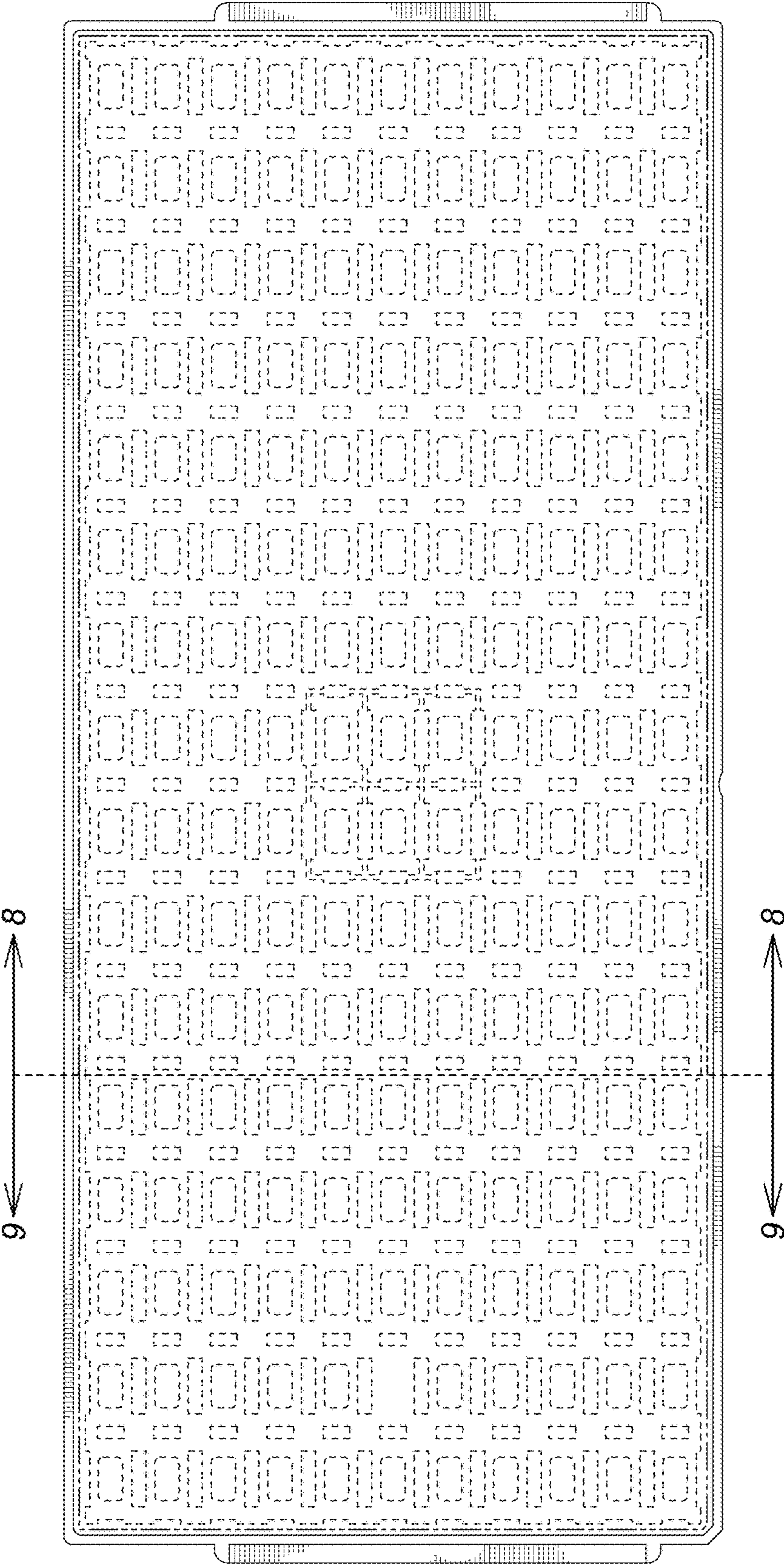
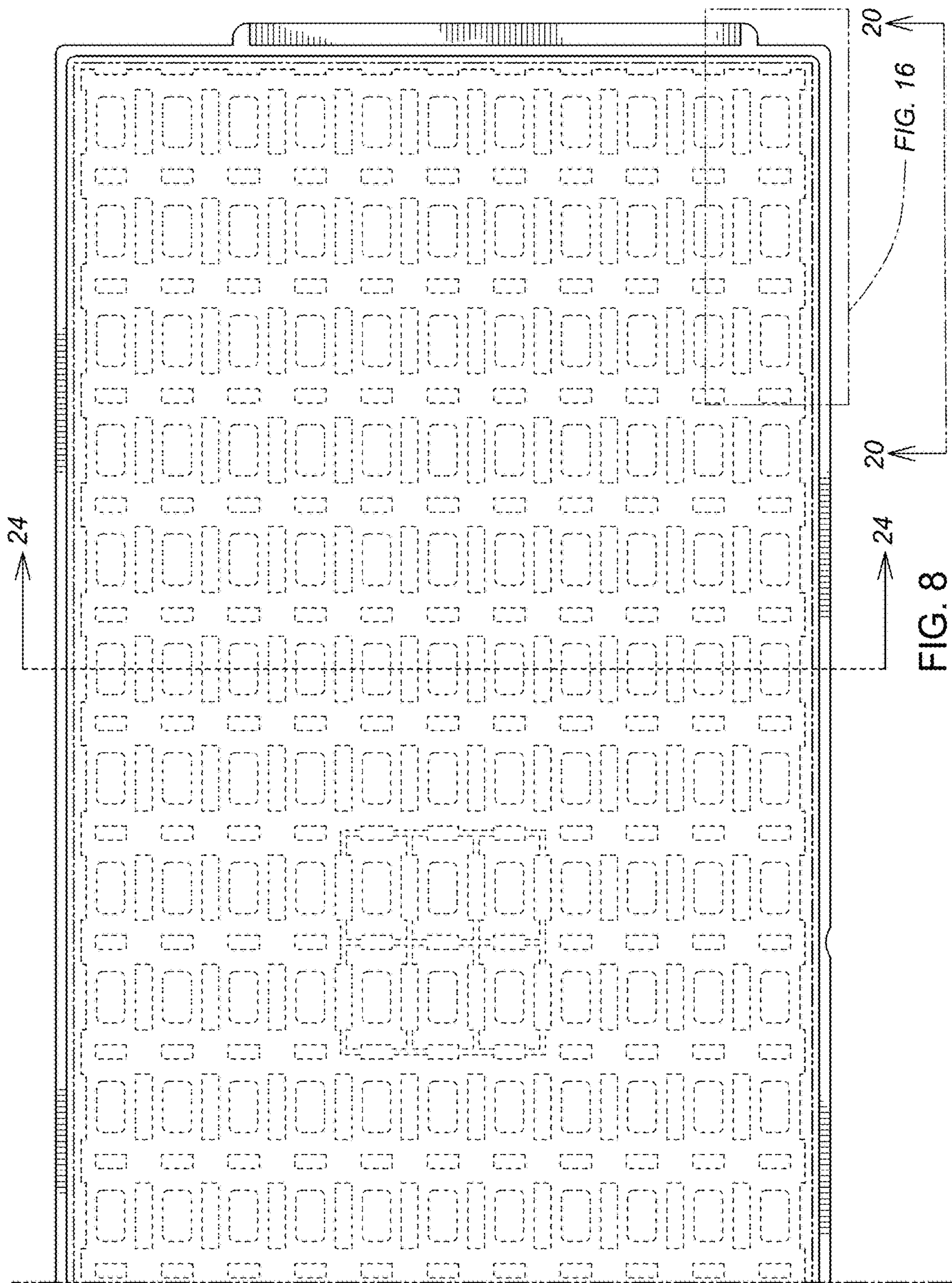


FIG. 7



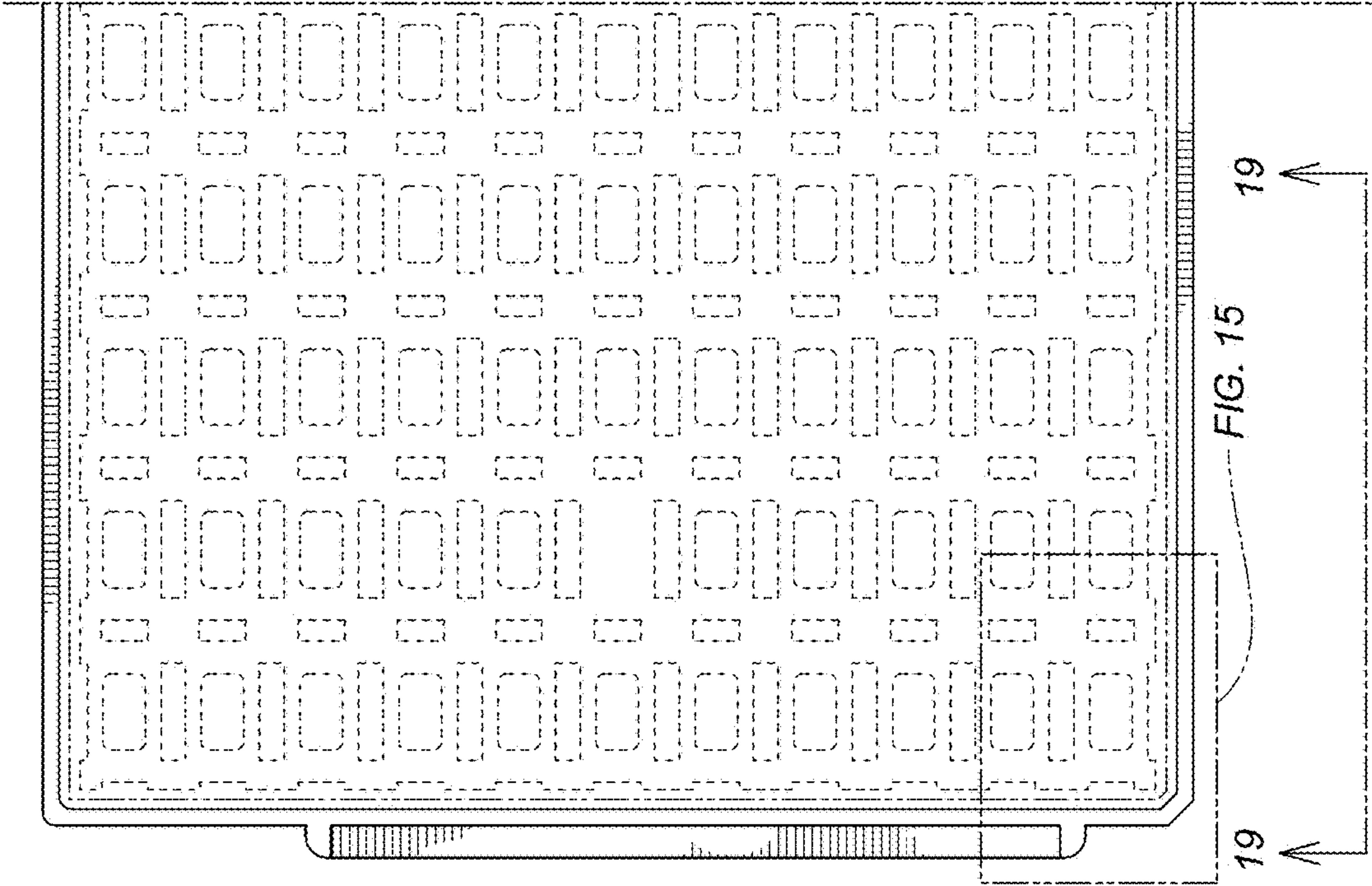


FIG. 9

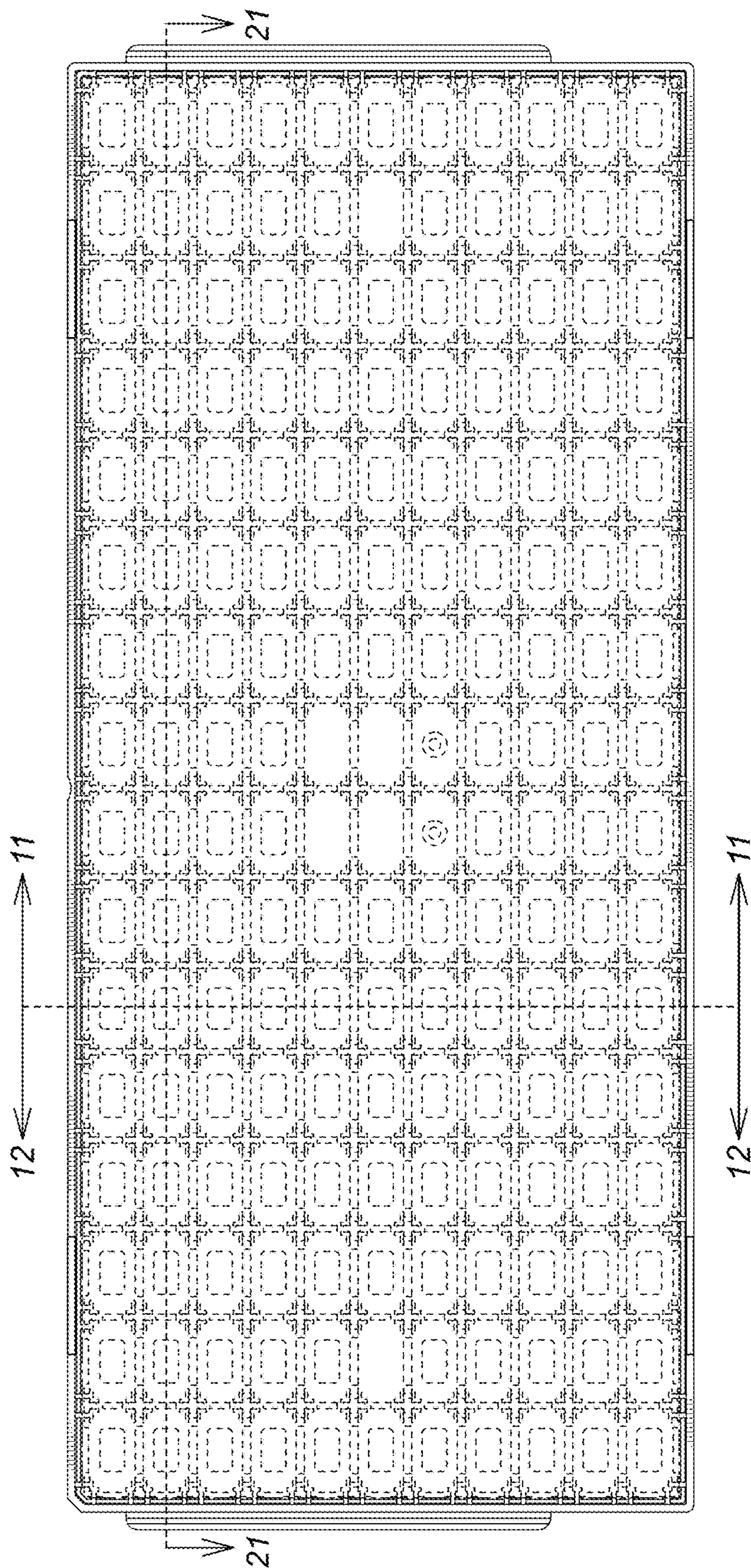
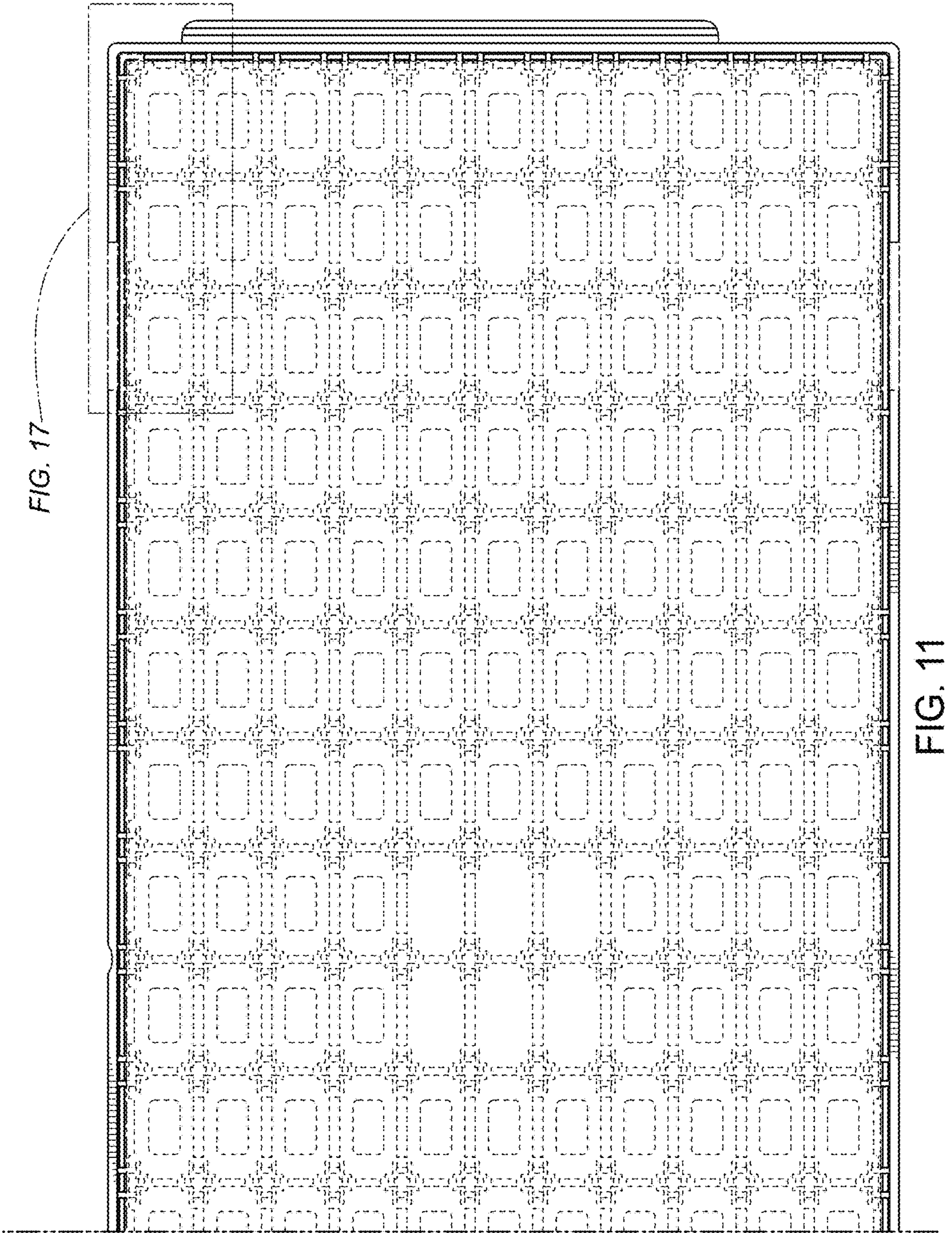


FIG. 10



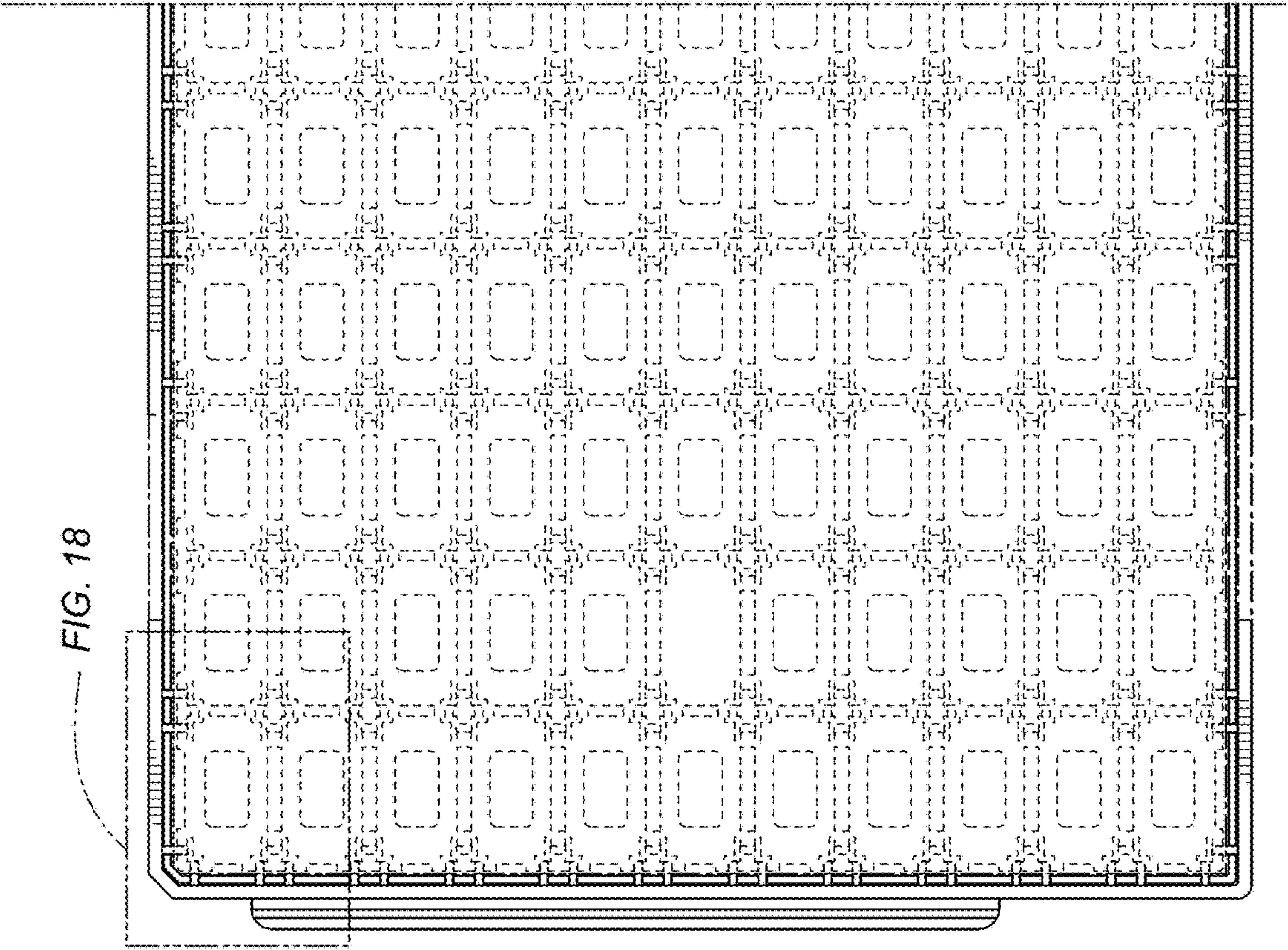


FIG. 12

FIG. 18



FIG. 13

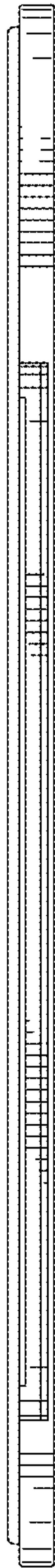


FIG. 14

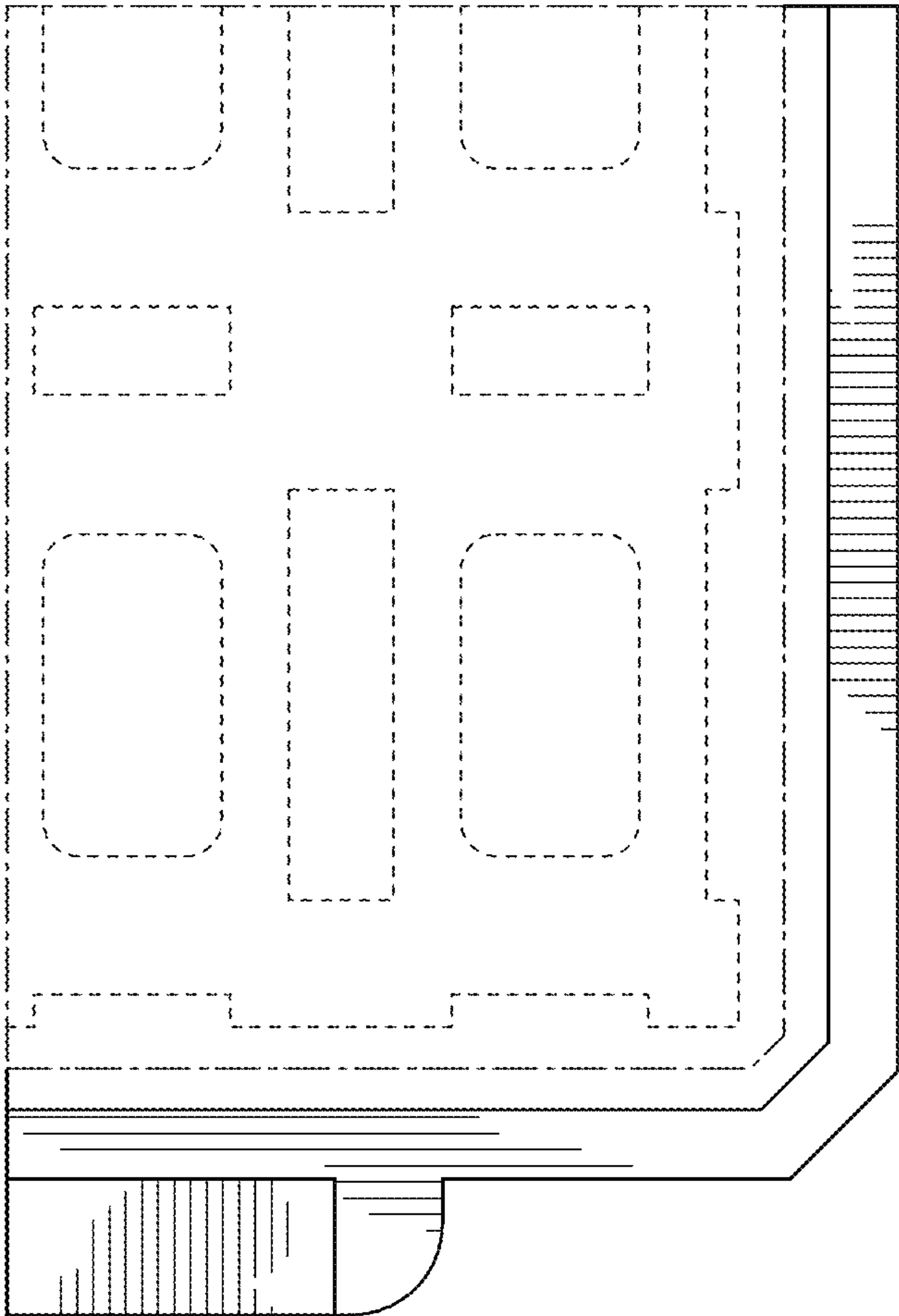


FIG. 15

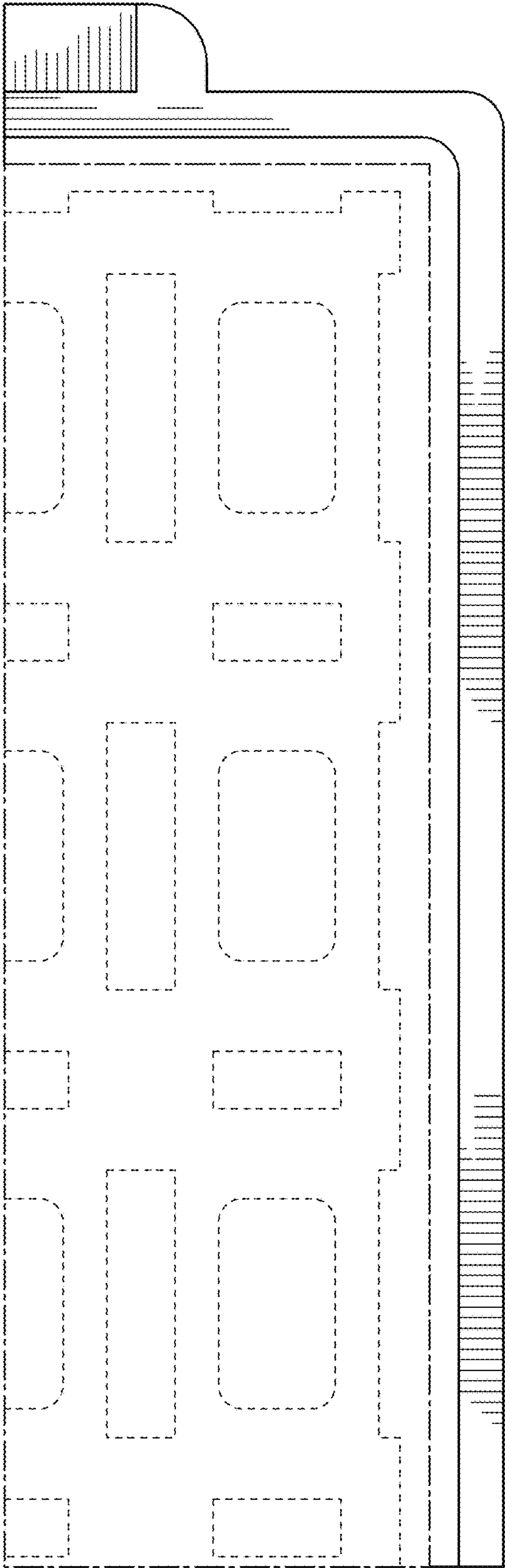


FIG. 16

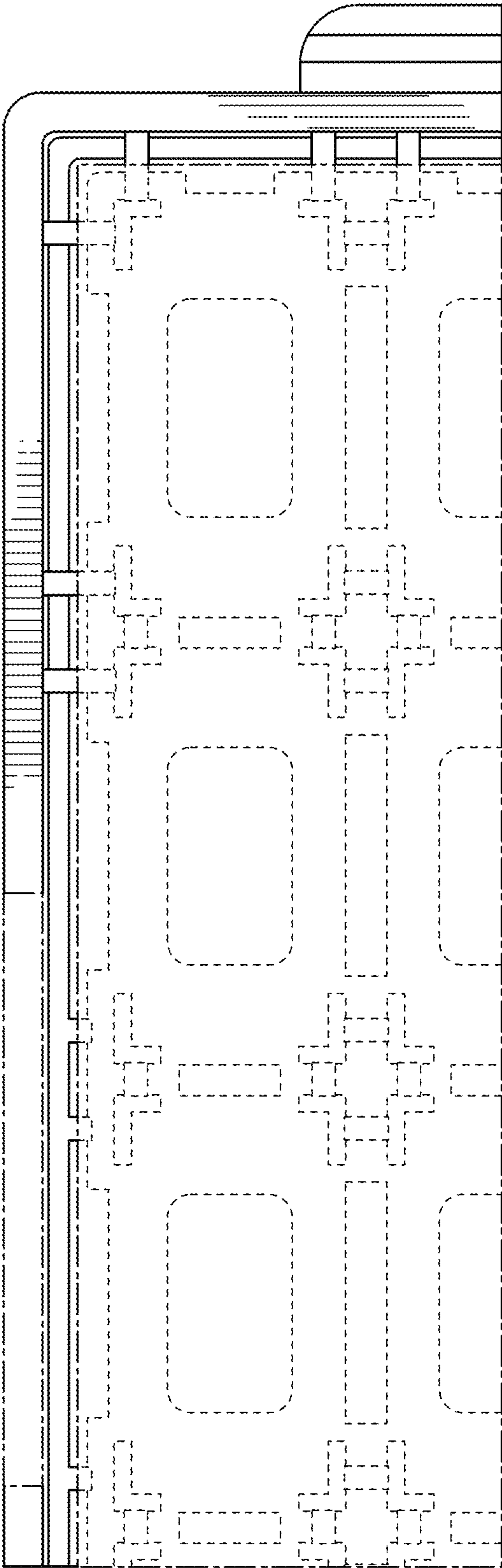


FIG. 17

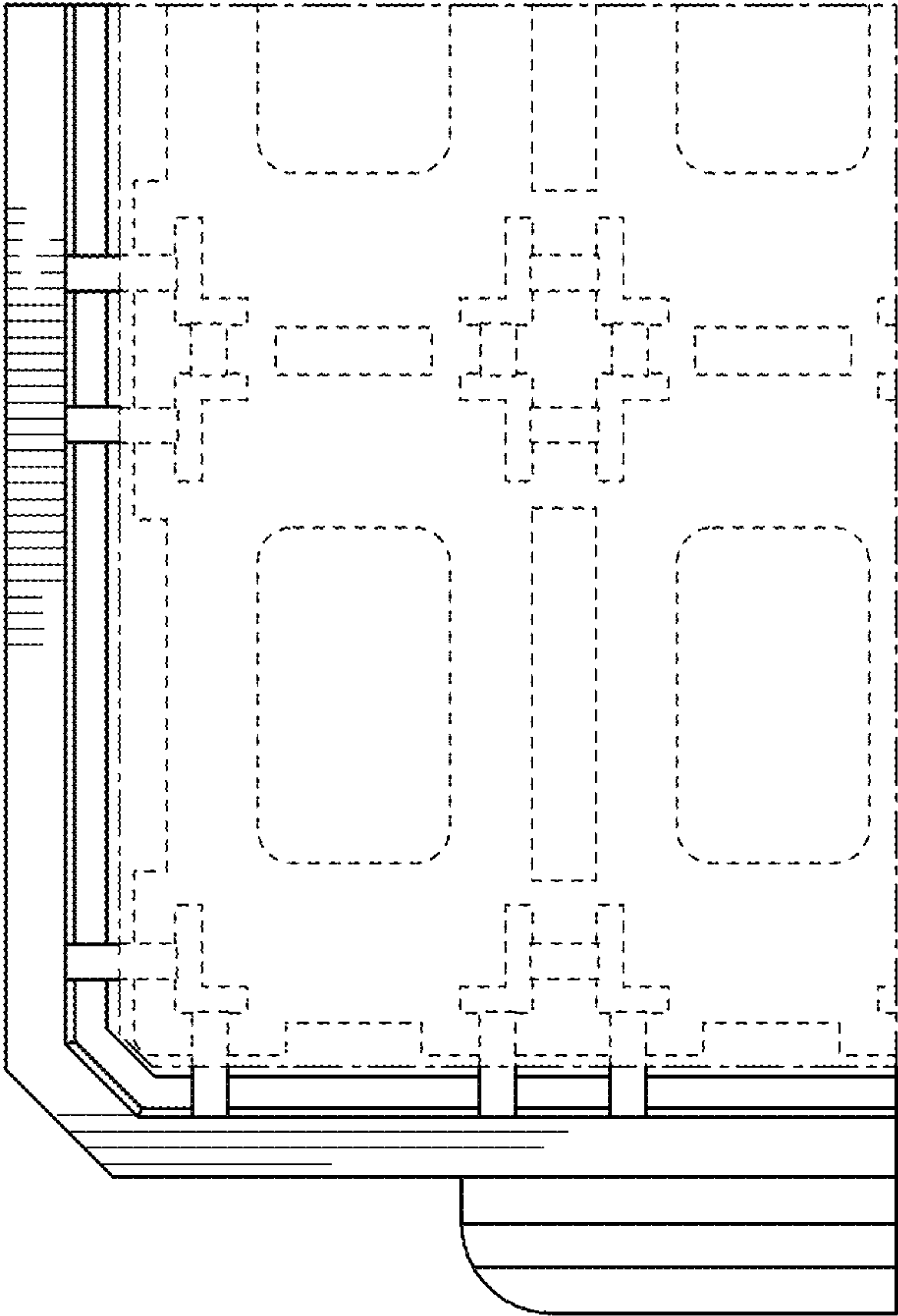


FIG. 18

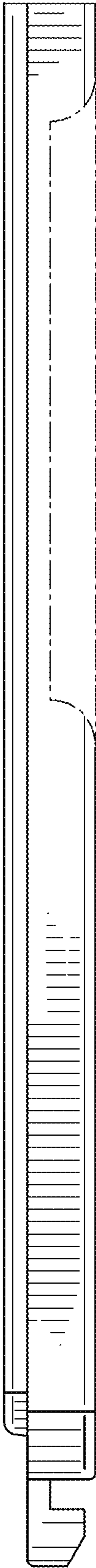


FIG. 19

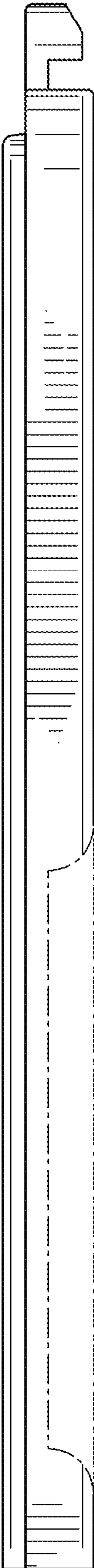


FIG. 20

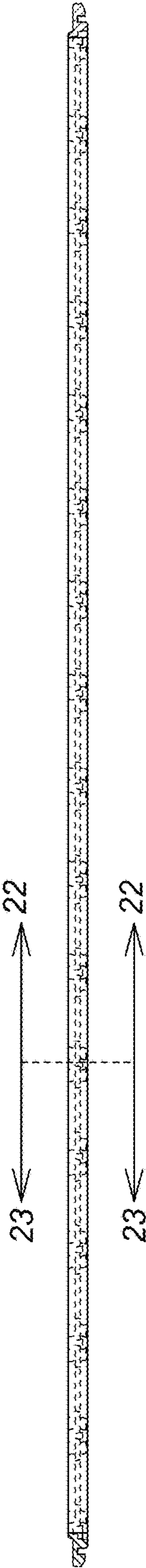


FIG. 21

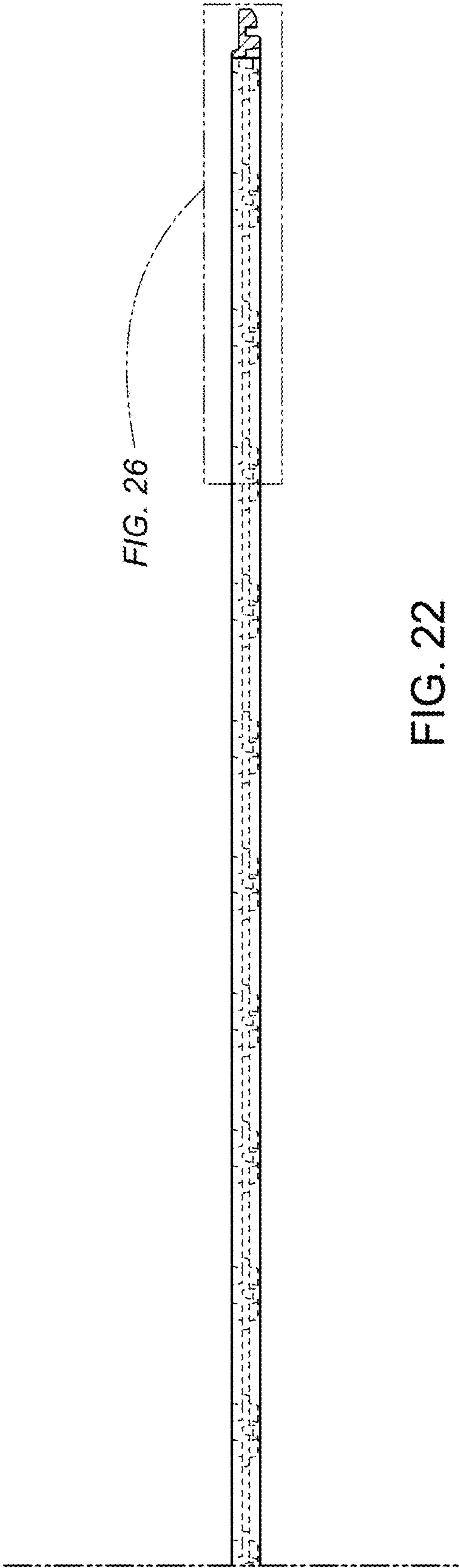


FIG. 22

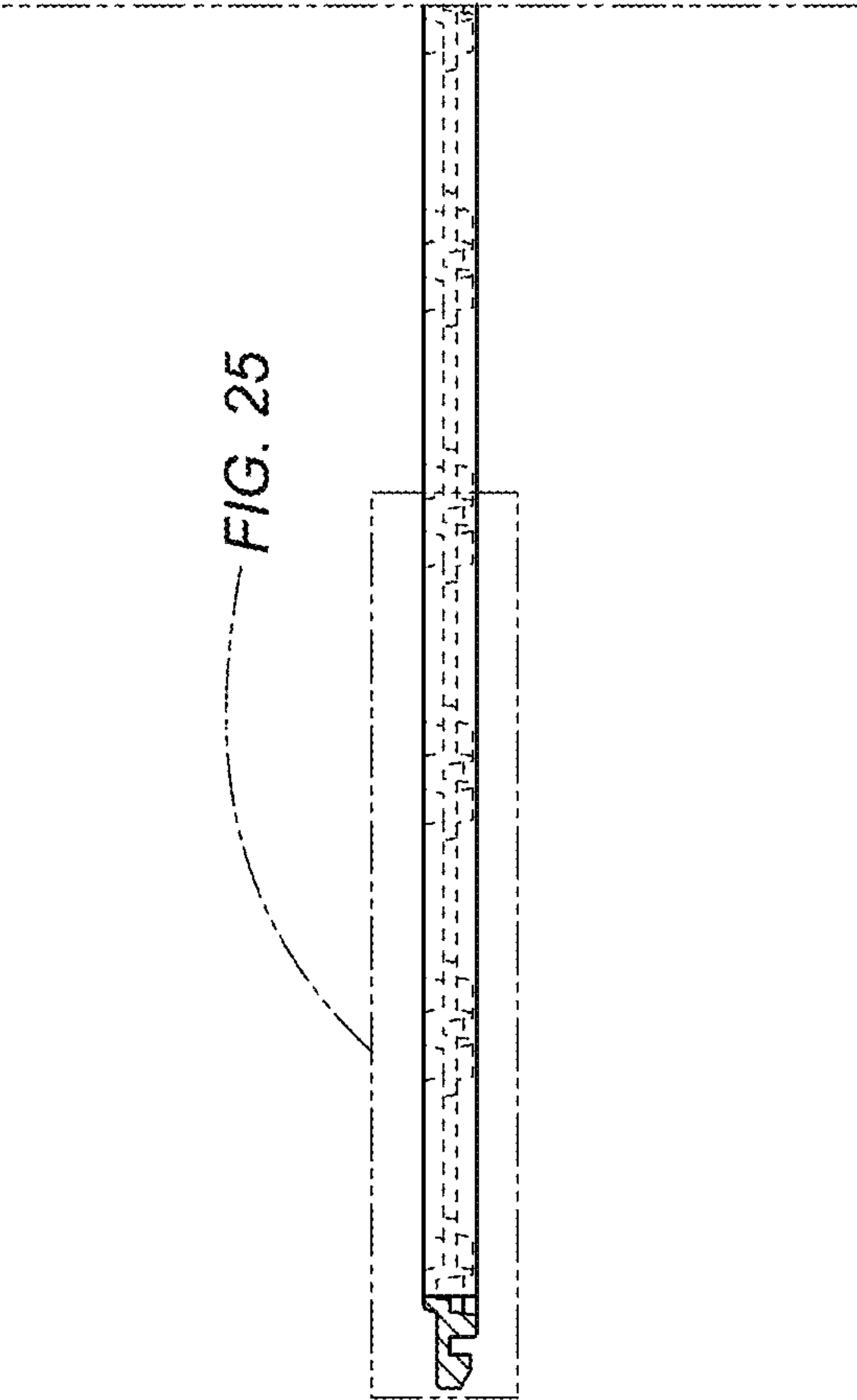


FIG. 25

FIG. 23

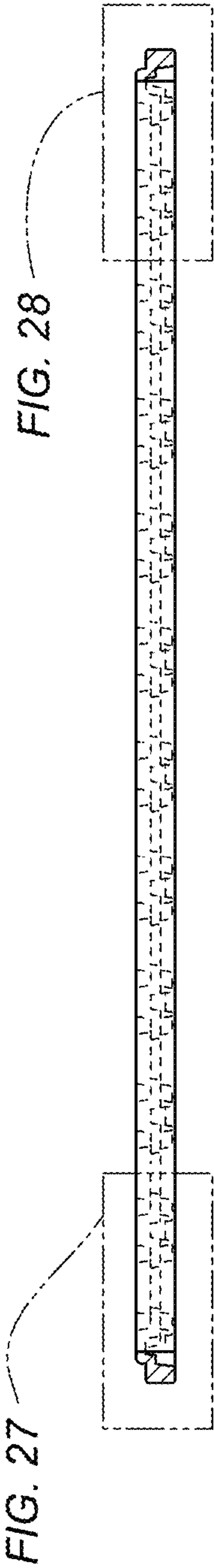


FIG. 24

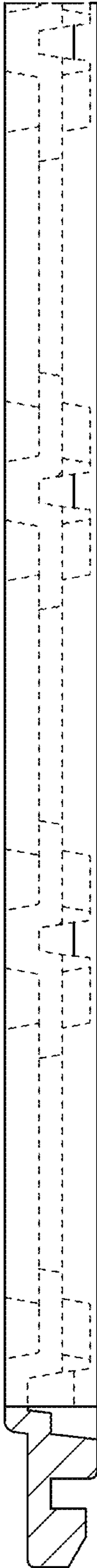


FIG. 25

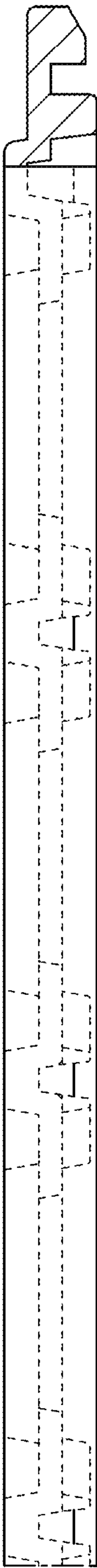


FIG. 26

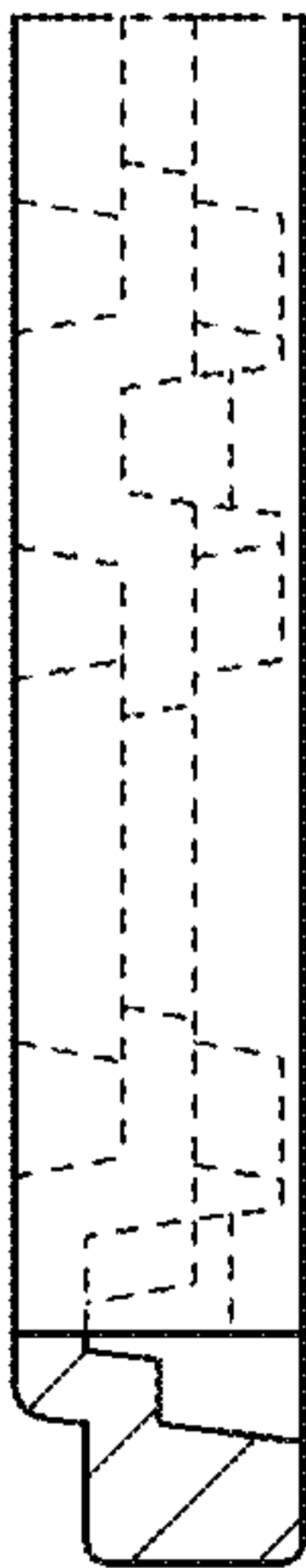


FIG. 27



FIG. 28