



US00D779483S

(12) **United States Design Patent** (10) **Patent No.:** **US D779,483 S**
Wall et al. (45) **Date of Patent:** **** Feb. 21, 2017**

(54) **ELECTRONIC DEVICE**
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(73) Assignee: **Amazon Technologies, Inc.**, Seattle, WA (US)
(**) Term: **15 Years**
(21) Appl. No.: **29/542,764**
(22) Filed: **Oct. 16, 2015**
(51) **LOC (10) Cl.** **14-02**
(52) **U.S. Cl.**
USPC **D14/341**
(58) **Field of Classification Search**
USPC . D14/341-347, 137, 138 R, 138 AA, 138 C, D14/138 G, 496, 203.1, 203.3, 203.4, D14/203.7, 129, 130, 147, 218, 248, 389, D14/388, 426, 420, 440; D10/65, 104.1; D18/6, 7; D21/324, 329, 330; 455/556.1, 455/556.2, 566, 575.1, 90.3; 379/433.04, 379/433.01, 433.06, 916; 345/173, 901, 345/905; 361/679.26, 670.3, 679.55, 361/679.56
CPC H04M 1/00; H04M 11/066; H04M 1/2478; H04M 1/72519; H04M 2201/38; H04M 2250/22; G06F 15/00
See application file for complete search history.

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(57) **CLAIM**

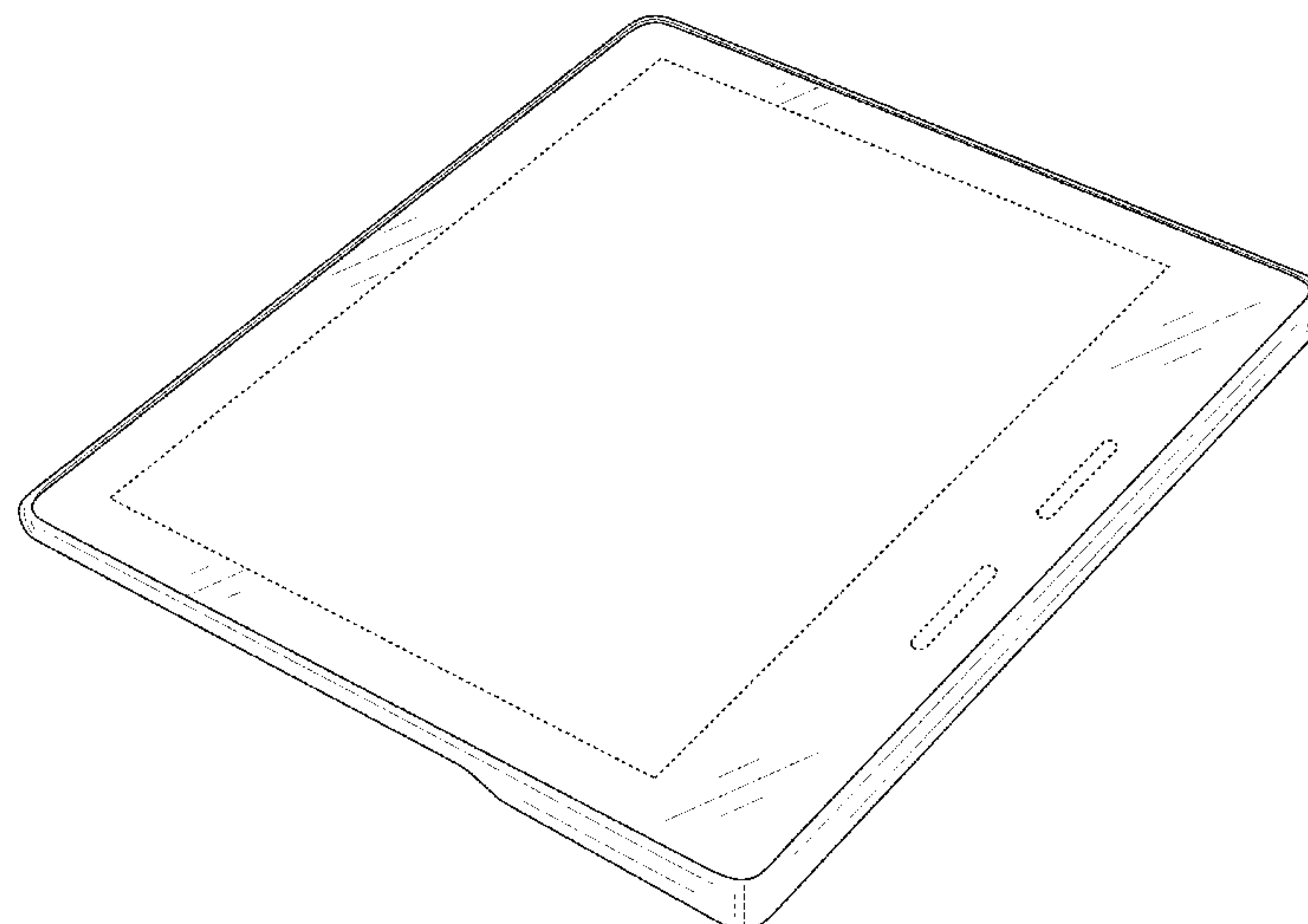
The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a first perspective view of an electronic device, taken from the front;
FIG. 2 is a second perspective view of the electronic device, taken from the back;
FIG. 3 is a front view of the electronic device;
FIG. 4 is a back view of the electronic device;
FIG. 5 is a right-side view of the electronic device;
FIG. 6 is a left-side view of the electronic device;
FIG. 7 is a top view of the electronic device; and,
FIG. 8 is a bottom view of the electronic device.
The broken lines in FIGS. 1-8 illustrate portions of the electronic device that form no part of the claimed design.

1 Claim, 6 Drawing Sheets

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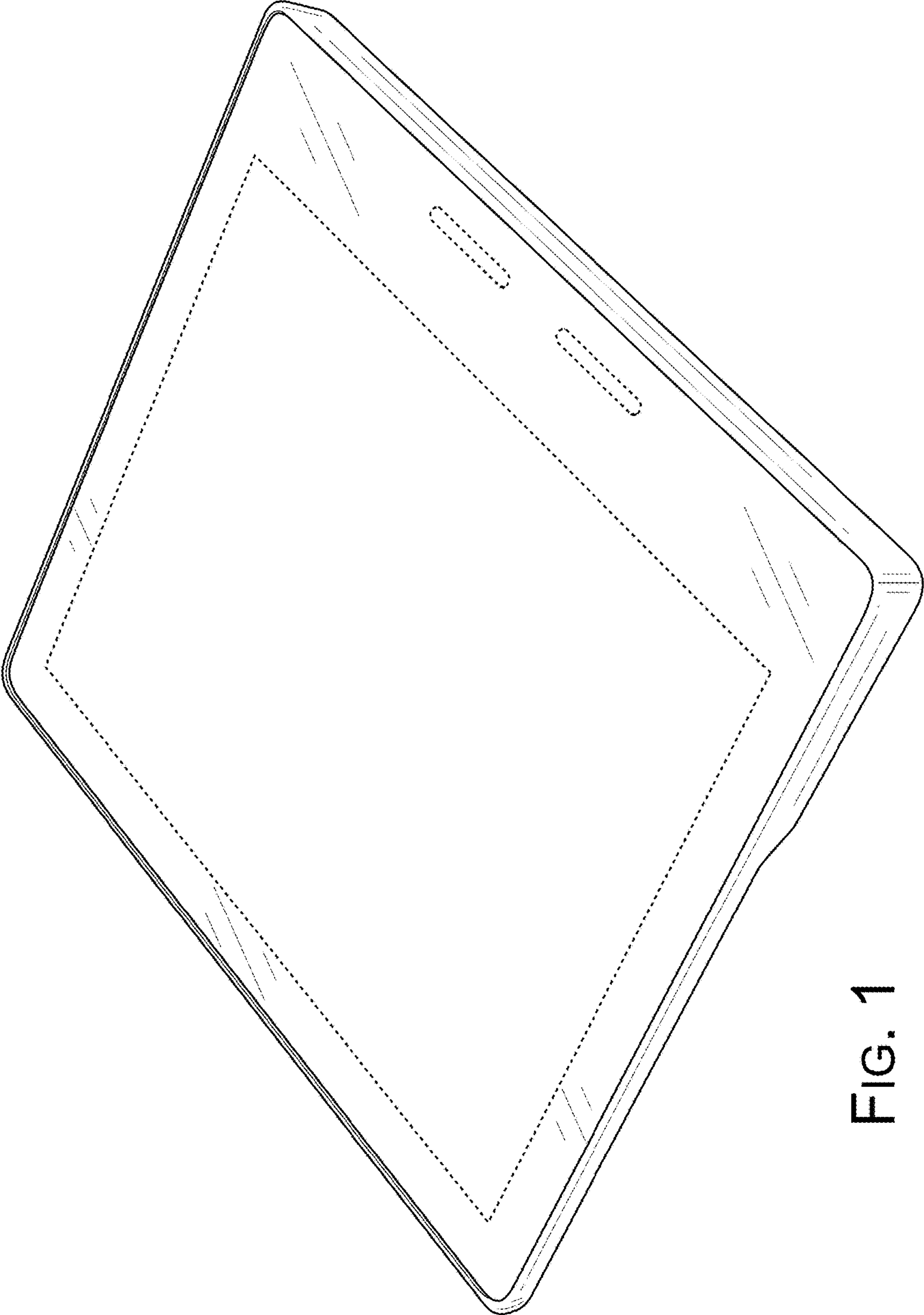


FIG. 1

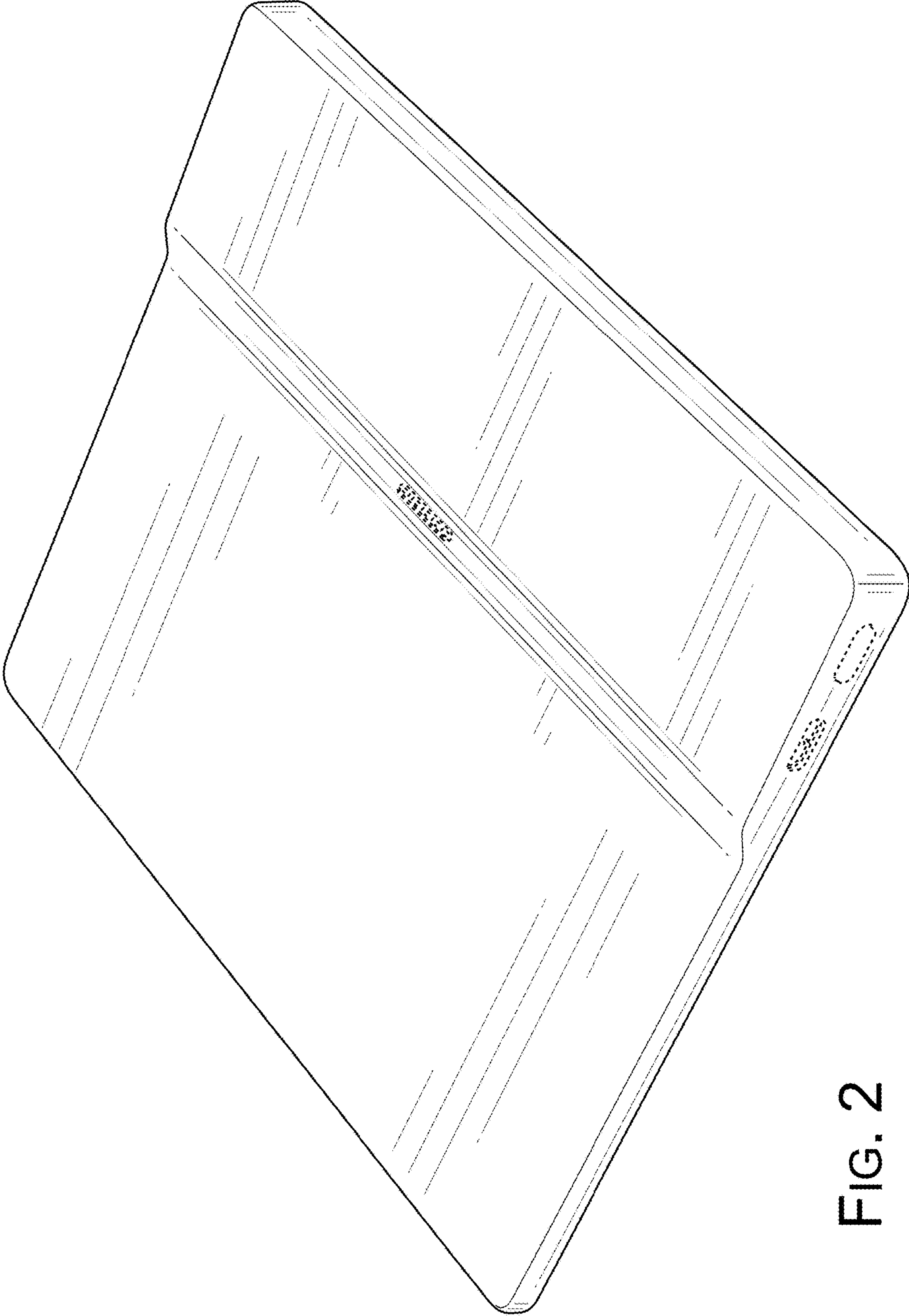


FIG. 2

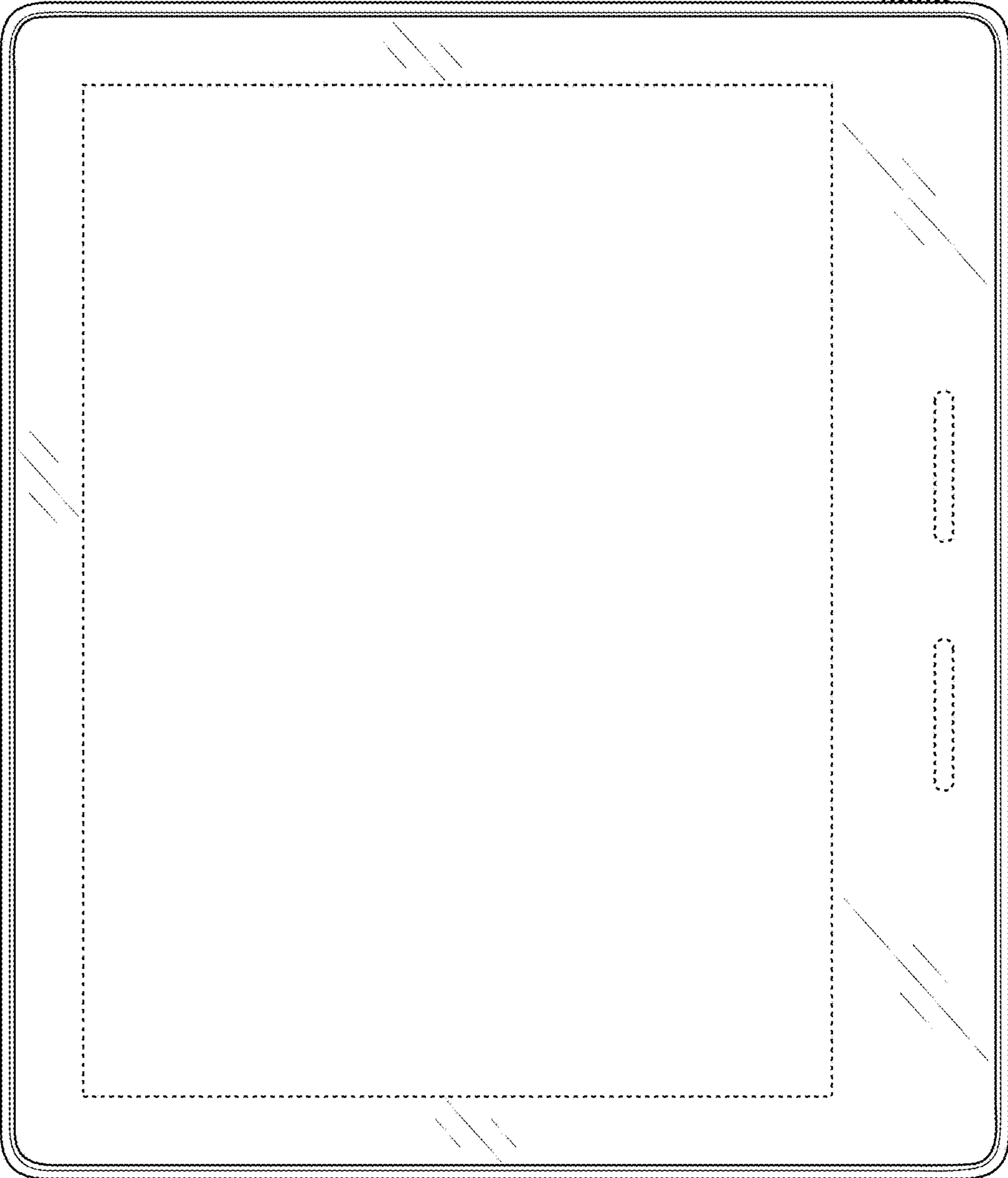


FIG. 3

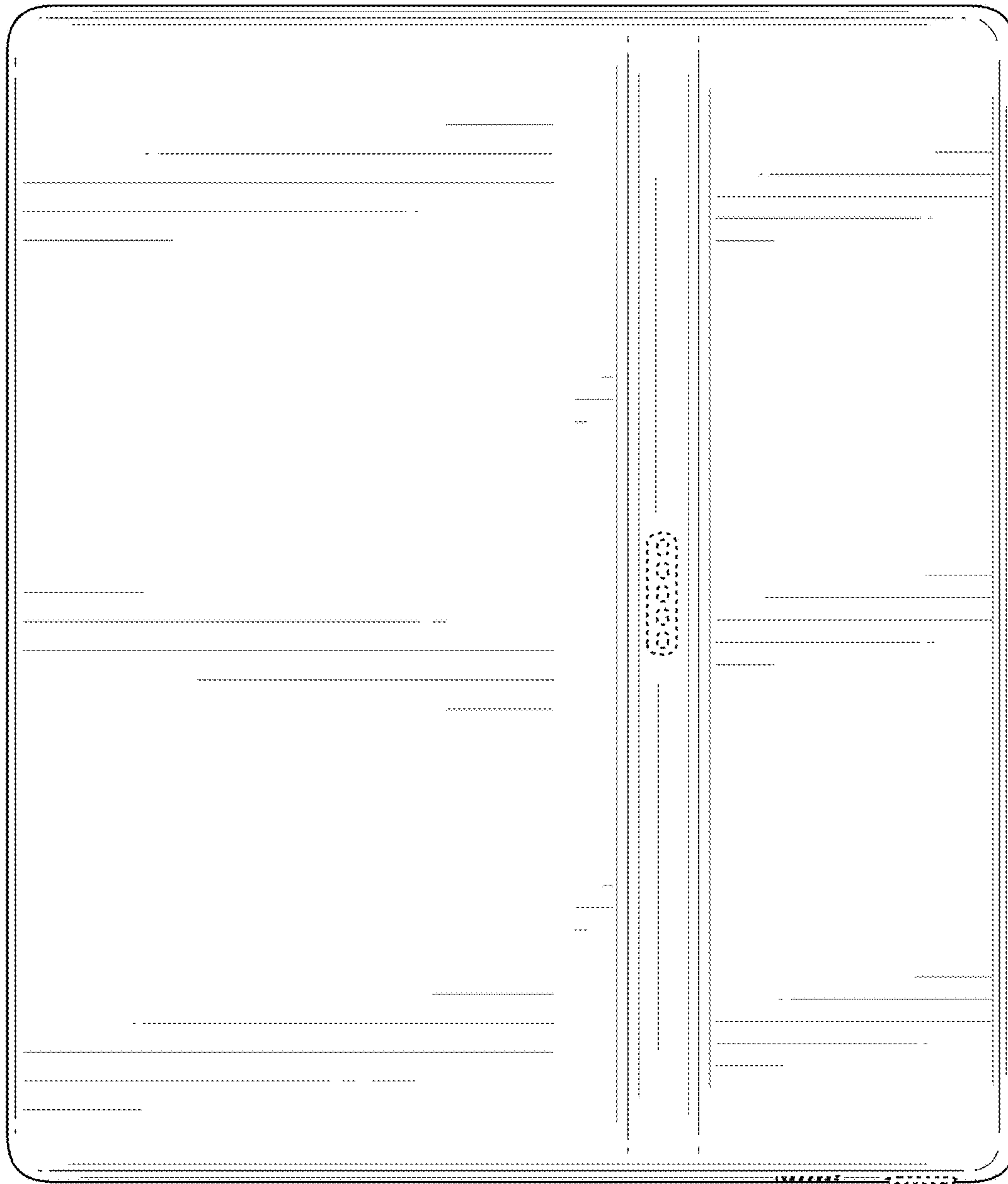


FIG. 4

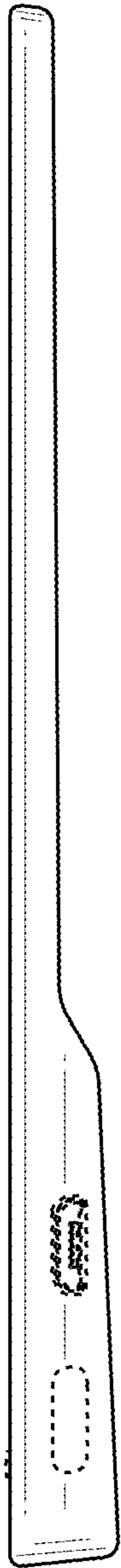


FIG. 5

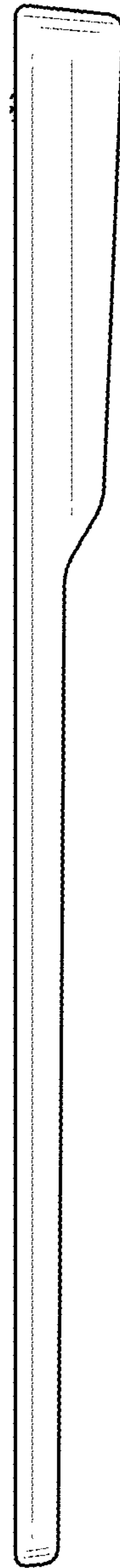


FIG. 6

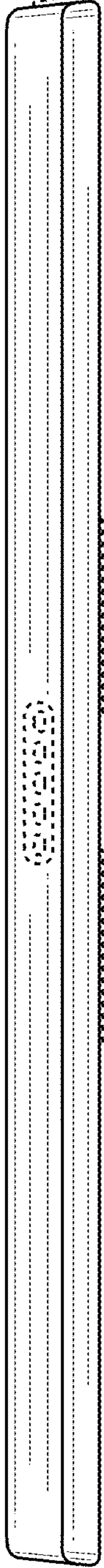


FIG. 7

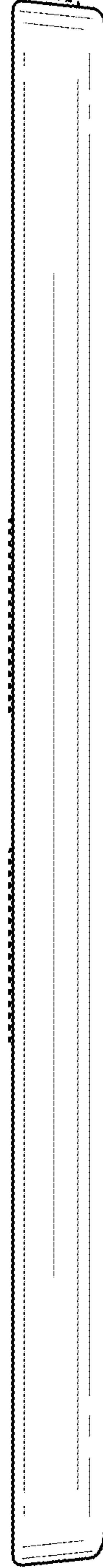


FIG. 8