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(12) **United States Design Patent** (10) **Patent No.:** **US D778,853 S**  
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(54) **HOUSING FOR ELECTRICAL OR ELECTRONIC COMPONENT**

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(51) **LOC (10) Cl.** ..... **13-99**

(52) **U.S. Cl.**  
USPC ..... **D13/184**

(58) **Field of Classification Search**  
USPC ... D13/162, 162.1, 164, 184, 152; D14/439;  
361/600, 601, 679.01, 679.02, 679.33,  
361/679.46, 690, 696, 709, 715, 728, 730,  
735; 717/128; 307/154  
CPC ..... G01R 31/40; H05K 5/0021  
See application file for complete search history.

(56) **References Cited**

**U.S. PATENT DOCUMENTS**

D329,844 S \* 9/1992 Ishida ..... D13/162  
5,424,588 A \* 6/1995 Cantor ..... G01R 31/40  
307/154

D501,450 S \* 2/2005 Watanabe ..... D13/179  
D511,326 S \* 11/2005 Watanabe ..... D13/179  
D516,526 S \* 3/2006 Watanabe ..... D13/179  
D527,349 S \* 8/2006 Lee ..... D13/162.1  
D558,683 S \* 1/2008 Pape ..... D13/158  
D566,049 S \* 4/2008 Radau ..... D13/162  
D588,552 S \* 3/2009 Radau ..... D13/162  
7,606,039 B2 \* 10/2009 Trinh ..... H05K 7/1474  
361/732  
D620,454 S \* 7/2010 Lissola ..... D13/164  
D640,983 S \* 7/2011 Alves ..... D13/152  
D657,759 S \* 4/2012 Devillier ..... D13/184  
D684,130 S \* 6/2013 Vincent ..... D13/184  
D693,776 S \* 11/2013 Skowranek ..... D13/162  
2001/0039649 A1 \* 11/2001 Boggs ..... G06F 1/025  
717/128

(Continued)

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(57) **CLAIM**

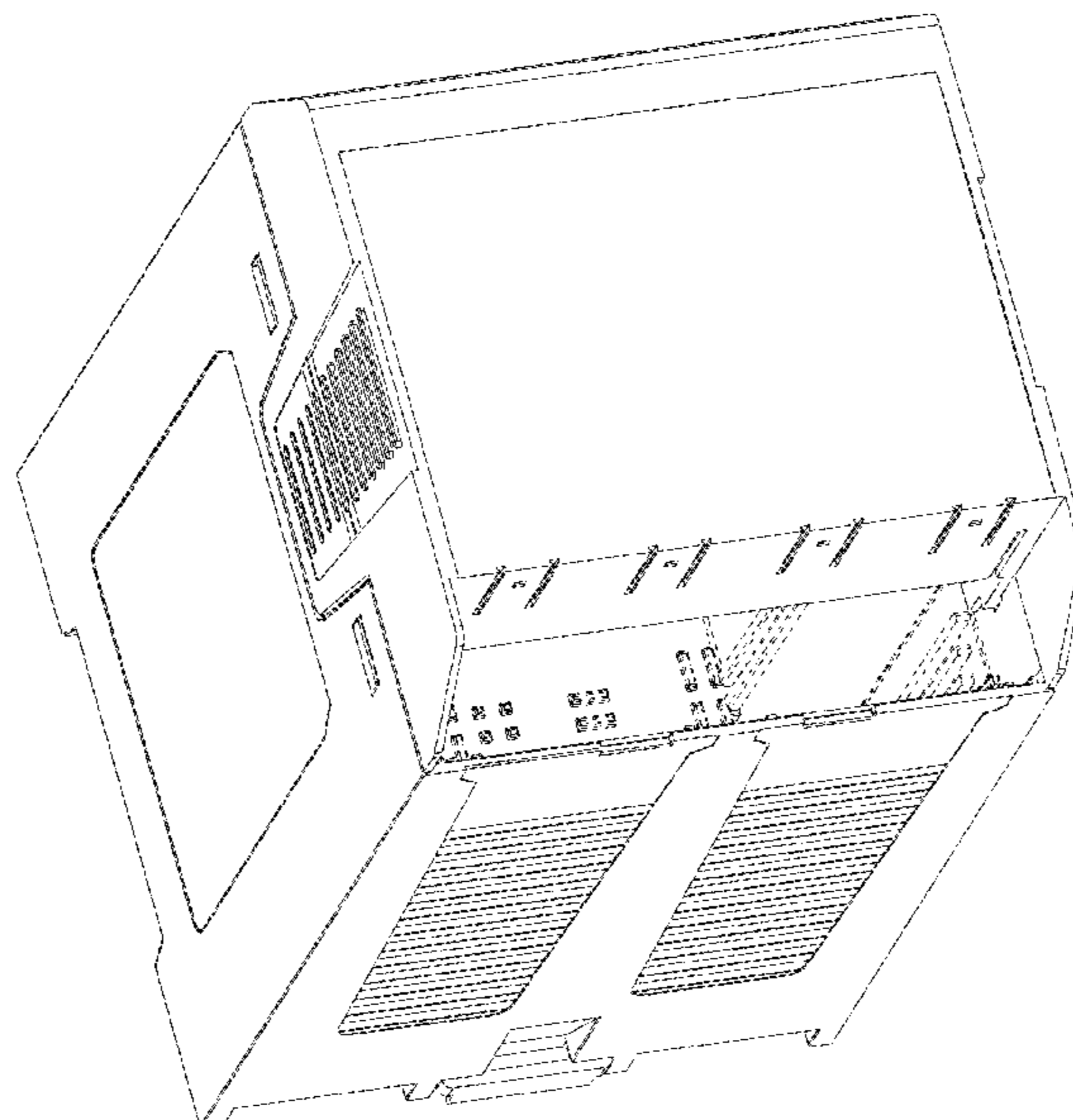
The ornamental design for a housing for electrical or elec-  
tronic component, as shown and described above.

**DESCRIPTION**

FIG. 1 is a top left front perspective view of the housing for  
electrical or electronic component showing our new design;  
FIG. 2 is a left side elevational view thereof;  
FIG. 3 is rear elevational view thereof;  
FIG. 4 is a bottom plan view thereof;  
FIG. 5 is a front elevational view thereof;  
FIG. 6 is a right side elevational view thereof; and,  
FIG. 7 is a top plan view thereof.

The broken lines in the drawings illustrate portions of the  
housing for electrical or electronic component which form  
no part of the claimed design.

**1 Claim, 7 Drawing Sheets**



(56)

**References Cited**

U.S. PATENT DOCUMENTS

2004/0190232 A1\* 9/2004 Chang ..... H05K 5/0021  
361/679.02

\* cited by examiner

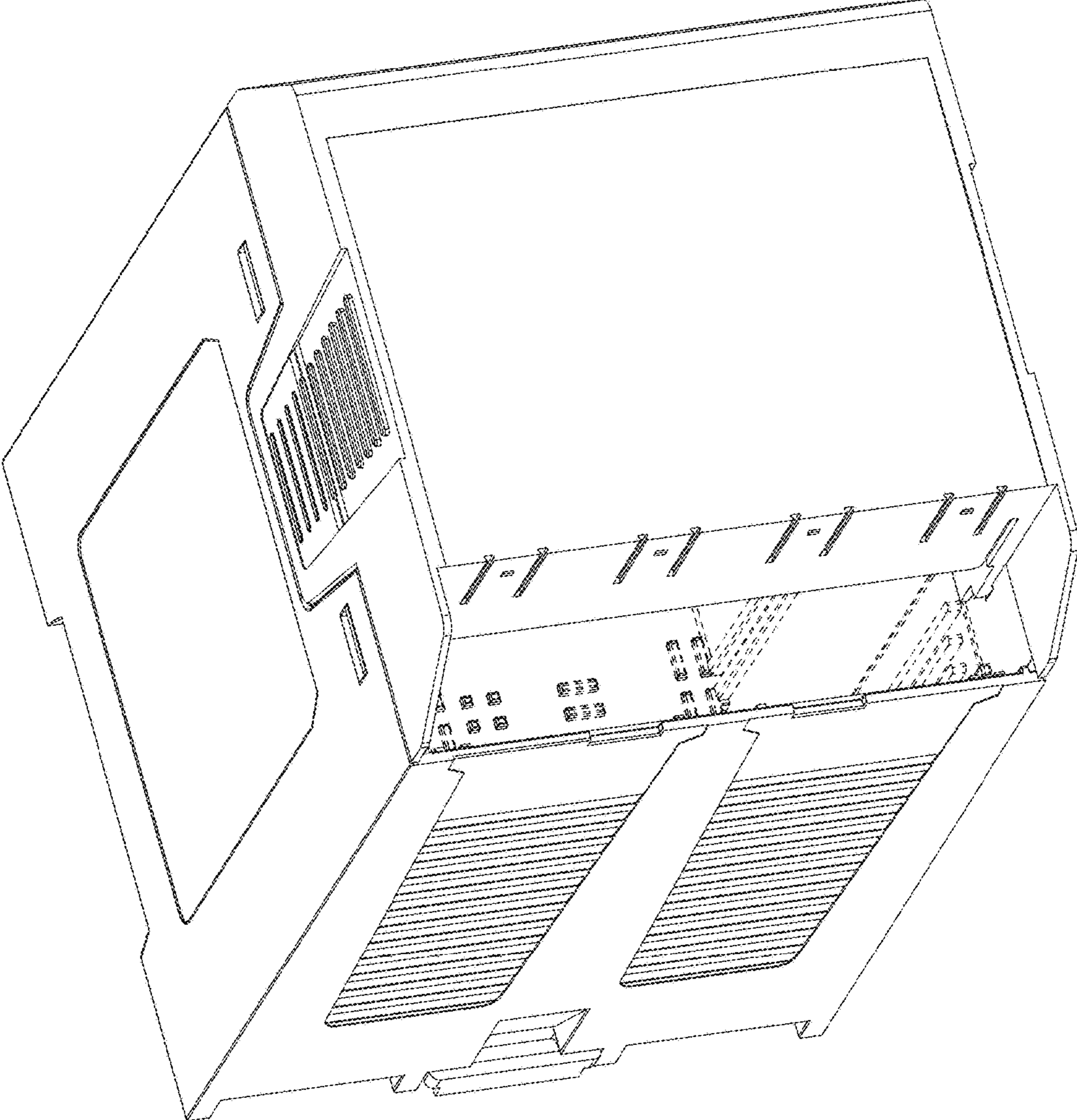


FIG. 1

Fig. 2

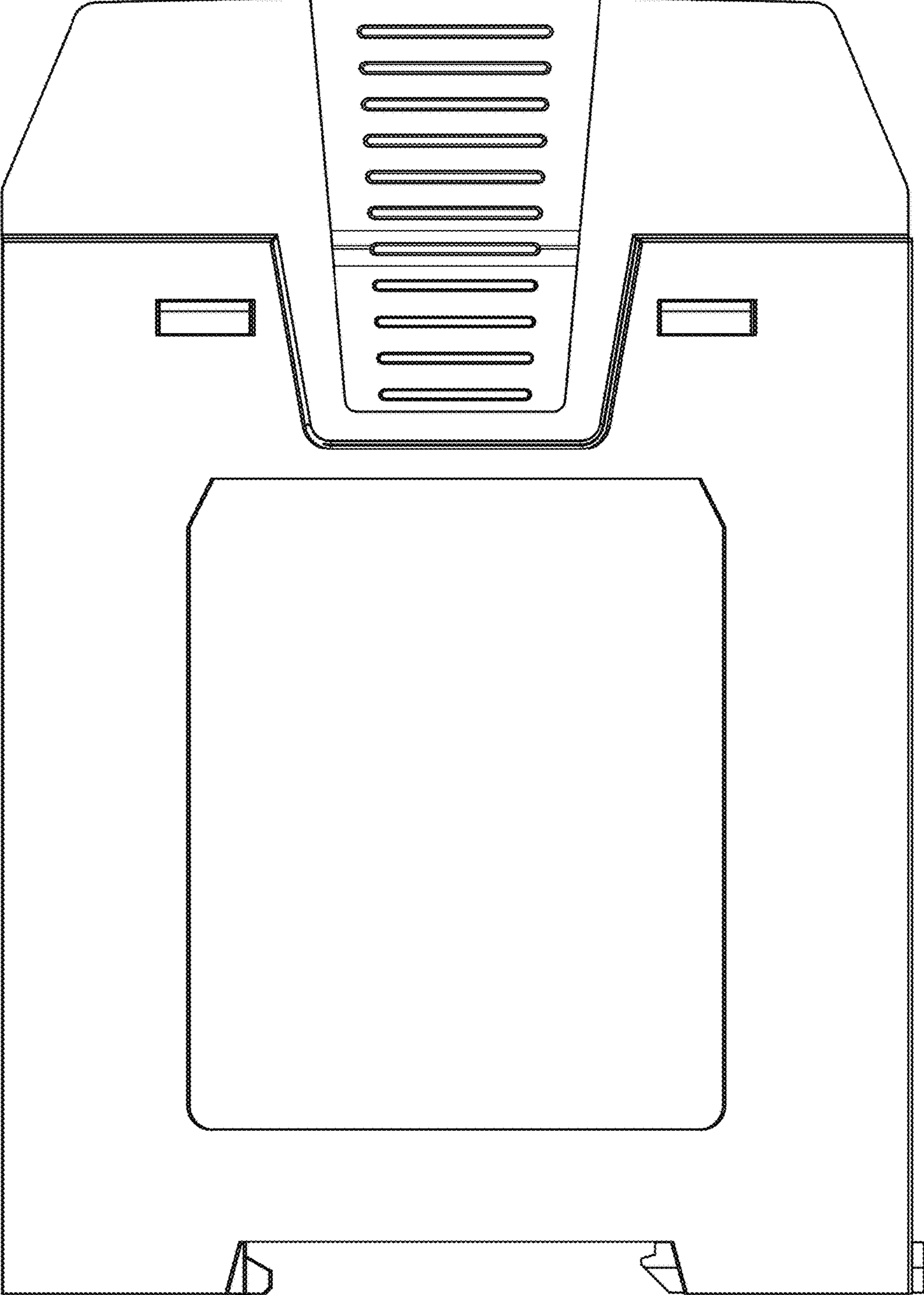
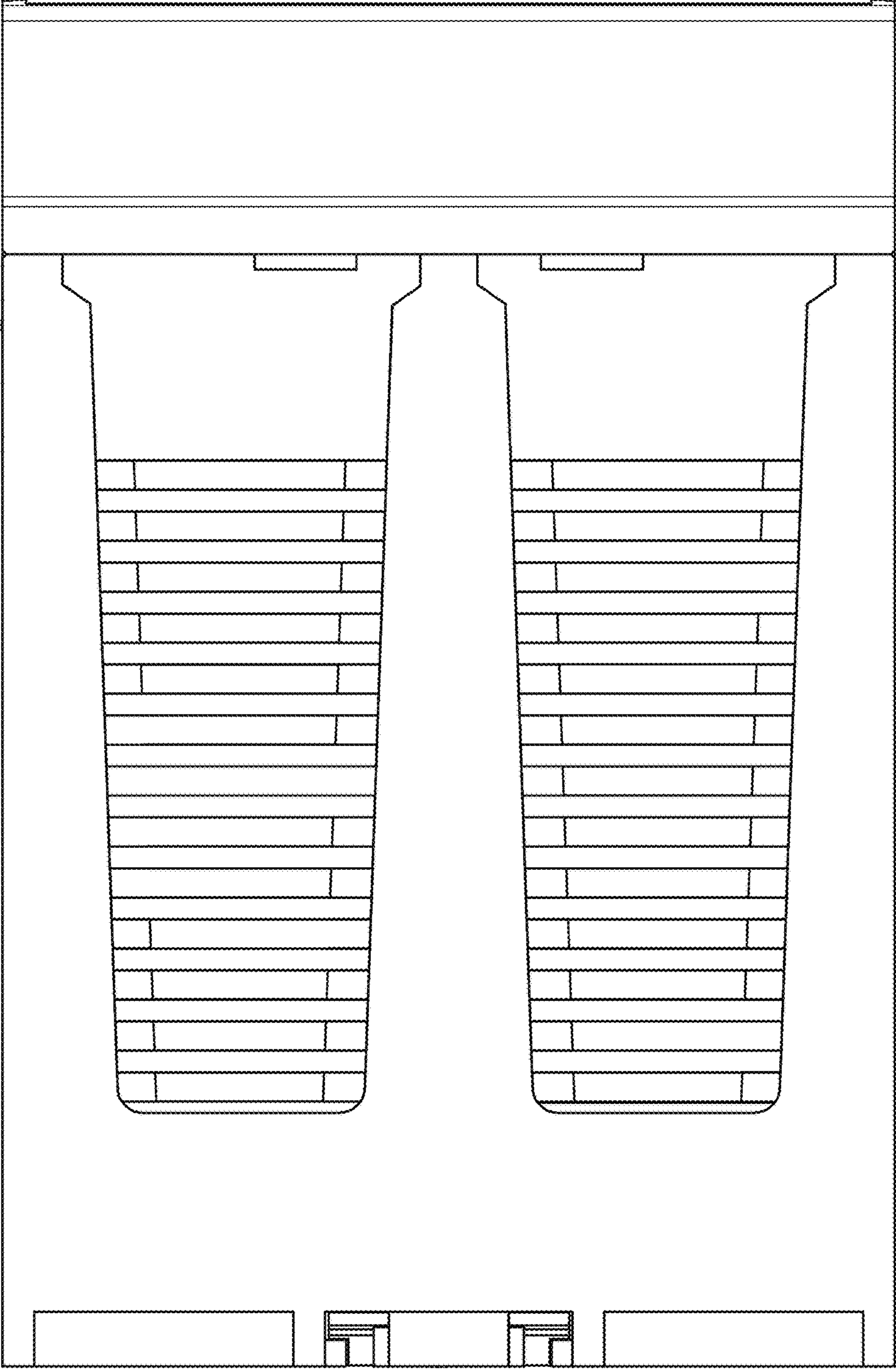


Fig. 3



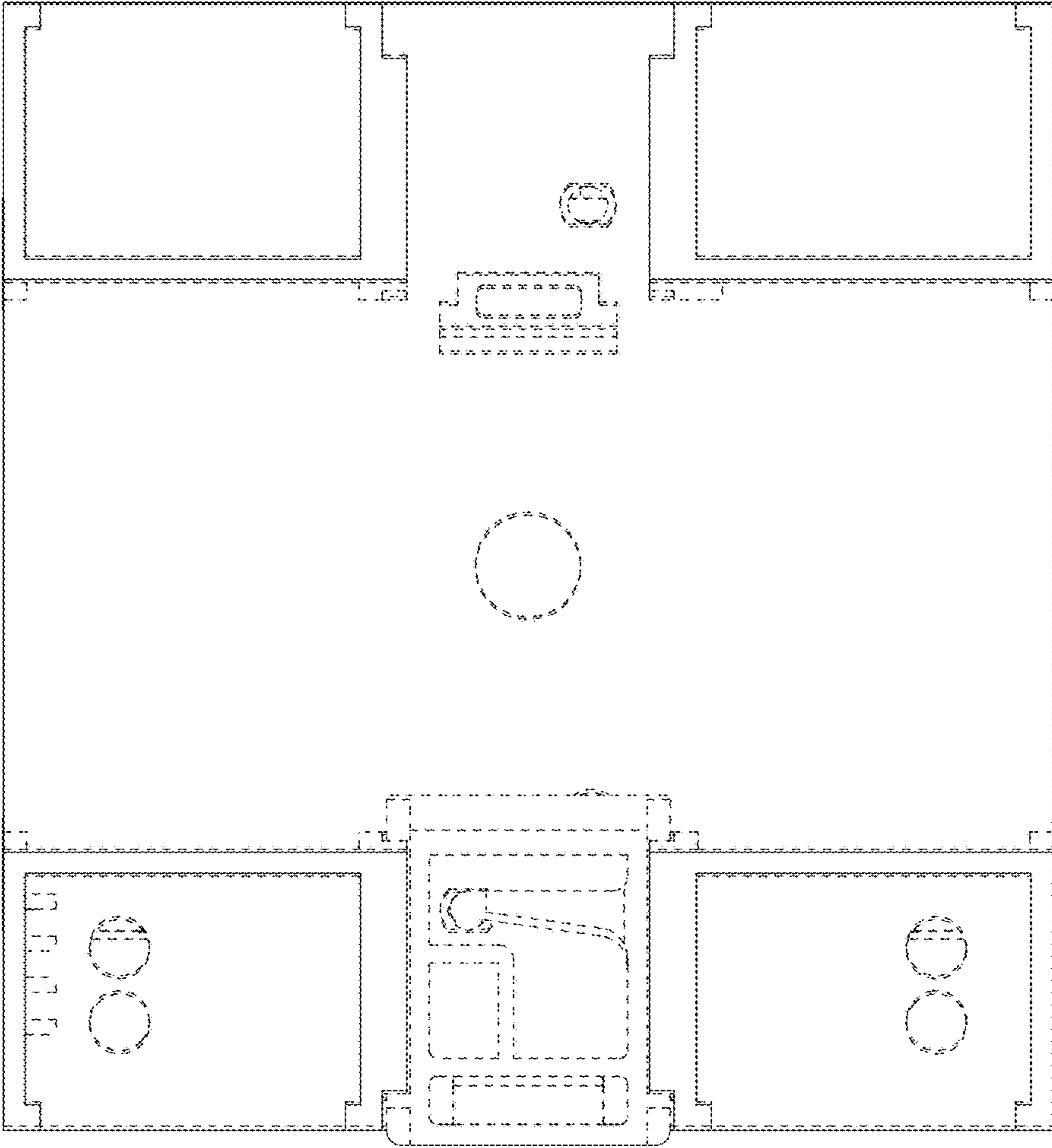


FIG. 4

Fig. 5

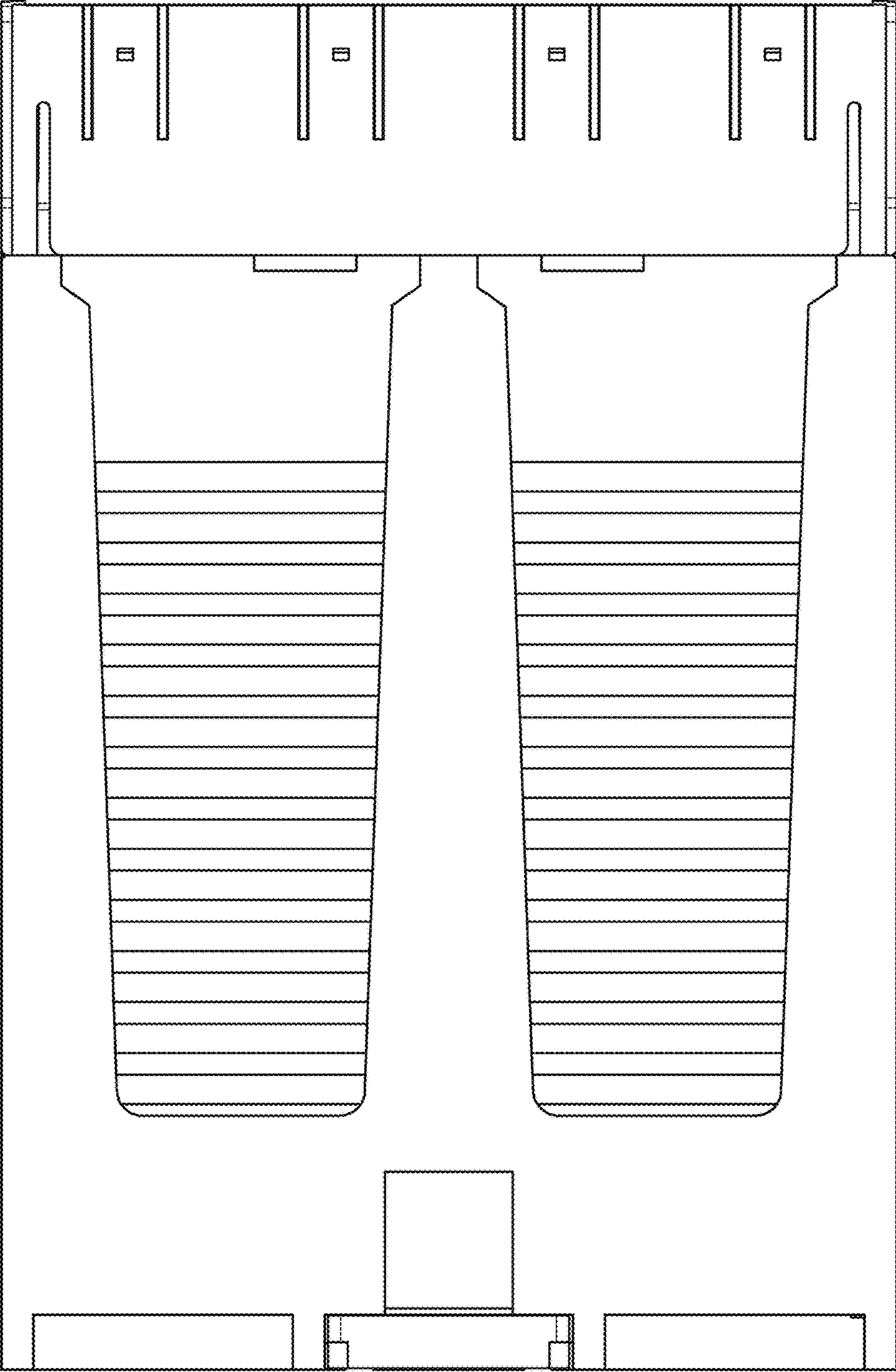
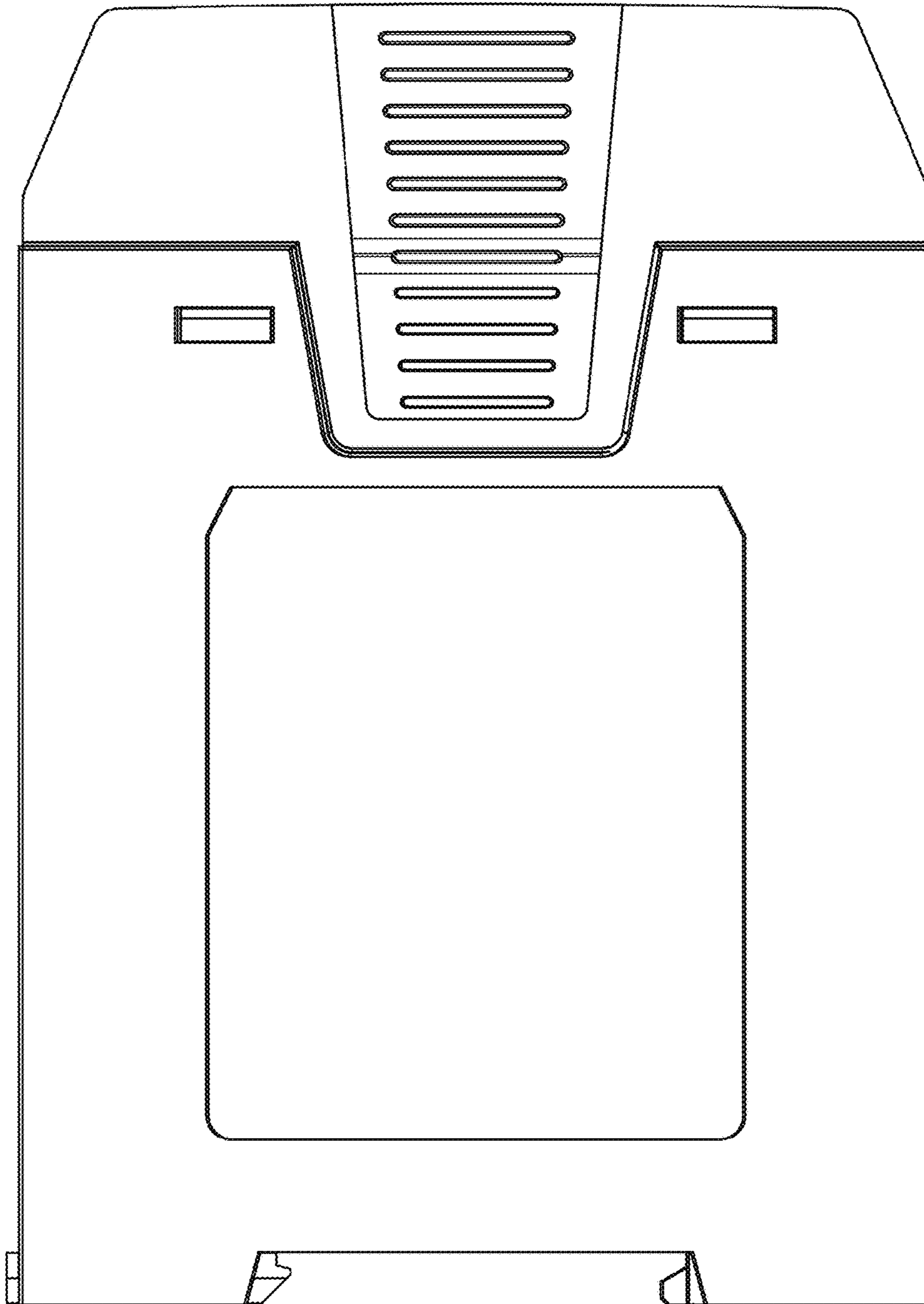


Fig. 6





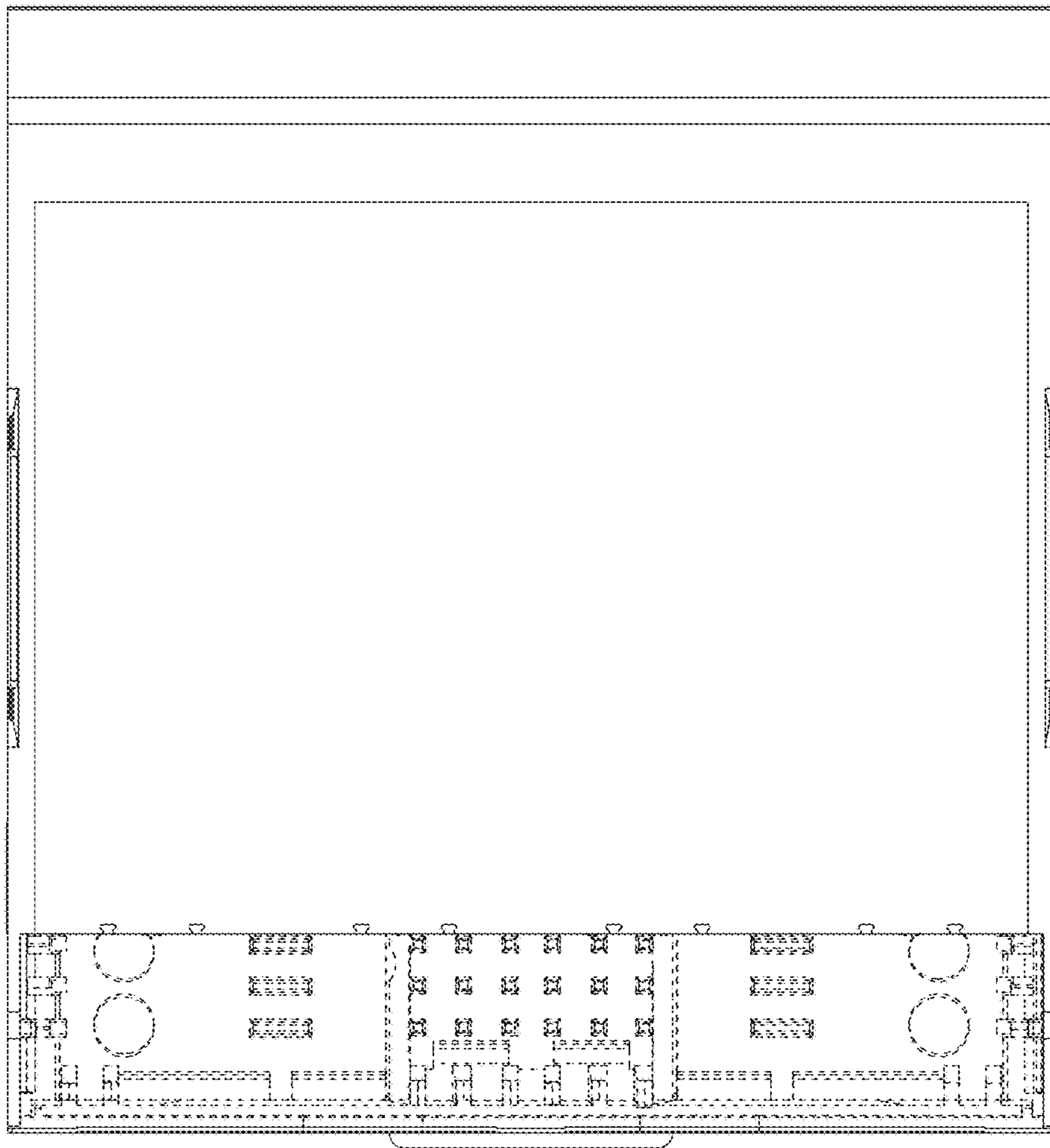


FIG. 7