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(12) **United States Design Patent**
Gurary et al.

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(54) **WAFER CARRIER WITH A MULTI-POCKET CONFIGURATION**

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**; D23/386; D23/499

(58) **Field of Classification Search**
USPC D14/356; D10/93, 104.1, 108, 114;

D8/4, 25, 52; D17/20, 22, 23; D23/323, D23/328, 333, 335, 336, 337, 341, 352, D23/357, 370, 385, 386, 397, 399, 400, D23/406, 411, 413, 499; D11/143, 144; D13/182

CPC H01R 13/5213; B60Q 1/04; B60Q 1/26; G10D 3/006; G10D 13/023; G10D 13/027; B25B 15/001; G10H 3/143; G10H 3/181; G10G 5/005; A42B 1/04; B64C 11/04; B64C 11/16; B64C 11/18; B64C 11/20; B64C 2001/009; F03D 3/062; F03D 11/04; F04D 19/002; F04D 25/068; F04D 25/088; F24F 1/0018; B64D 13/02; E04C 2/427; E06B 3/5892; B24B 37/04

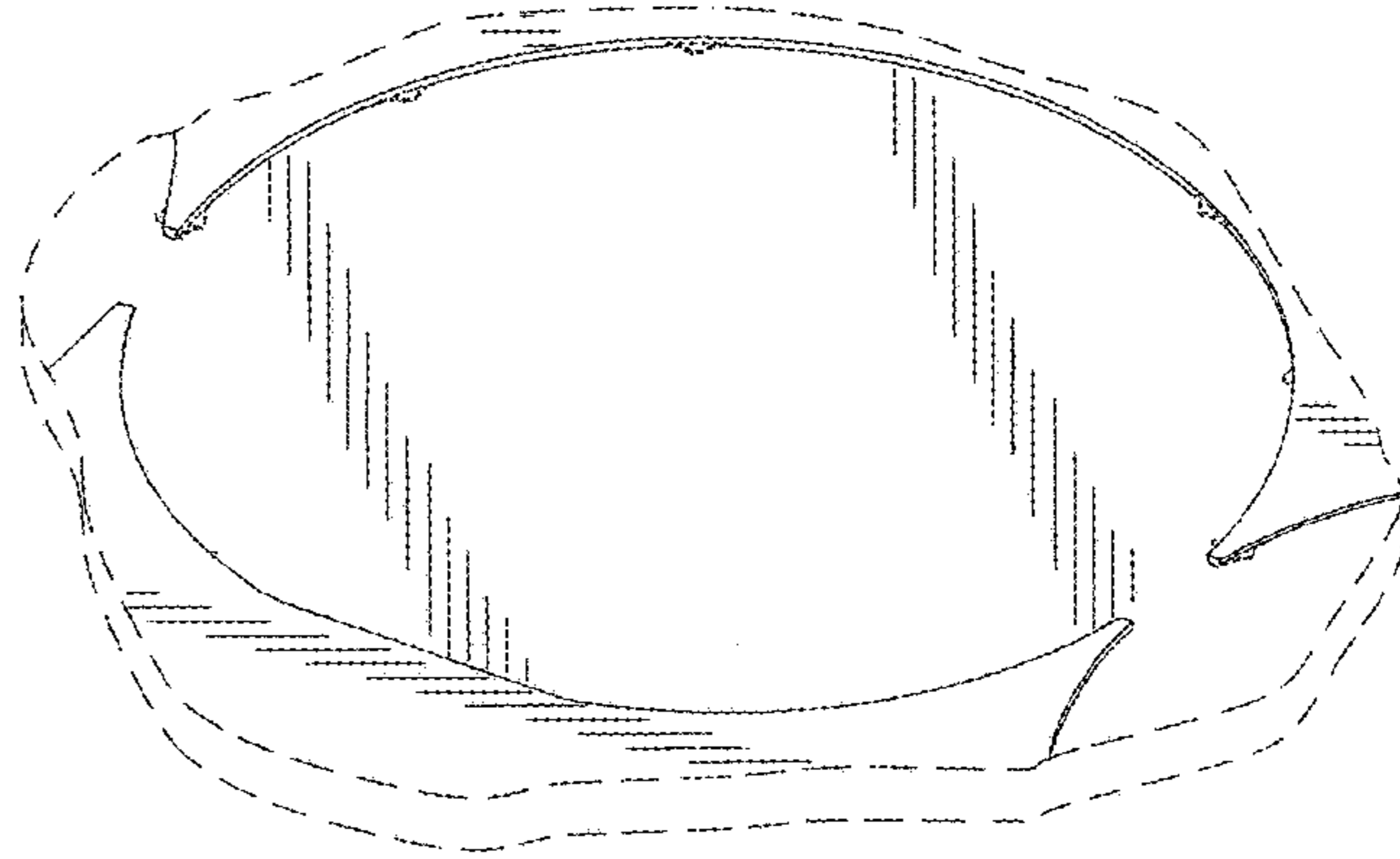
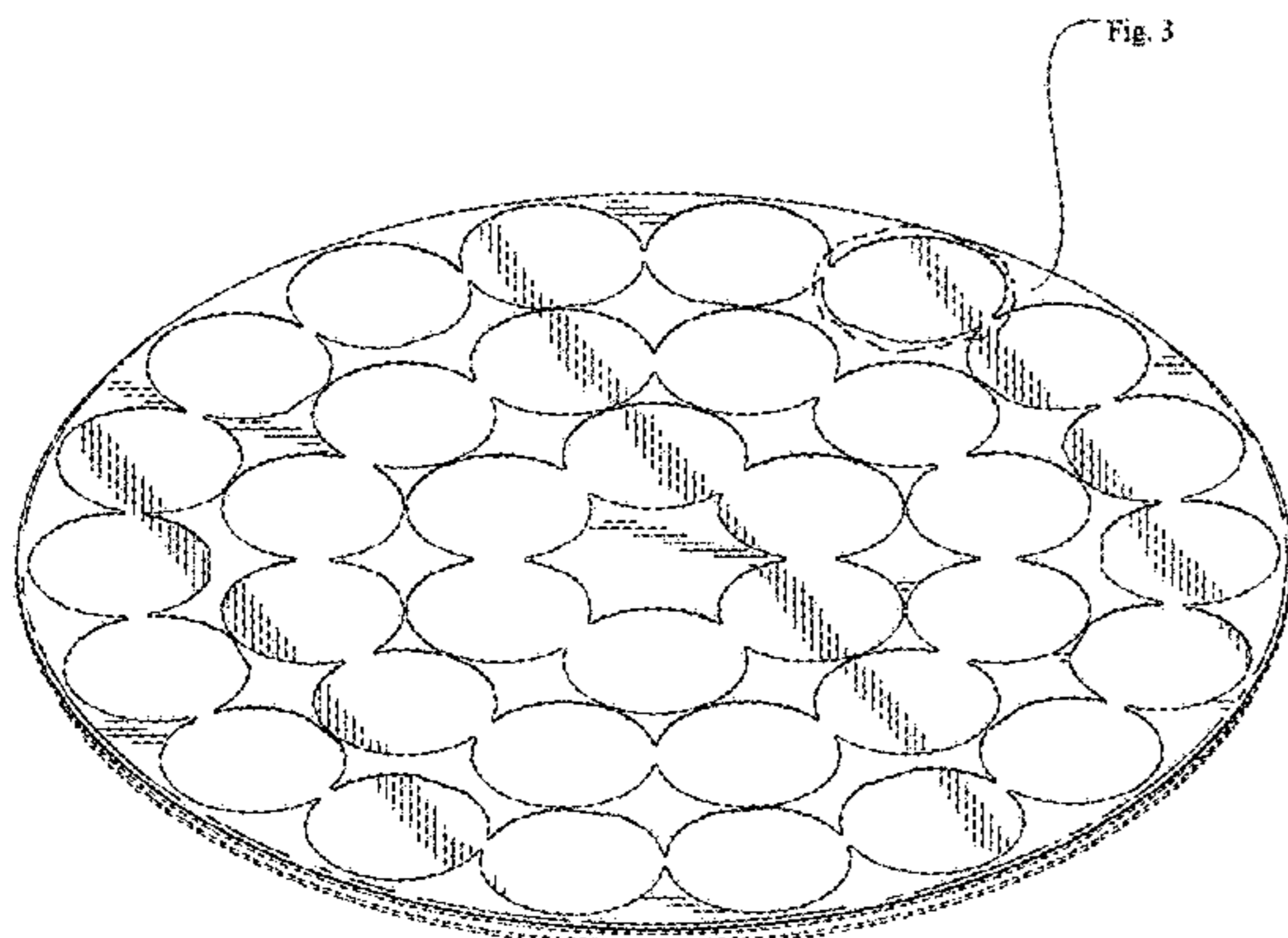
See application file for complete search history.

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(57) **CLAIM**

We claim the ornamental design for a wafer carrier with a multi-pocket configuration, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a wafer carrier with a multi-pocket configuration according to the new design.
 FIG. 2 is a top plan view of the wafer carrier with a multi-pocket configuration shown in FIG. 1.
 FIG. 3 is a detail view of a portion of the wafer carrier with a multi-pocket configuration shown in FIG. 1 detailing an enlarged portion corresponding to the area indicated in FIG. 1 that includes a single pocket and surrounding region in a perspective view.
 FIG. 4 is a right side view of a wafer carrier with a multi-pocket configuration according to the new design shown in FIG. 1, with the left side, front, and rear views being the same as the right side view; and,
 FIG. 5 is a bottom plan view of a wafer carrier with a multi-pocket configuration according to the new design shown in FIG. 1.
 The broken lines, where present, in FIGS. 1, 3, 4 & 5 illustrate portions of the wafer carrier with a multi-pocket configuration that represent environment of the claimed design and form no part of the claimed design. The dash-dot lines of FIG. 1 define the area corresponding to the enlarged portion shown in FIG. 3 and form no part of the claimed design. The dash-dot and broken lines in FIG. 3 represent the edge of the enlarged portion of the design and form no part of the claimed design.

1 Claim, 5 Drawing Sheets

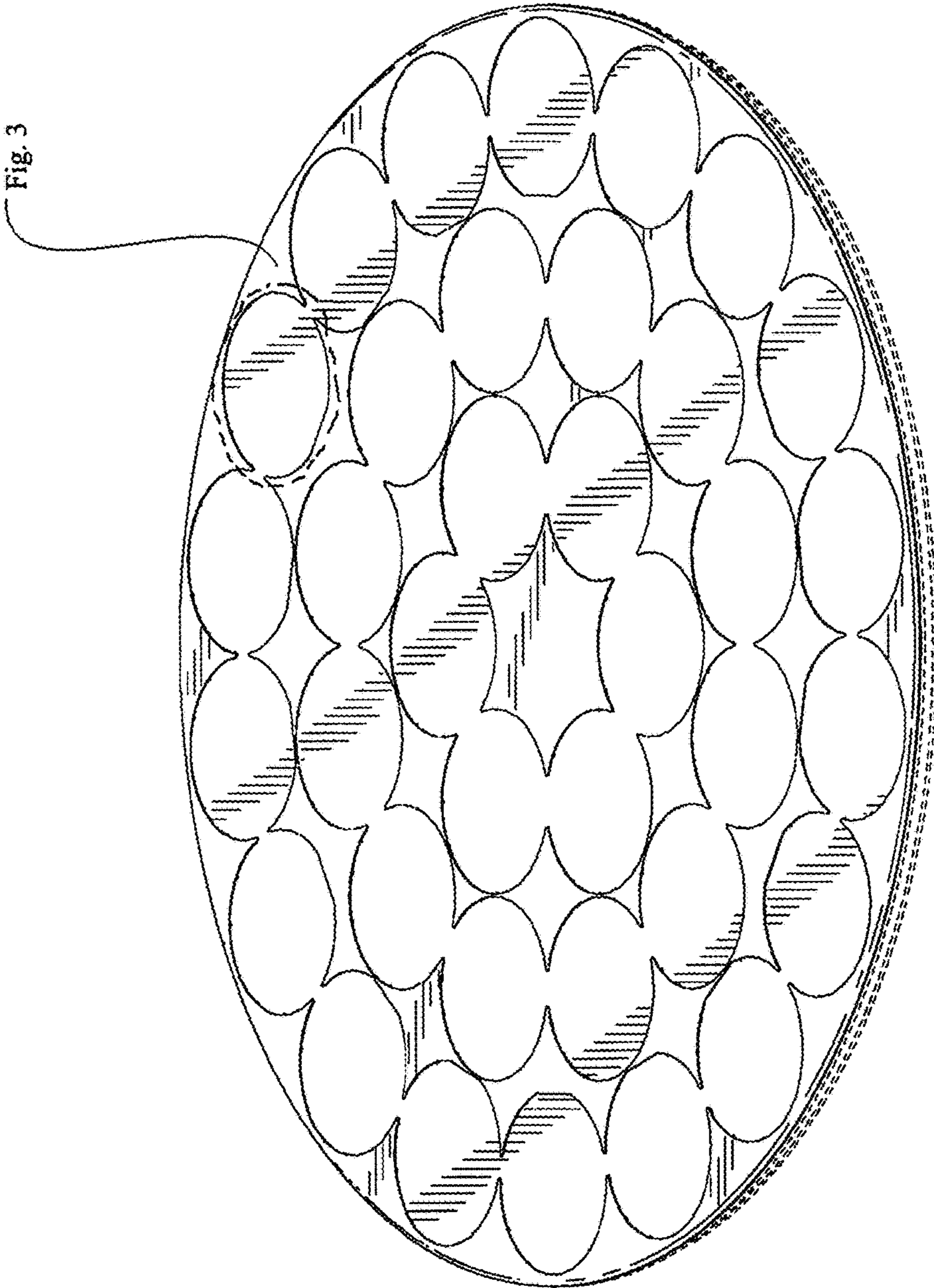


Fig. 3

Fig. 1

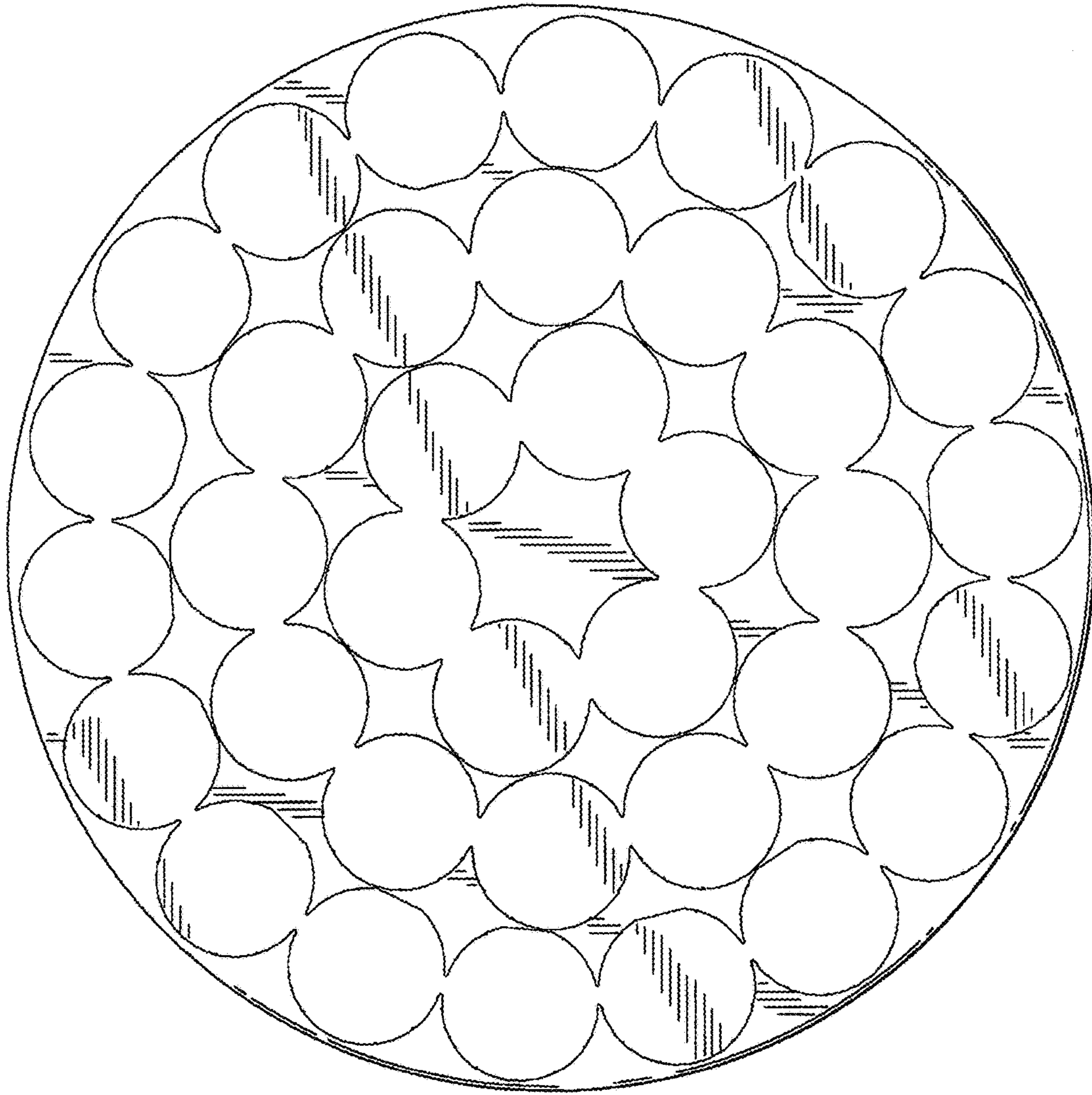


Fig. 2

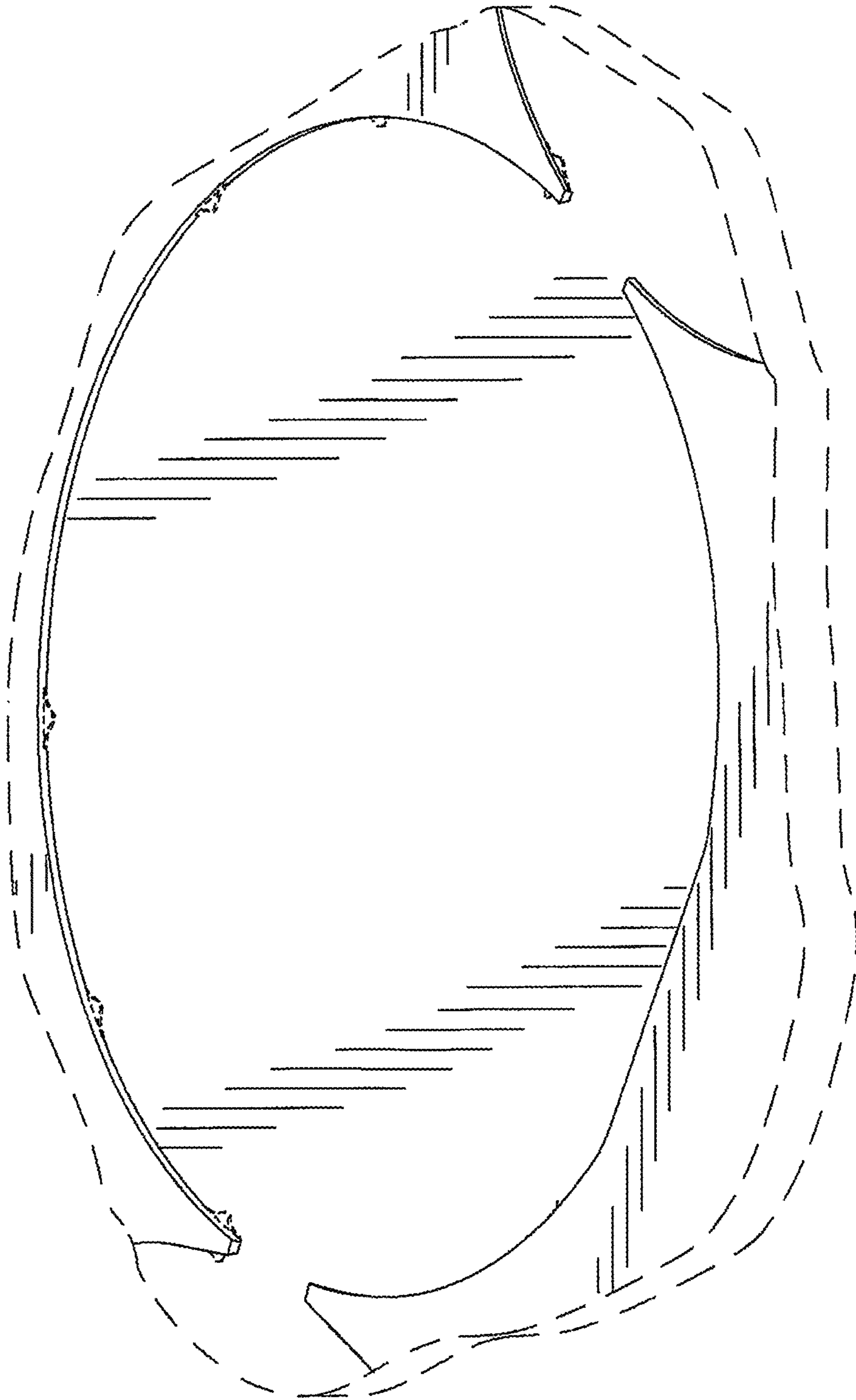


Fig. 3

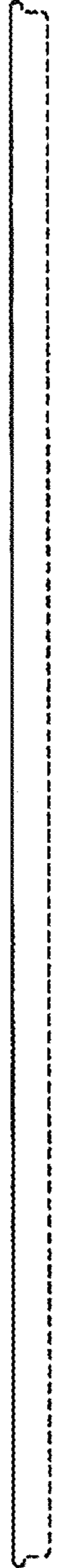


Fig. 4

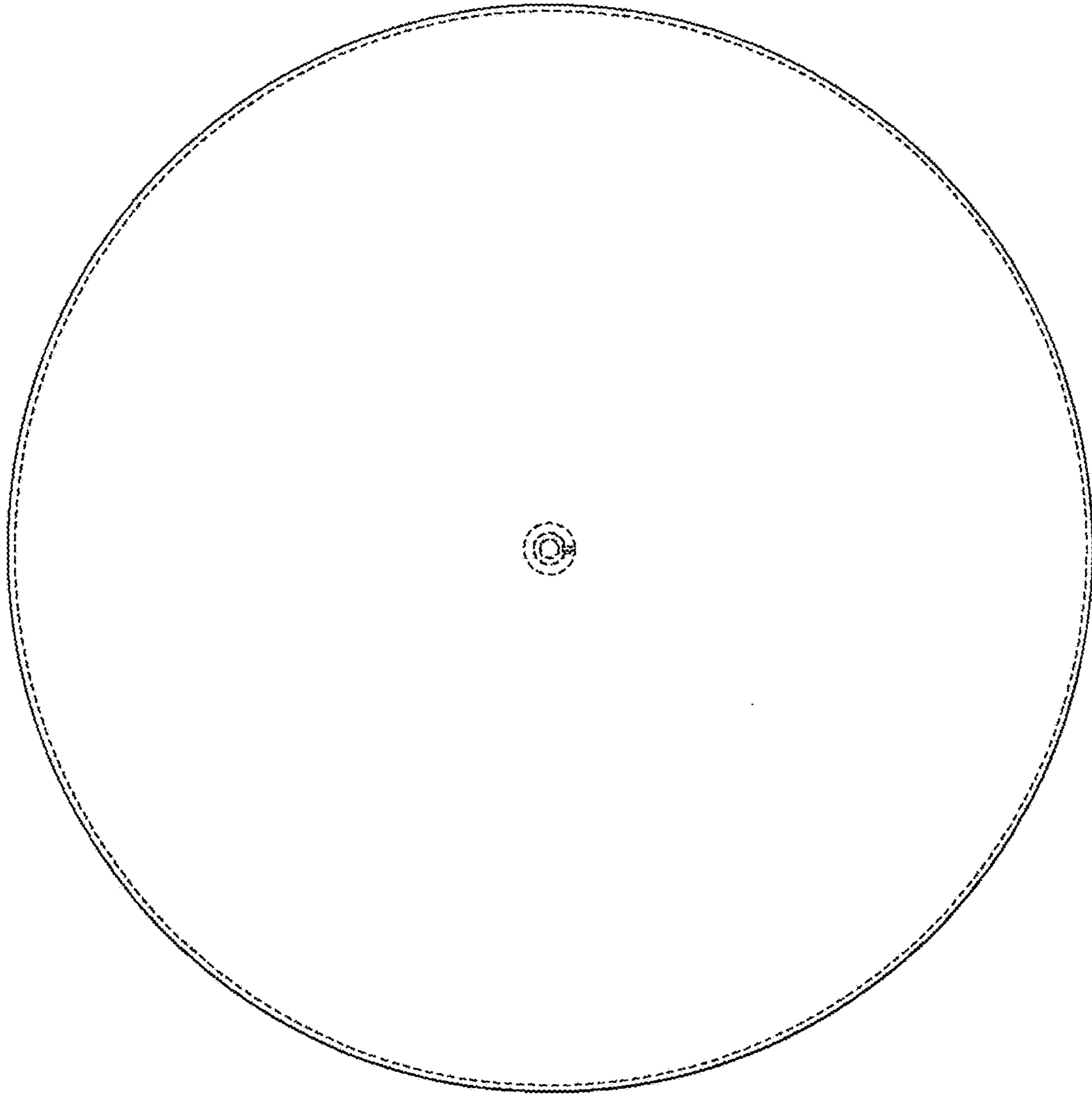


Fig. 5

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D778,247 S
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Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

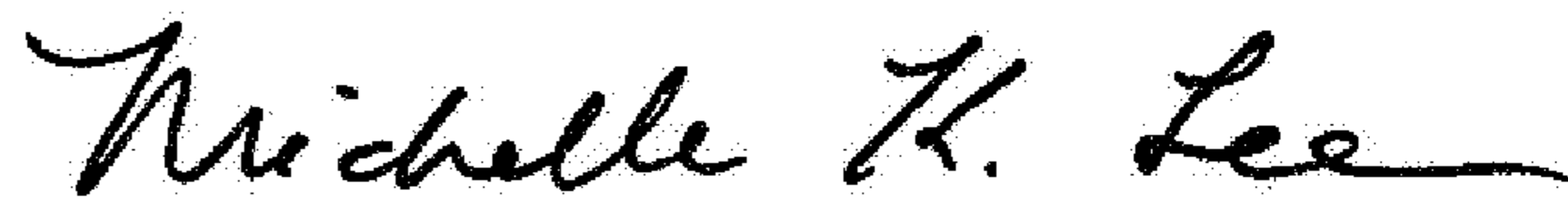
(72) Inventors delete:

“Alexander Gurary, Bridgewater, NJ (US); Mandar Deshpande, Bridgewater, NJ (US); Aniruddh Parekh, Milburn, NJ (US); Yuliy Rashkovsky, New York, NY (US)”

Insert:

--Alexander Gurary, Bridgewater, NJ (US); Mandar Deshpande, Bridgewater, NJ (US); Aniruddh Parekh, New York, NY (US); Yuliy Rashkovsky, Milburn, NJ (US)--

Signed and Sealed this
Ninth Day of May, 2017



Michelle K. Lee
Director of the United States Patent and Trademark Office