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(12) **United States Design Patent**
Phillips et al.

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(45) **Date of Patent:** **** Jan. 3, 2017**

(54) **ELECTRONIC DEVICE**

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(71) Applicant: **Samsung Electronics Co., Ltd.**,
Suwon-si (KR)

(72) Inventors: **Sheldon George Phillips**, Glendale, CA
(US); **Frank Nuovo**, Los Angeles, CA
(US)

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(73) Assignee: **SAMSUNG ELECTRONICS CO., LTD.**,
Suwon-Si (KR)

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(**) Term: **15 Years**

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(21) Appl. No.: **29/527,448**

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(22) Filed: **May 19, 2015**

Related U.S. Application Data

Primary Examiner — Antoine D Davis

(63) Continuation of application No. 29/491,646, filed on
May 22, 2014, now Pat. No. Des. 729,650.

(74) *Attorney, Agent, or Firm* — McAndrews Held &
Malloy, Ltd.

(51) **LOC (10) Cl.** **10-04**

(57) **CLAIM**

(52) **U.S. Cl.**
USPC **D10/70**; D10/32; D10/78; D10/103;
D24/167

The ornamental design for an electronic device, as shown
and described.

(58) **Field of Classification Search**

DESCRIPTION

USPC D10/30–39, 65, 70, 78, 97, 98, 103; D11/3;
D13/173–177; D14/138 R, 203.5,
D14/247, 338–340, 344, 346, 347;
D24/167, D24/168
CPC G04B 19/00–19/34; G04B 21/12;
G04B 23/12; G04B 37/00–37/228; G04B
47/00–47/068; G01C 17/00; G01C
21/00–21/3697; G01C 22/00–22/025;
G01C 23/00; G06F 19/3481; G06F
3/00–3/027

FIG. 1 is a left front perspective view of an electronic device
showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left side elevation view thereof;
FIG. 5 is a right side elevation view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof; and,
FIG. 8 is a bottom left rear perspective view thereof. ps The
dashed broken lines are for environmental purposes only and
form no part of the claimed design.
Any views of the electronic device that are not shown in the
figures form no part of the claimed design.

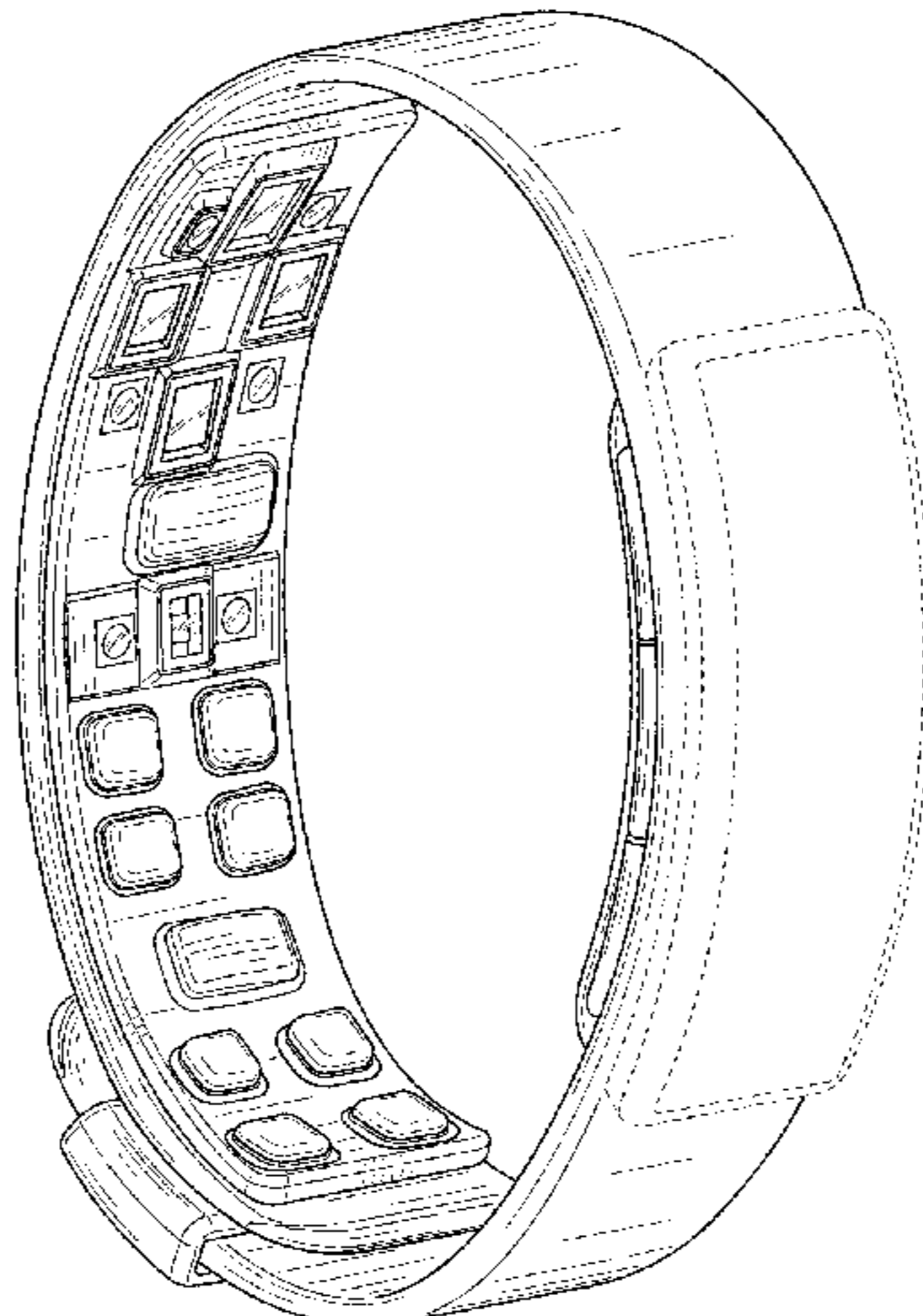
See application file for complete search history.

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1 Claim, 6 Drawing Sheets

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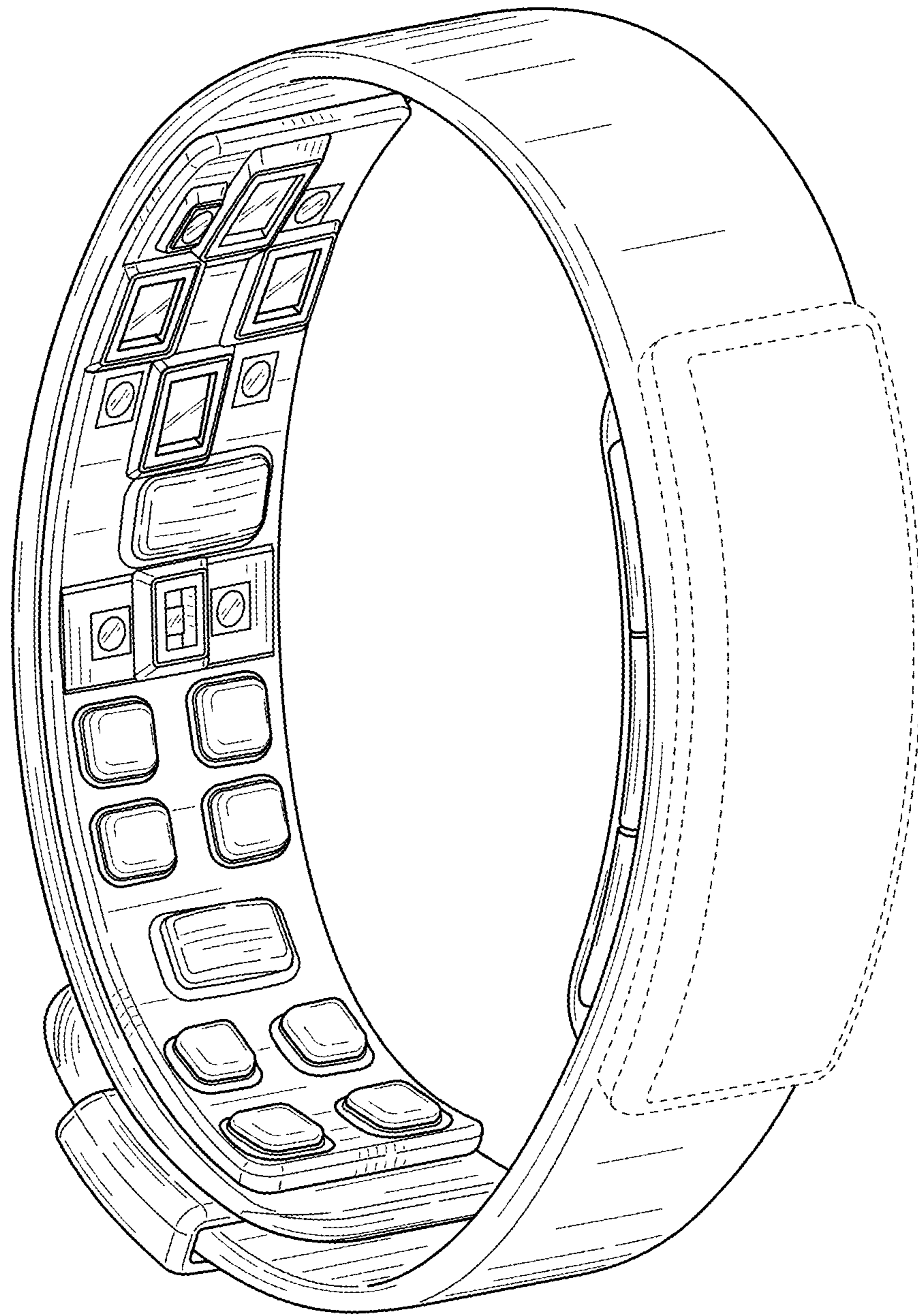


FIG. 1

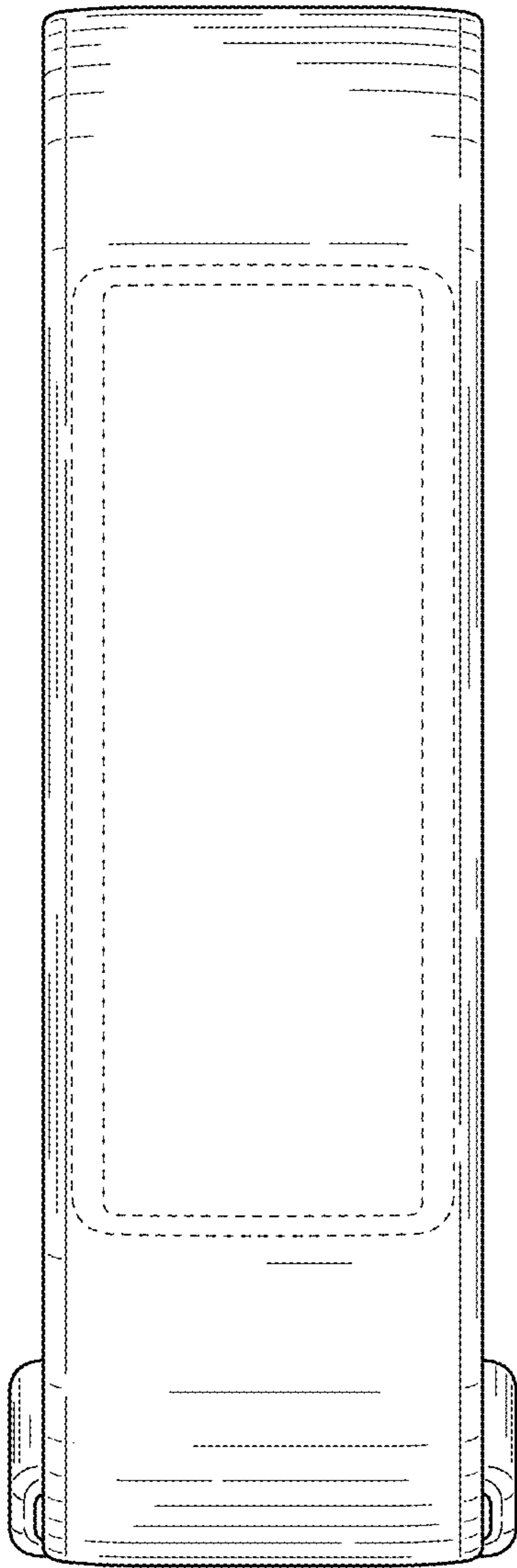


FIG. 2

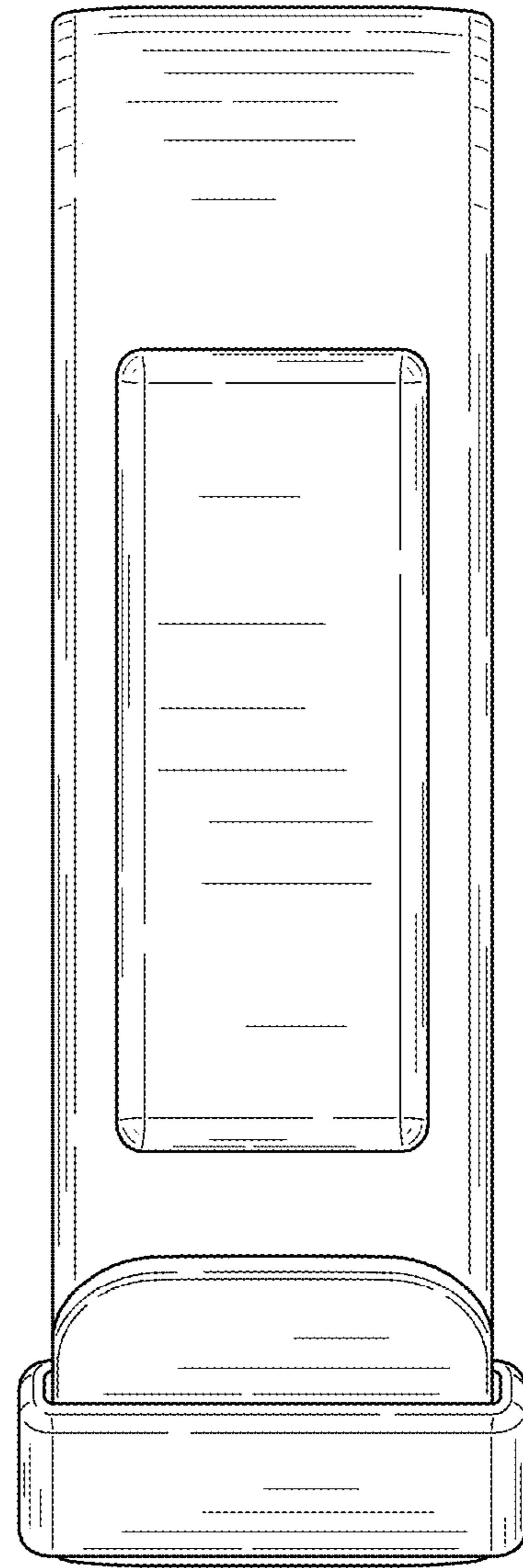


FIG. 3

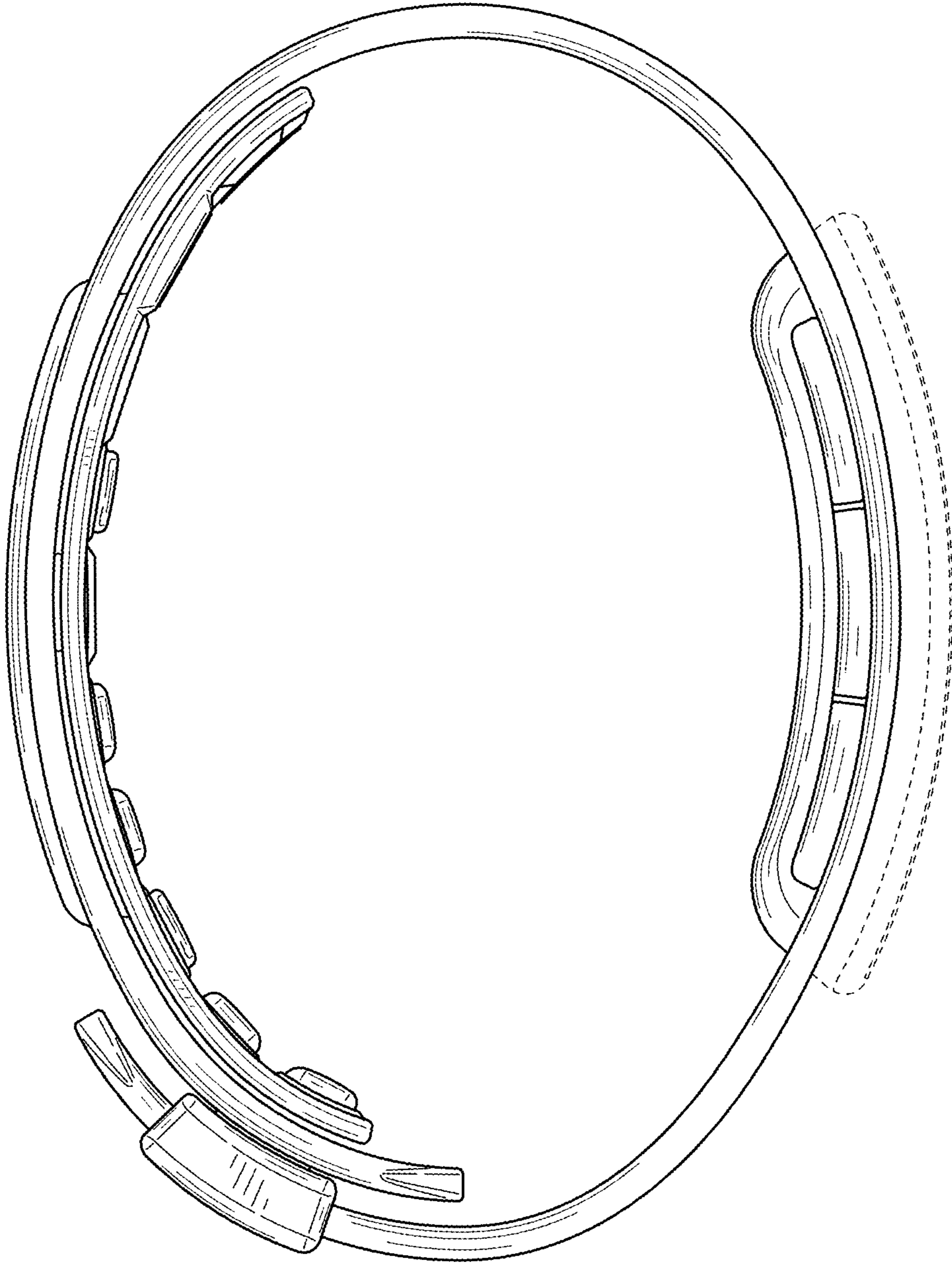


FIG. 4

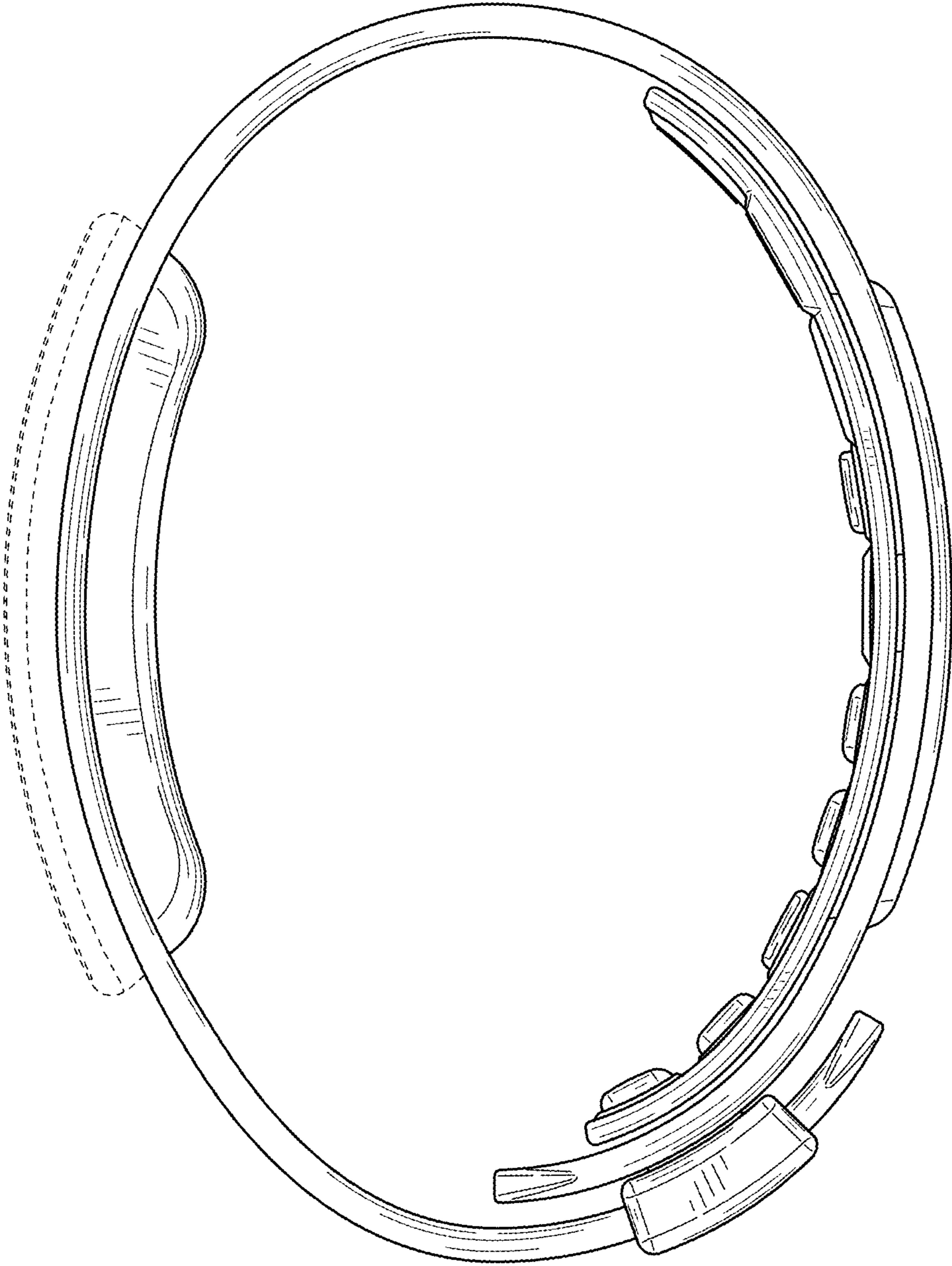


FIG. 5

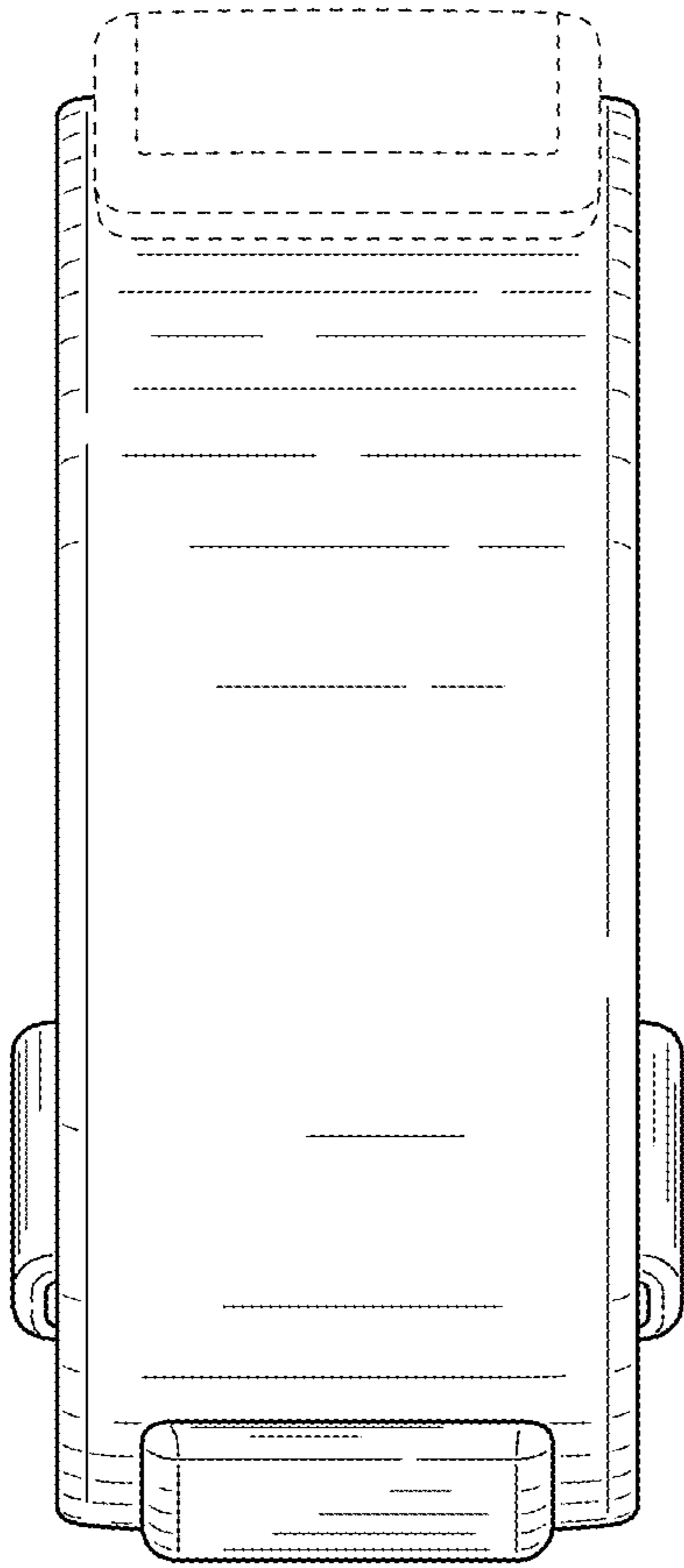


FIG. 6

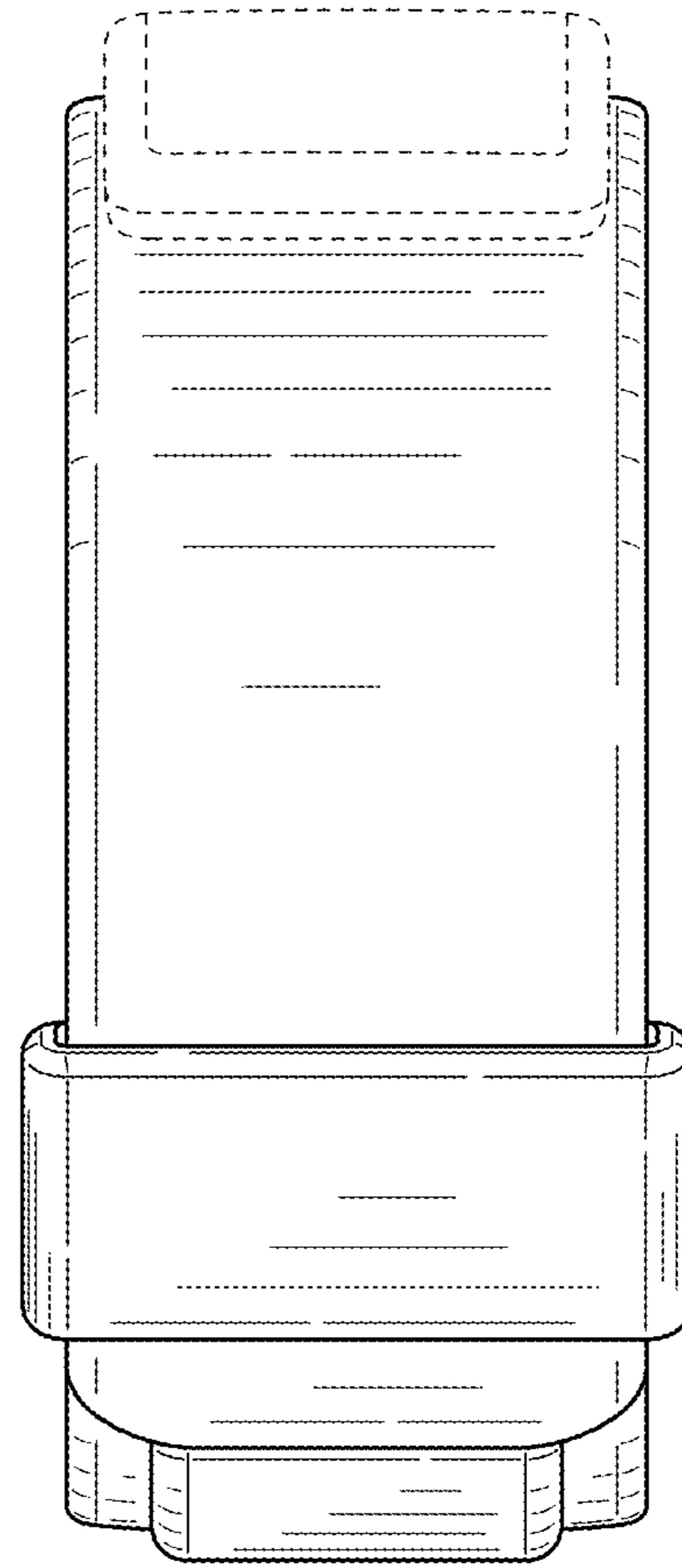


FIG. 7

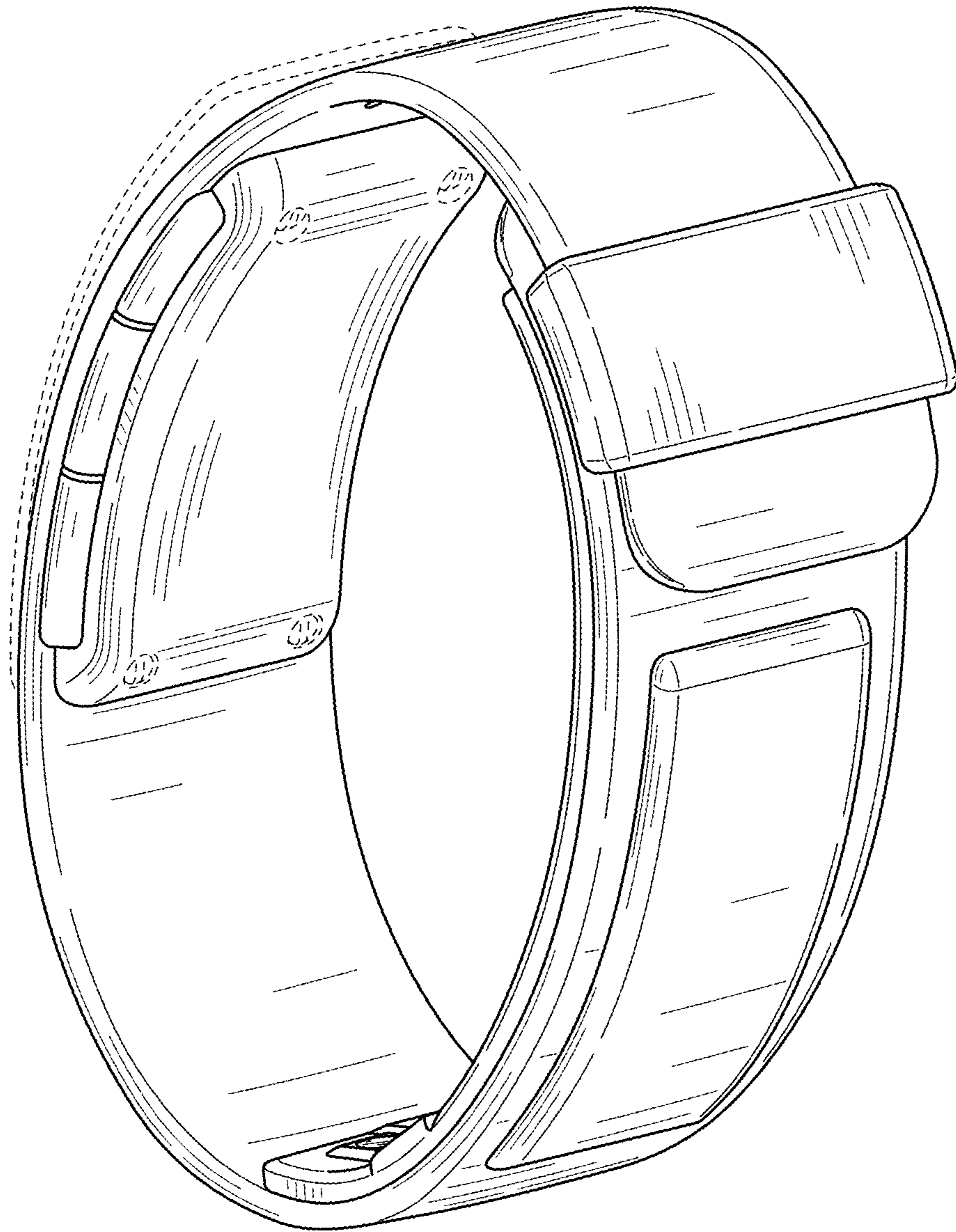


FIG. 8