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(12) **United States Design Patent** (10) **Patent No.:** **US D774,474 S**
Li (45) **Date of Patent:** **** Dec. 20, 2016**

(54) **LIGHT EMITTING DIODES ON A PRINTED CIRCUIT BOARD**

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/180**

(58) **Field of Classification Search**

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D25/110, 160
CPC H01L 25/167; H01L 25/0753; H01L 27/15;
H01L 27/156; H01L 31/02; H01L 33/00;
H01L 33/04; H01L 33/08; H01L 33/10;
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H01L 33/486; F21K 9/00; F21K
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See application file for complete search history.

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(57) **CLAIM**

The ornamental design for light emitting diodes on a printed circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a pattern and orientation of LEDs of the present disclosure;

FIG. 2 is a front view of the pattern and orientation of LEDs of FIG. 1;

FIG. 3 is a rear view of the pattern and orientation of LEDs of FIG. 1;

FIG. 4 is a right side view of the pattern and orientation of LEDs of FIG. 1;

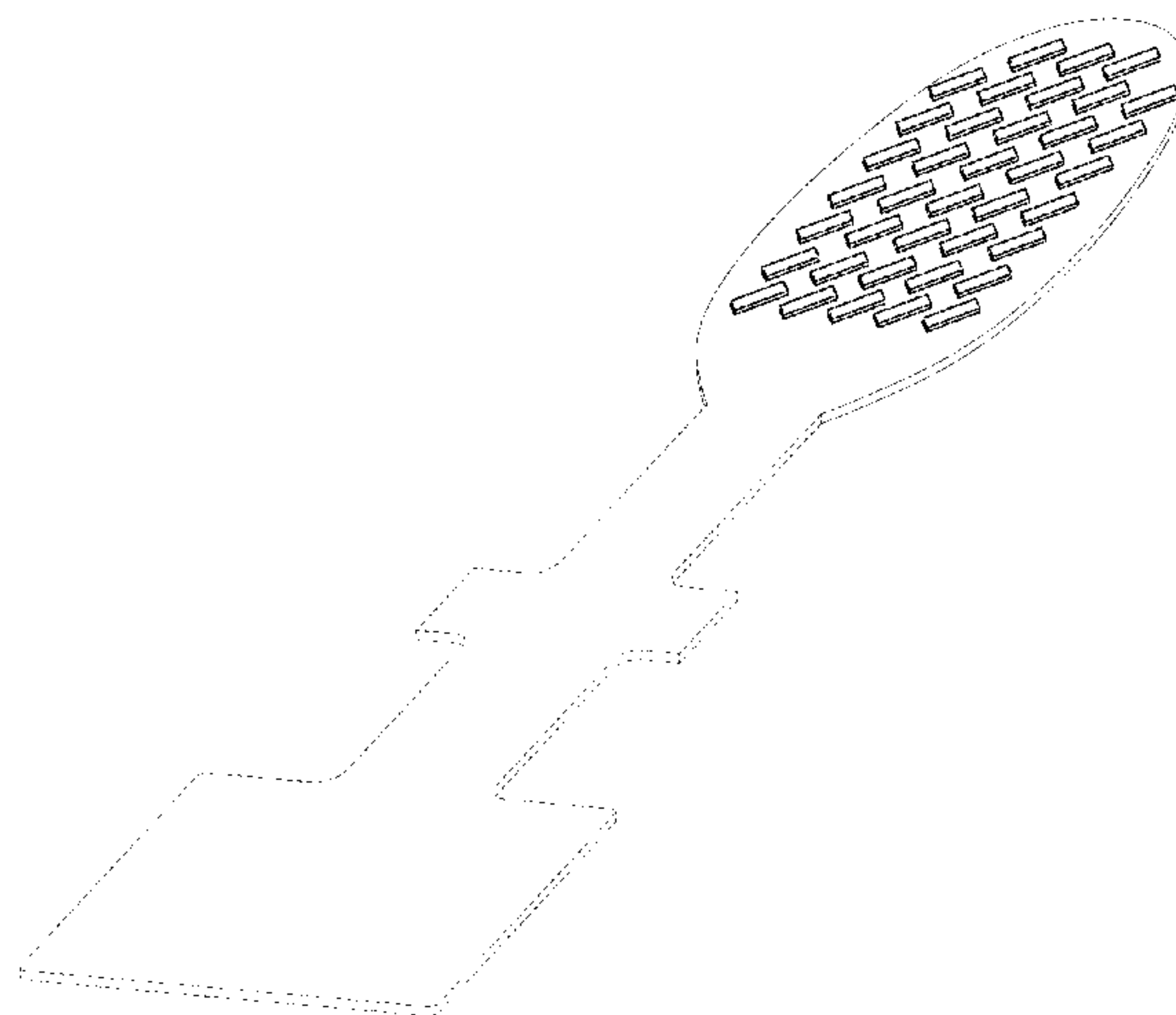
FIG. 5 is a left side view of the pattern and orientation of LEDs of FIG. 1;

FIG. 6 is a top view of the pattern and orientation of LEDs of FIG. 1; and,

FIG. 7 is a bottom view of the pattern and orientation of LEDs of FIG. 1.

The broken lines of FIGS. 1-7 are provided for purposes of illustration and form no part of the claimed design of the present application.

1 Claim, 5 Drawing Sheets



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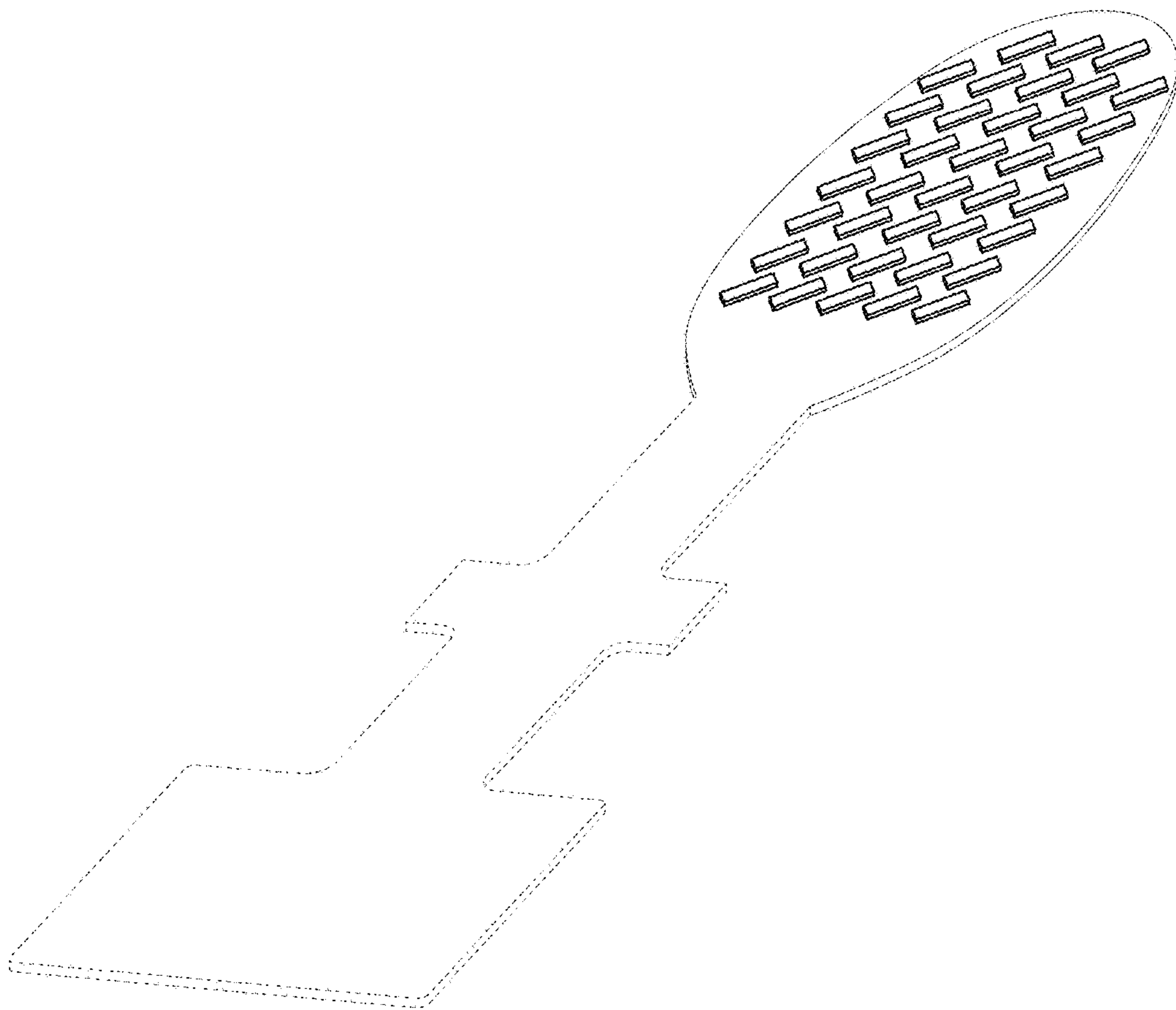


Fig. 1

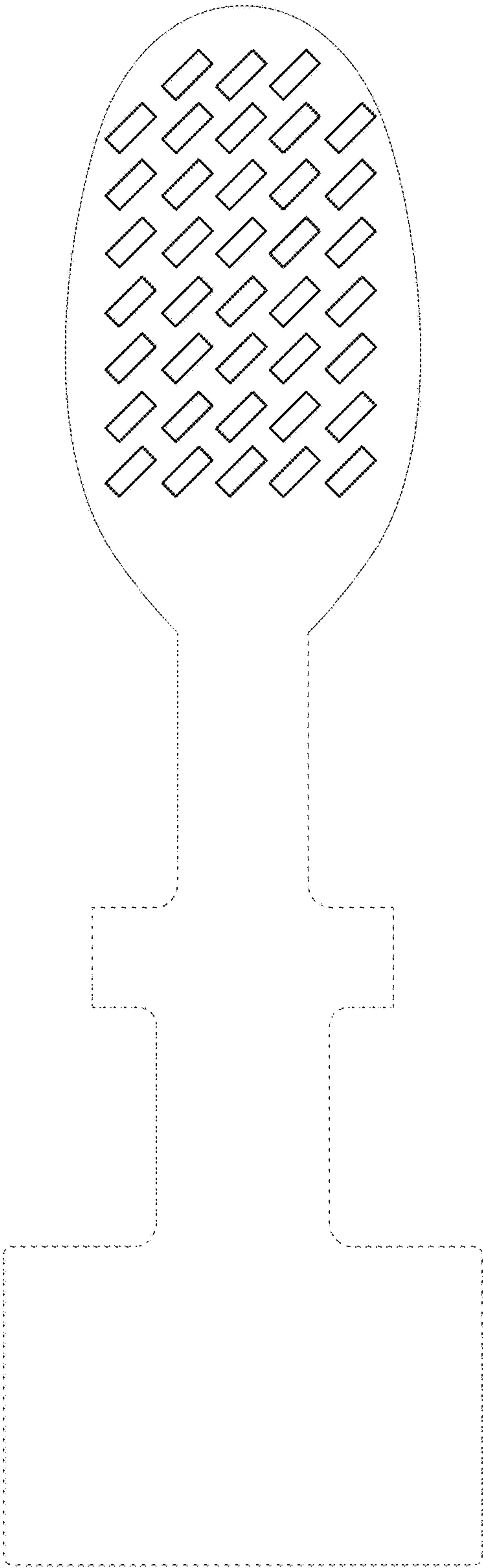


Fig. 2

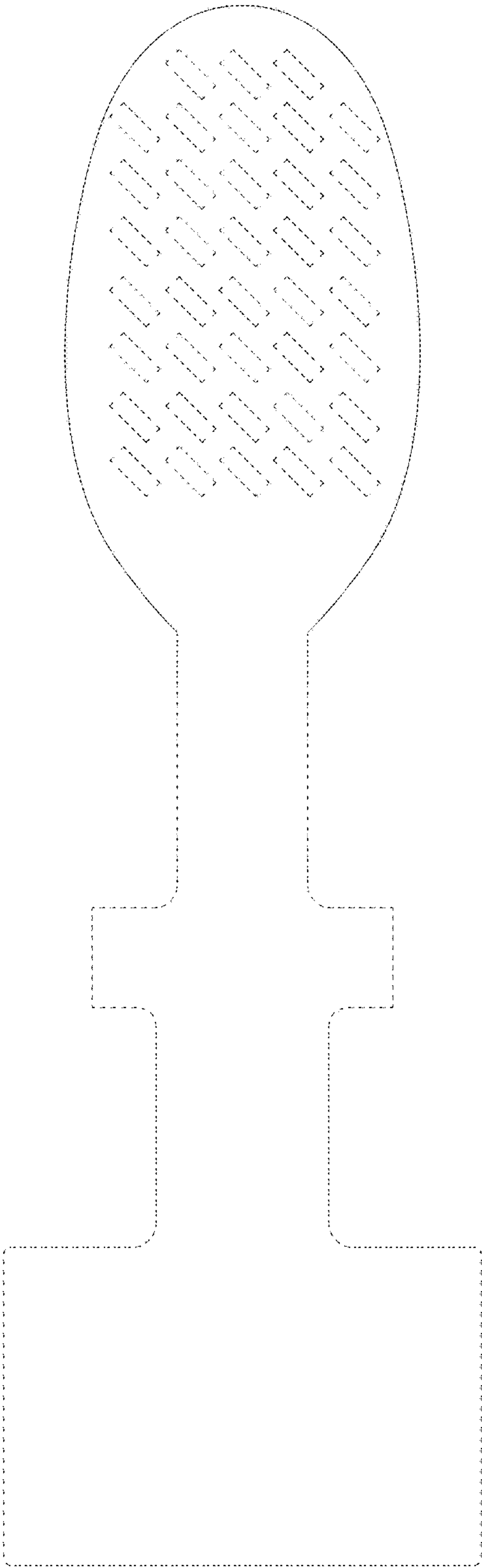


Fig. 3

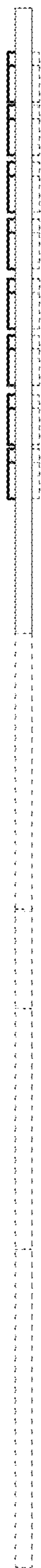


Fig. 4

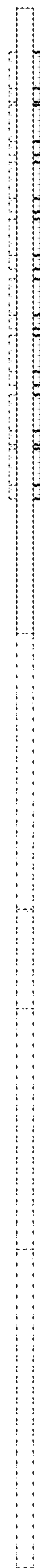


Fig. 5

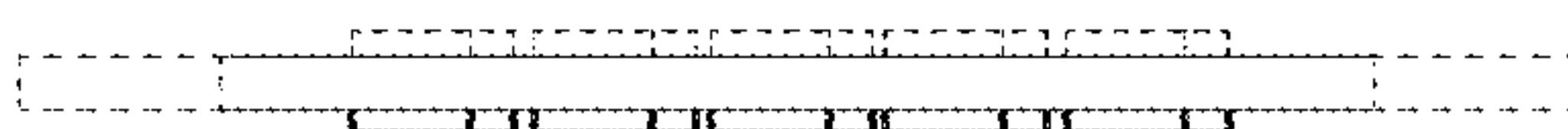


Fig. 6

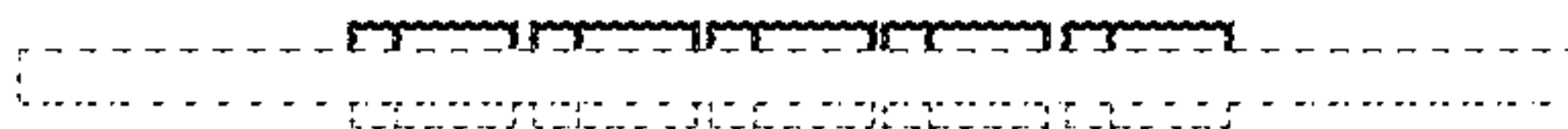


Fig. 7