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(12) **United States Design Patent** (10) **Patent No.:** **US D771,597 S**
Akana et al. (45) **Date of Patent:** **** Nov. 15, 2016**

(54) **ELECTRONIC DEVICE**

(56) **References Cited**

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U.S. PATENT DOCUMENTS

D551,215 S *	9/2007	Lee, Jr.	D14/218
D558,757 S	1/2008	Andre et al.	
D580,387 S	11/2008	Andre et al.	
D606,040 S	12/2009	Andre et al.	
D617,746 S *	6/2010	Choi	D13/164
D618,217 S *	6/2010	Harden	D14/218
D620,488 S	7/2010	Andre et al.	
D630,630 S	1/2011	Andre et al.	
D645,848 S *	9/2011	Arnholt	D14/218
D663,289 S *	7/2012	Murchison	D14/218
D665,382 S *	8/2012	Kim	D14/218
D673,947 S	1/2013	Andre et al.	
D673,948 S	1/2013	Andre et al.	
D673,949 S	1/2013	Andre et al.	

(Continued)

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/218**

(58) **Field of Classification Search**
USPC D14/137, 174, 217–218, 240, 247,
D14/388–390, 396, 511; D21/516;
D13/162–164, 168; 715/738; 434/350;
600/323; 348/211.99, 14.05, 114, 734;
341/175–176; 455/575.1
CPC G08C 23/04; G08C 19/12; H04N 5/44;
H04L 17/02

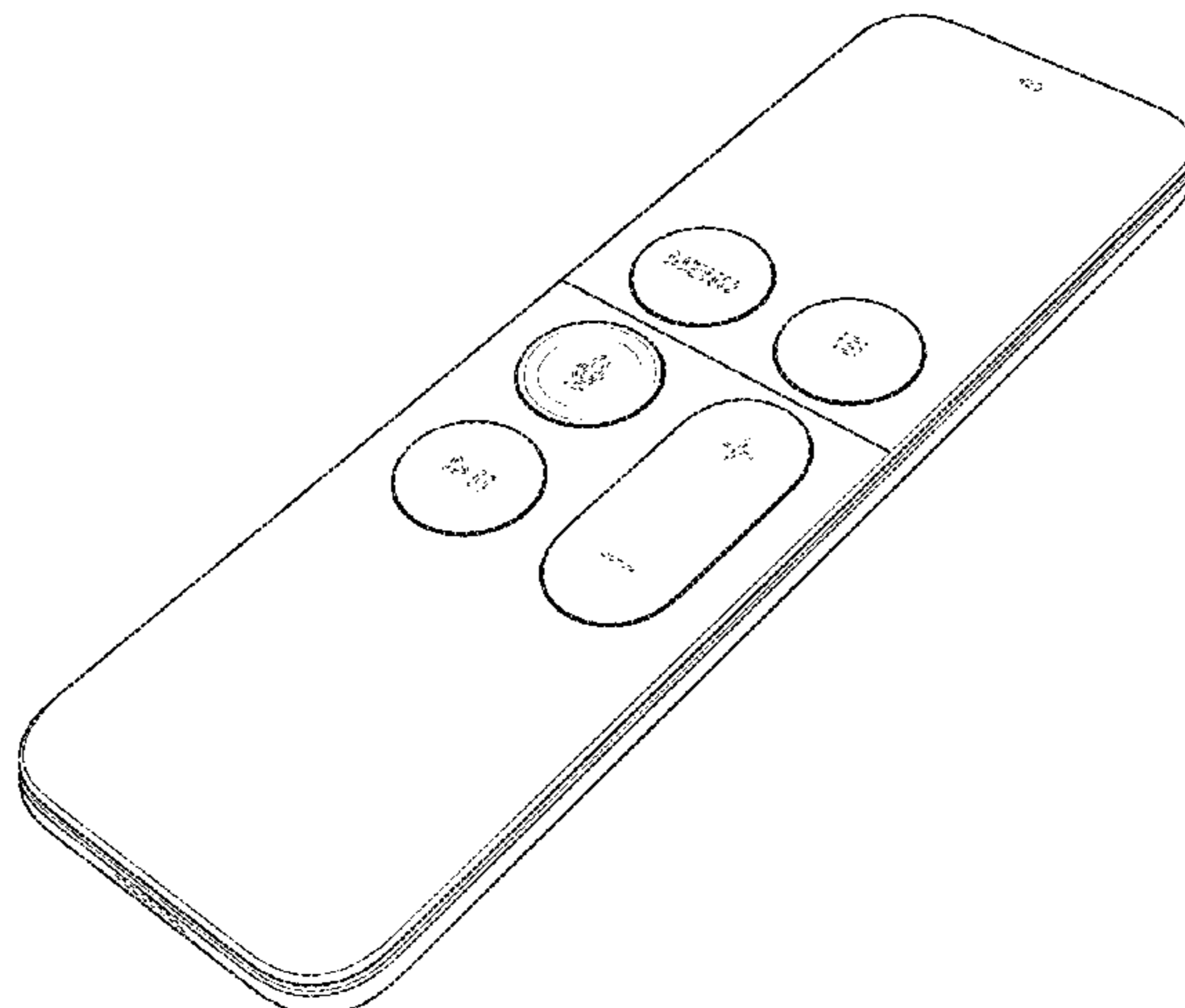
See application file for complete search history.

(57) **CLAIM**
The ornamental design for an electronic device, as shown
and described.

DESCRIPTION

FIG. 1 is a front perspective view of an electronic device
showing our new design;
FIG. 2 is a rear perspective view thereof;
FIG. 3 is another front perspective view thereof;
FIG. 4 is another rear perspective view thereof;
FIG. 5 is a front view thereof;
FIG. 6 is a rear view thereof;
FIG. 7 is a left side view thereof;
FIG. 8 is a right side view thereof;
FIG. 9 is a top view thereof; and,
FIG. 10 is a bottom view thereof.
The dashed broken lines represent structure or features that
form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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(56)

References Cited

U.S. PATENT DOCUMENTS

D692,417 S * 10/2013 Tu D14/218
D704,167 S * 5/2014 Akana D14/218
D706,302 S * 6/2014 Akana D14/486

D714,762 S * 10/2014 O'Neil D14/218
D728,530 S * 5/2015 Shin D14/218
D743,944 S * 11/2015 Karrman D14/218
D759,008 S 6/2016 Akana et al.

* cited by examiner

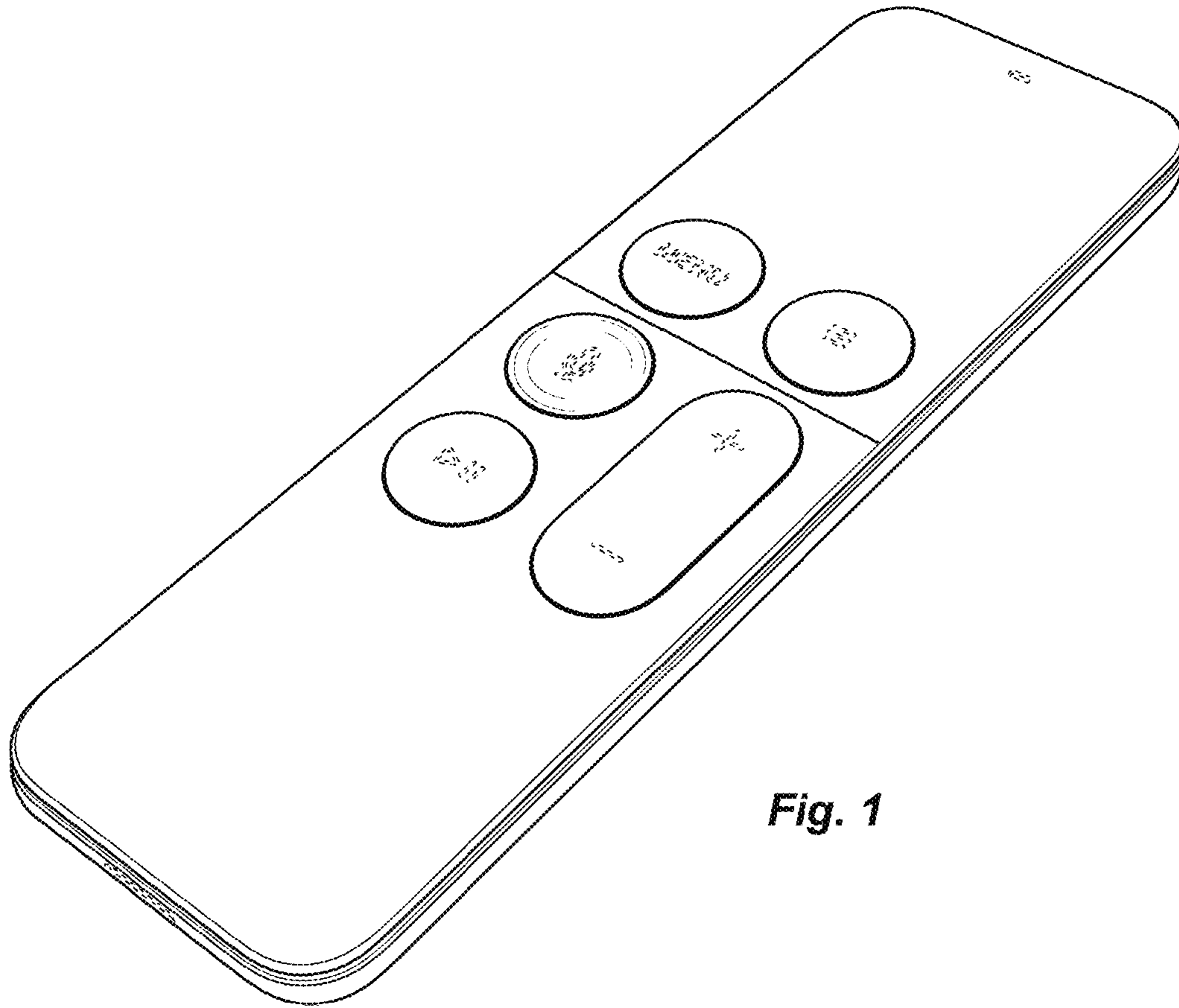


Fig. 1

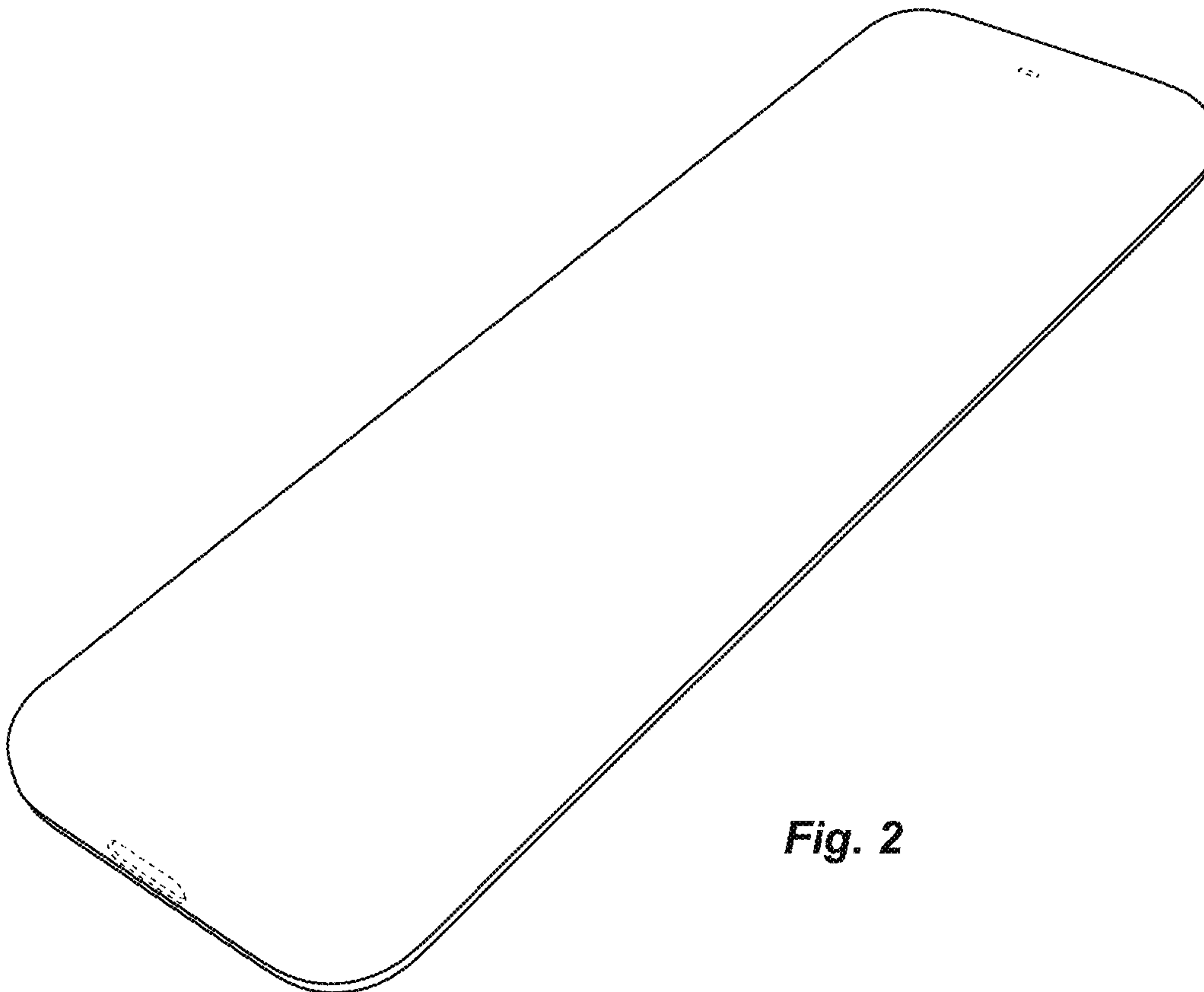


Fig. 2

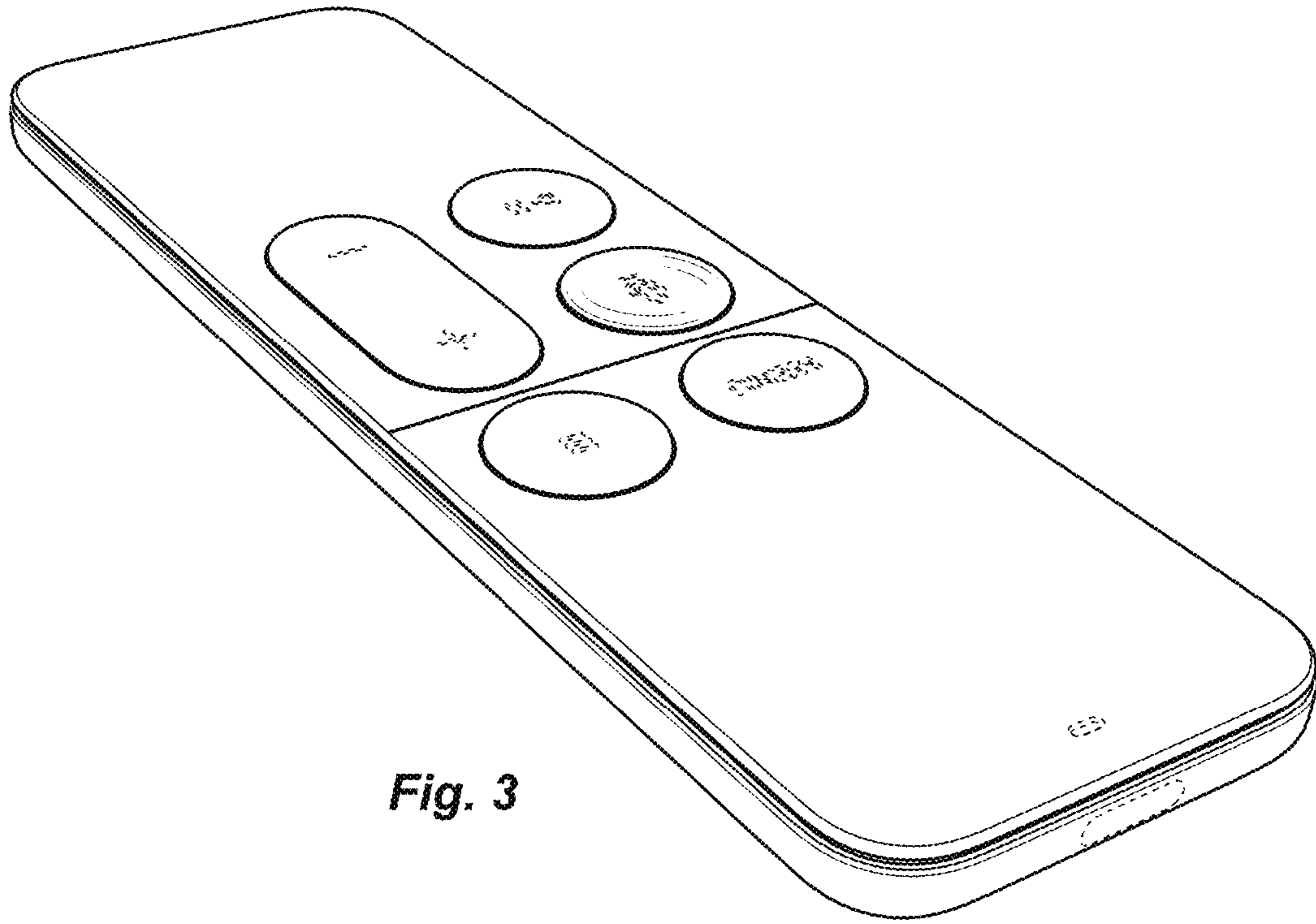


Fig. 3

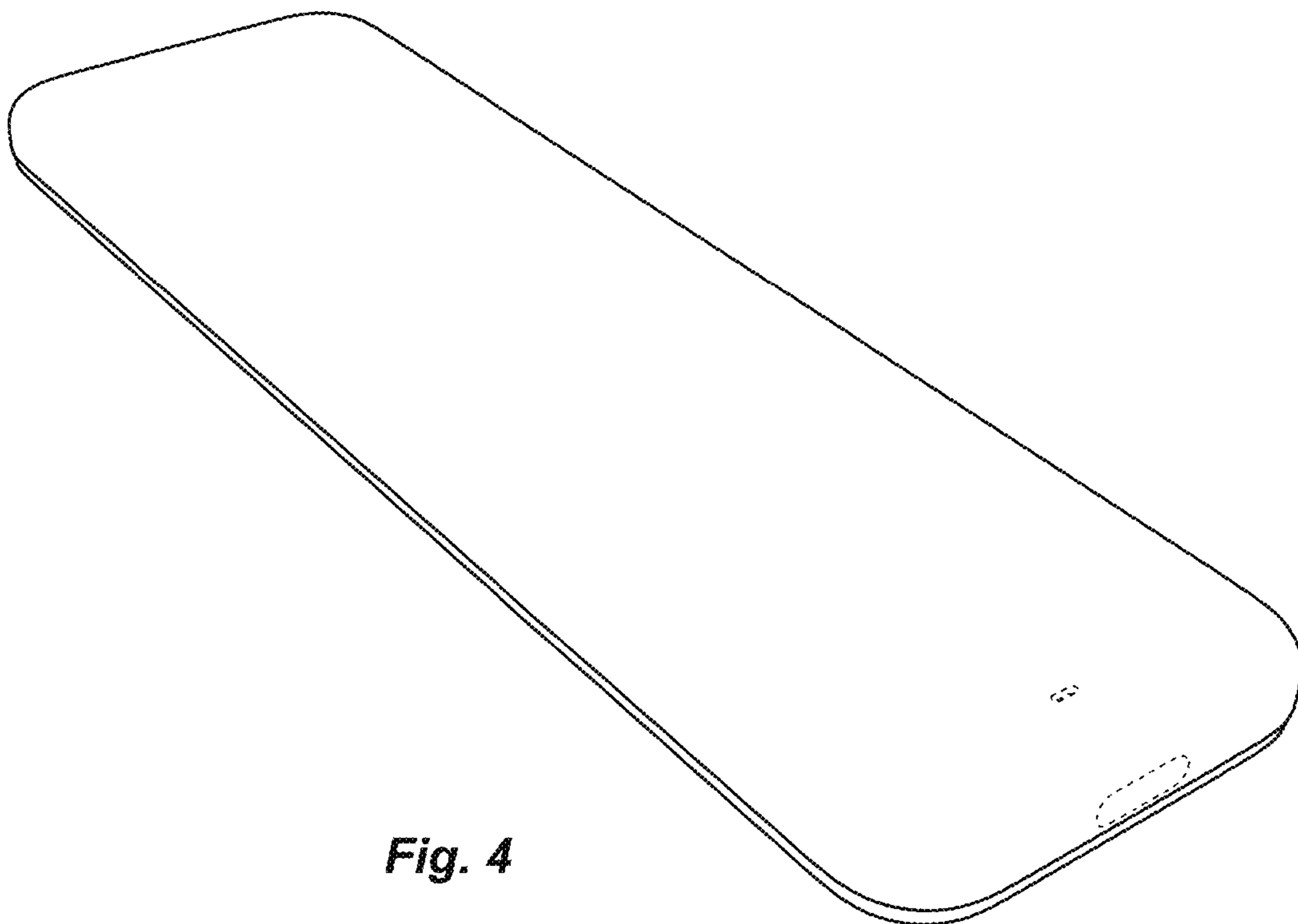


Fig. 4

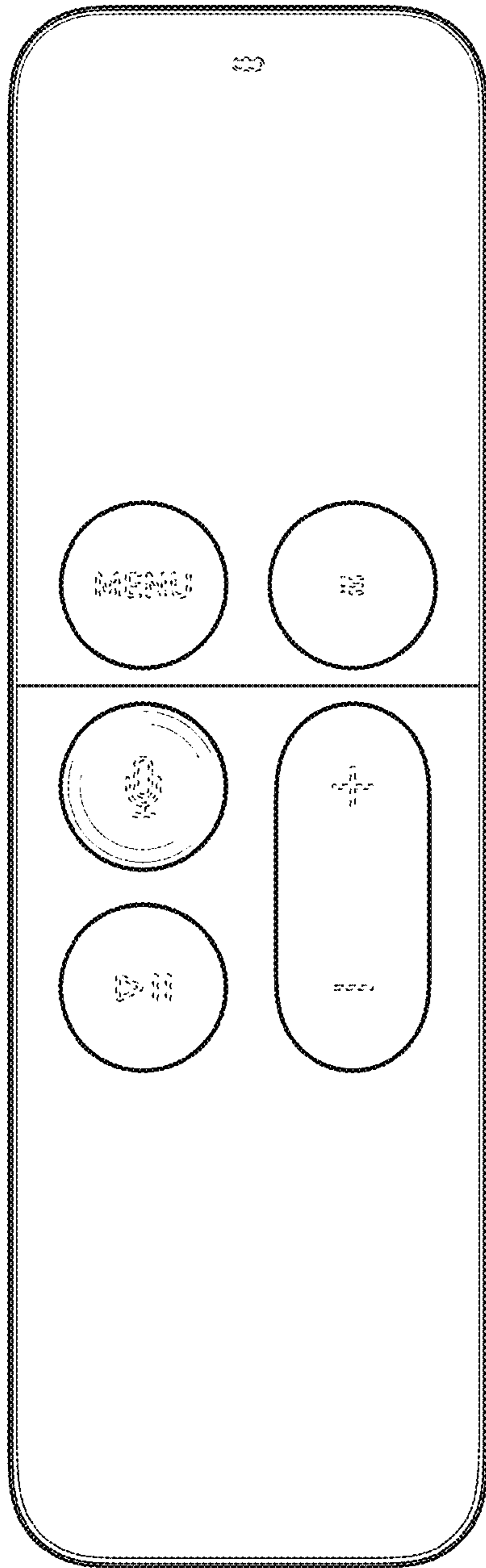


Fig. 5



Fig. 6

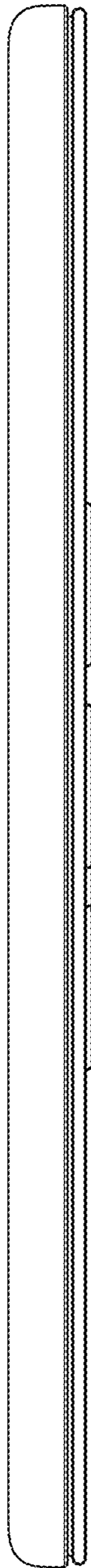


Fig. 7



Fig. 8



Fig. 9

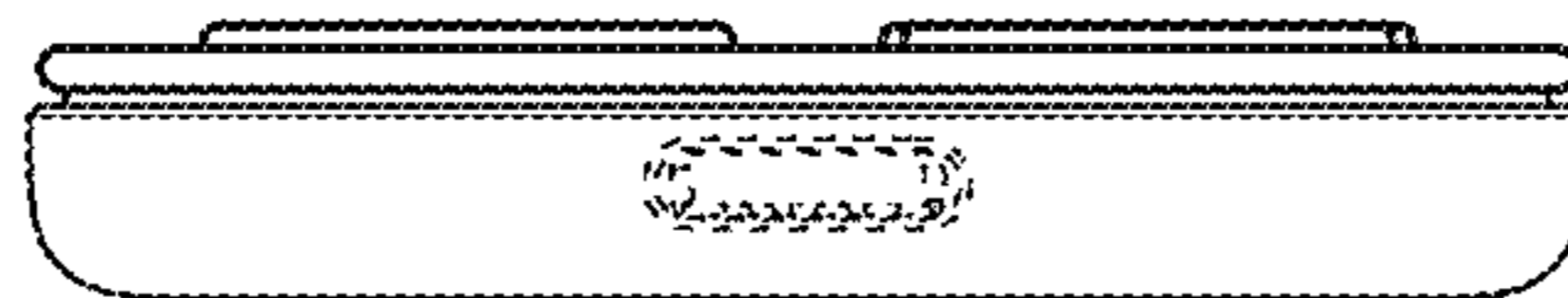


Fig. 10