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(12) **United States Design Patent** (10) **Patent No.:** **US D770,431 S**  
**Lee et al.** (45) **Date of Patent:** **\*\* Nov. 1, 2016**

(54) **ELECTRONIC DEVICE**

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(\*\*) Term: **14 Years**

(Continued)

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(22) Filed: **Aug. 8, 2014**

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**Related U.S. Application Data**

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(51) **LOC (10) Cl.** ..... **14-03**

(52) **U.S. Cl.**  
USPC ..... **D14/248**

(58) **Field of Classification Search**  
USPC ..... D14/138 G, 138 AD, 341, 138 R, 496,  
D14/203.1, 203.3, 203.5, 203.7, 248, 218,  
D14/250; 455/575.1, 556.2, 575.3, 575.4;  
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361/679.3, 679.56; D13/168  
CPC ..... G06F 1/1626; H04M 1/0266  
See application file for complete search history.

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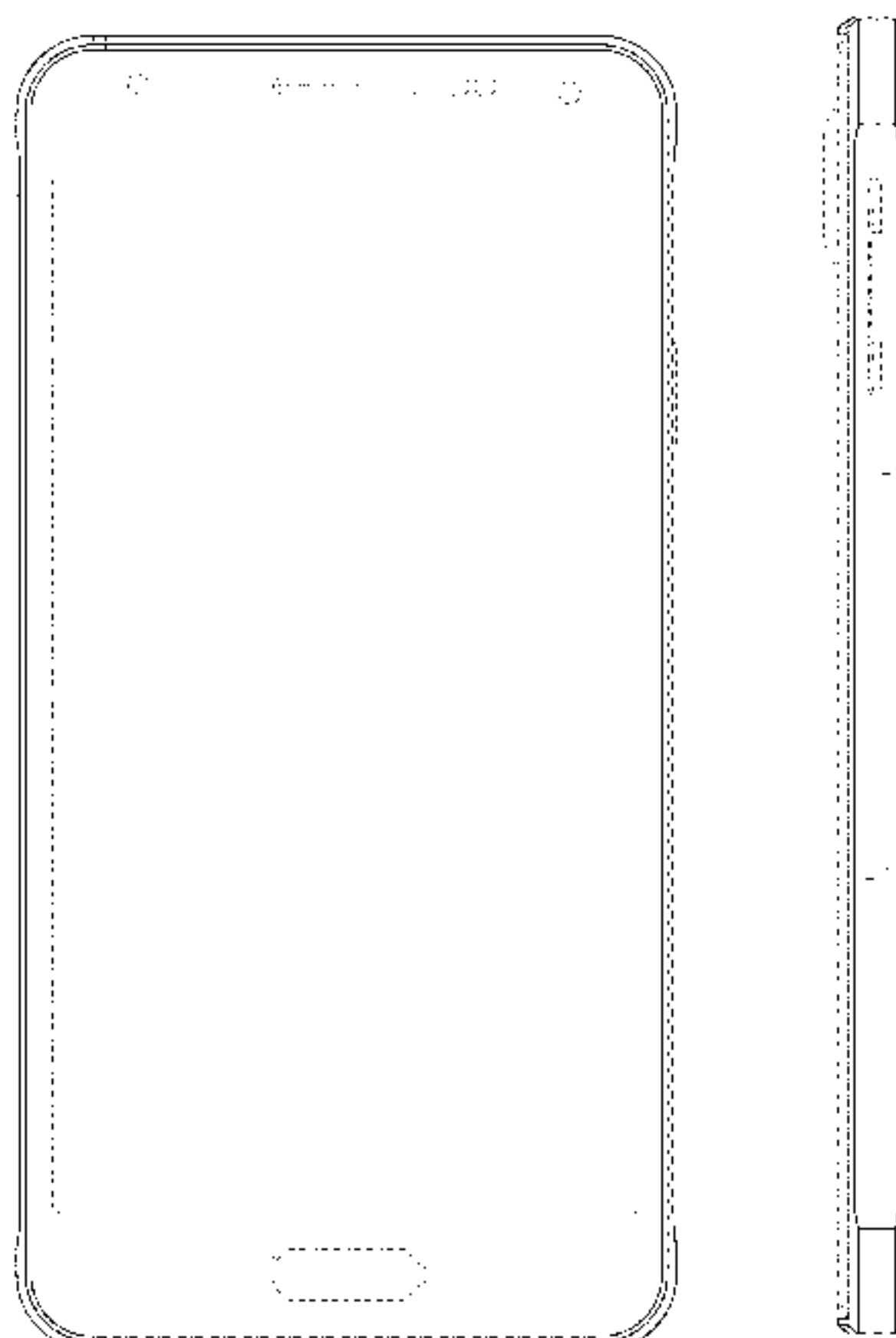
(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

**DESCRIPTION**

FIG. 1 is a top right front perspective view of an electronic device showing our new design;  
FIG. 2 is a front view thereof;  
FIG. 3 is a rear view thereof;  
FIG. 4 is a left-side elevation view thereof;  
FIG. 5 is a right-side elevation view thereof;  
FIG. 6 is a top plan view thereof; and,  
FIG. 7 is a bottom plan view thereof.  
The broken lines shown in the figures that are immediately adjacent to the shaded areas and define unshaded regions represent the bounds of the claim. All other broken lines, including the features within broken line boundaries, the short stripes at the top and bottom, and the broken lines outside of the claimed design, are directed to environment. The broken lines form no part of the claimed design.

**1 Claim, 5 Drawing Sheets**



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FIG.1

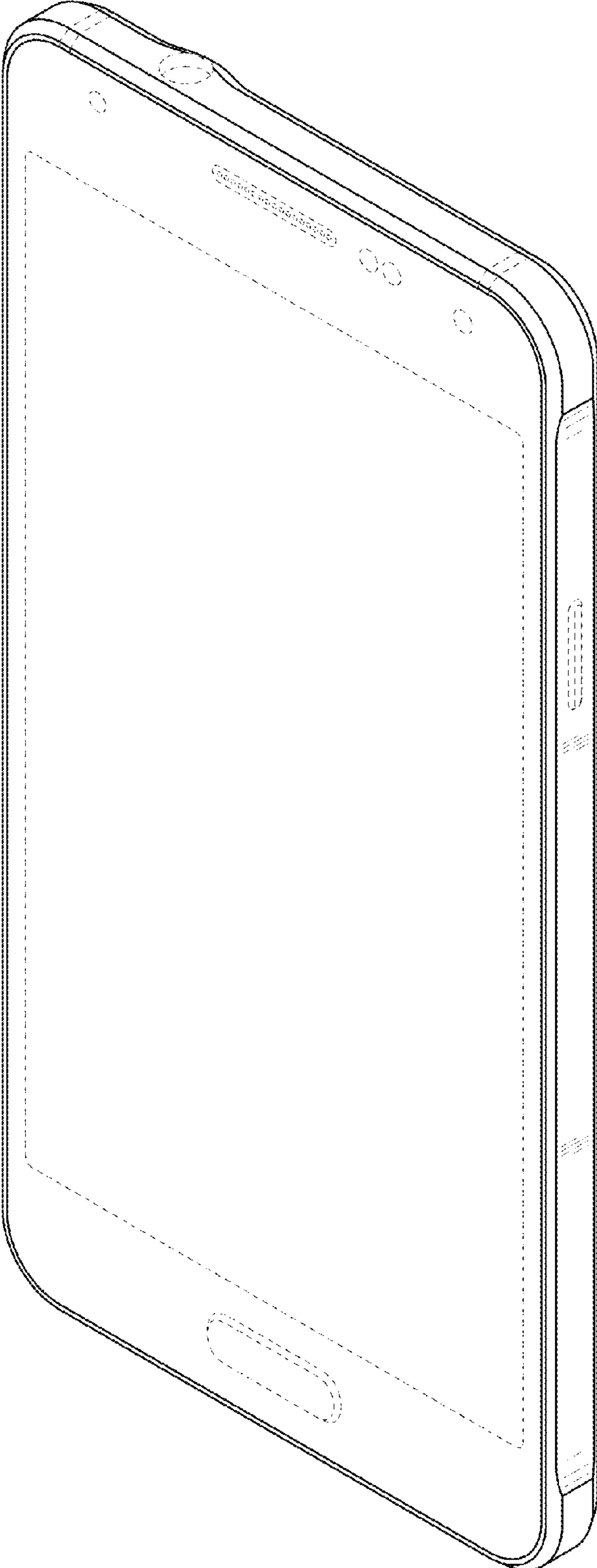


FIG.2

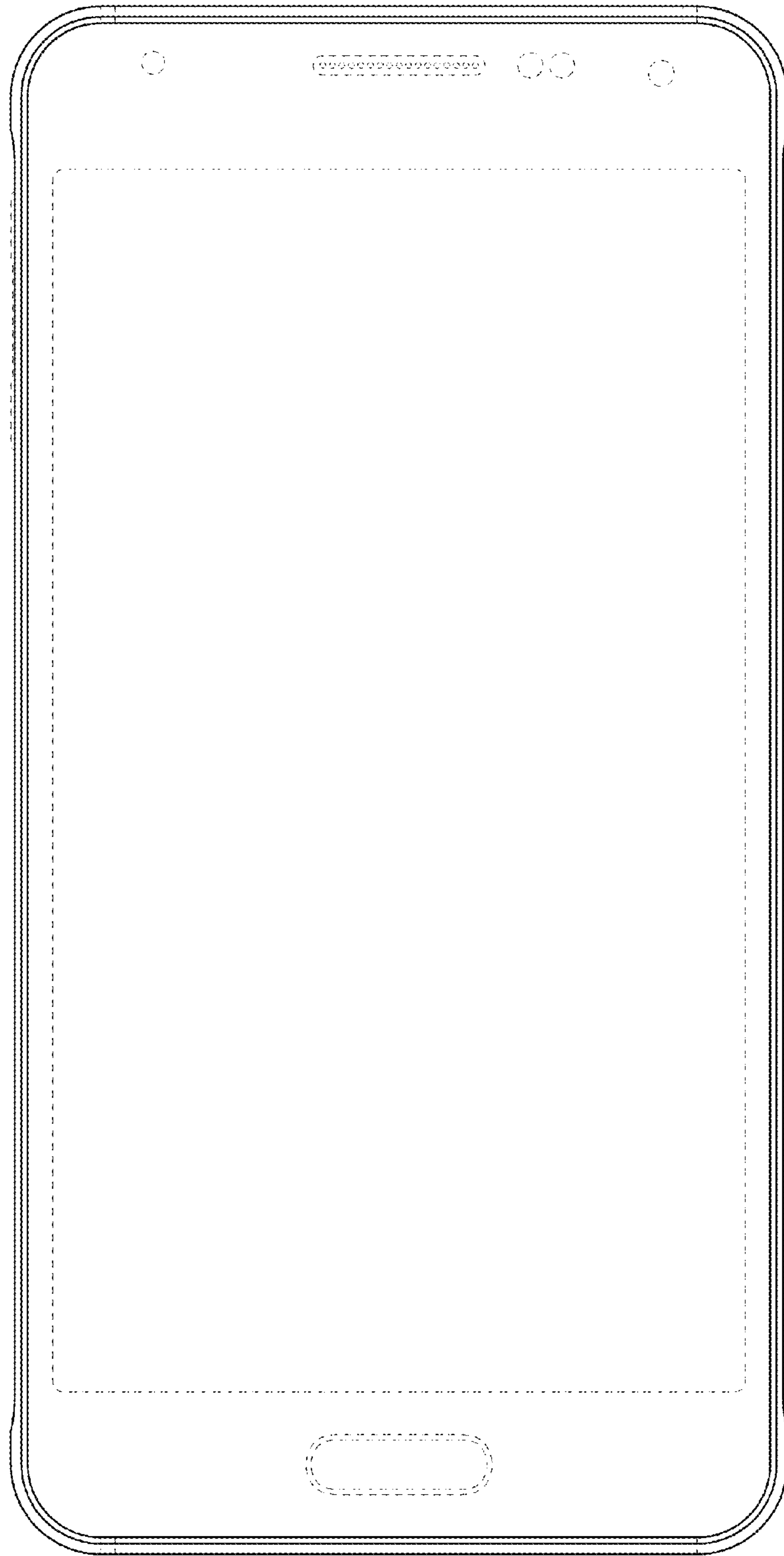


FIG.3

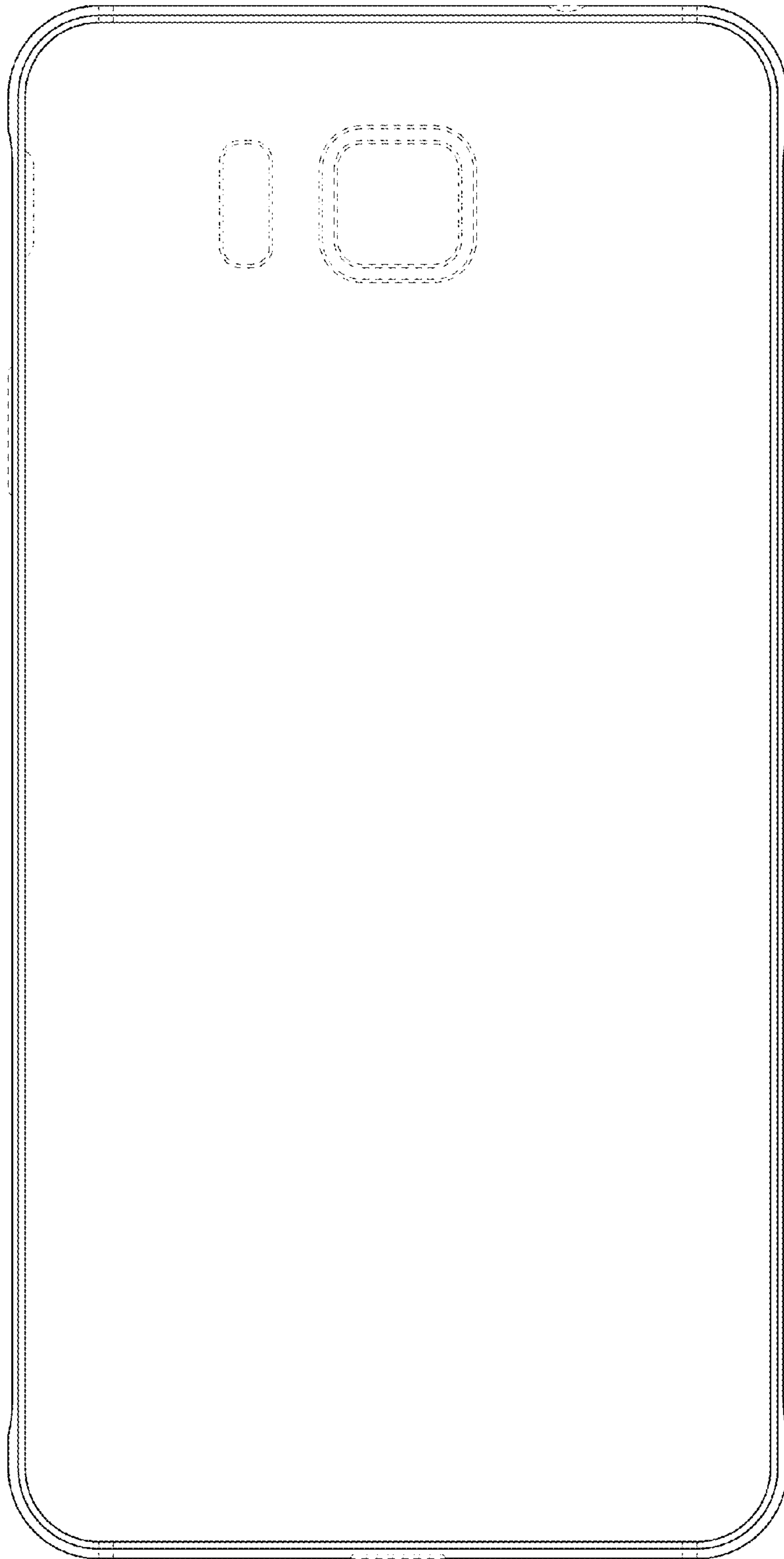




FIG.4

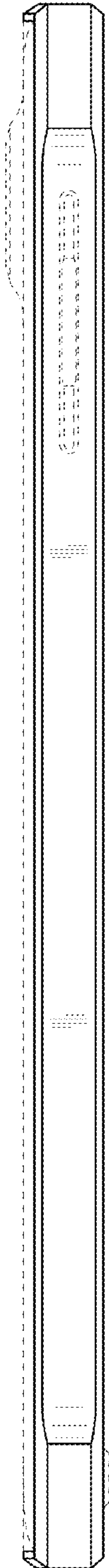


FIG.5

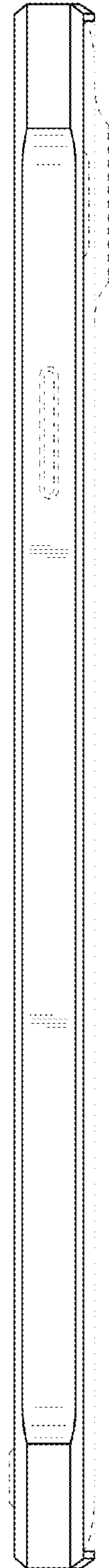


FIG.6

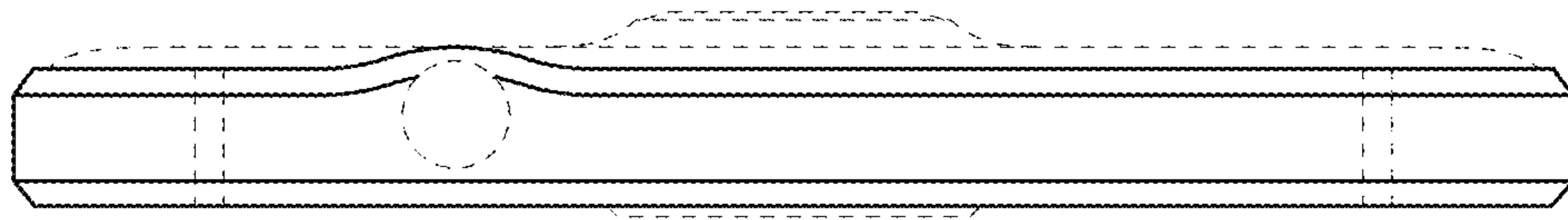


FIG.7

