



US00D769293S

(12) **United States Design Patent**
Colbeck et al.

(10) **Patent No.:** **US D769,293 S**
(45) **Date of Patent:** **** Oct. 18, 2016**

(54) **DISPLAY SCREEN WITH GRAPHICAL USER INTERFACE FOR NETWORK TOPOLOGY DISPLAY**

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(**) Term: **15 Years**

(21) Appl. No.: **29/531,669**

(22) Filed: **Jun. 29, 2015**

(51) **LOC (10) Cl.** **14-04**

(52) **U.S. Cl.**
USPC **D14/486; D14/488**

(58) **Field of Classification Search**
USPC D14/485–488
CPC G06F 11/00; G06F 11/36; G06F 12/00;
G06F 15/177; H04L 41/12; H04L 41/28
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

5,226,120	A *	7/1993	Brown	H04L 41/12 709/224
D414,758	S *	10/1999	Hodgson	D14/486
D425,496	S *	5/2000	Faris	D14/485
D426,525	S *	6/2000	Coleman	D14/486
D431,038	S *	9/2000	Faris	D14/486
D432,544	S *	10/2000	Faris	D14/486
D433,392	S *	11/2000	Hodgson	D14/486

7,315,985	B1 *	1/2008	Gauvin	H04L 41/12 715/734
D599,363	S *	9/2009	Mays	D14/485
D604,316	S *	11/2009	Hoefnagels	D14/485
D642,587	S *	8/2011	Jones	D14/485
D655,719	S *	3/2012	Zaman	D14/486
D687,051	S *	7/2013	Gardner	D14/486
D695,779	S *	12/2013	Edwards	D14/488
D724,603	S *	3/2015	Williams	D14/485
D727,339	S *	4/2015	Lee	D14/486
D733,728	S *	7/2015	Guner	D14/486
2003/0097438	A1 *	5/2003	Bearden	H04L 12/2697 709/224

OTHER PUBLICATIONS

Norris, Jim, Just How Fast is your PC, Feb. 6, 2014, pcworld.com [online], [site visited Jun. 2, 2016]. Available from Internet: <<http://www.pcworld.com/article/2094560/the-truth-about-your-pc-these-benchmark-tools-tell-all.html>>.*

Tomaszewski, How to design a network for Openstack, Feb. 23, 2014, rtomaszewski.blogspot.com [online], [site visited Jun. 2, 2016]. Available from Internet: <<http://rtomaszewski.blogspot.com/2014/02/how-to-design-network-for-openstack-or.html>>.*

* cited by examiner

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(57) **CLAIM**

The ornamental design for a display screen with graphical user interface for network topology display, as shown and described.

DESCRIPTION

FIG. 1 is a front view of a first image in a sequence for a display screen with graphical user interface for network topology display;

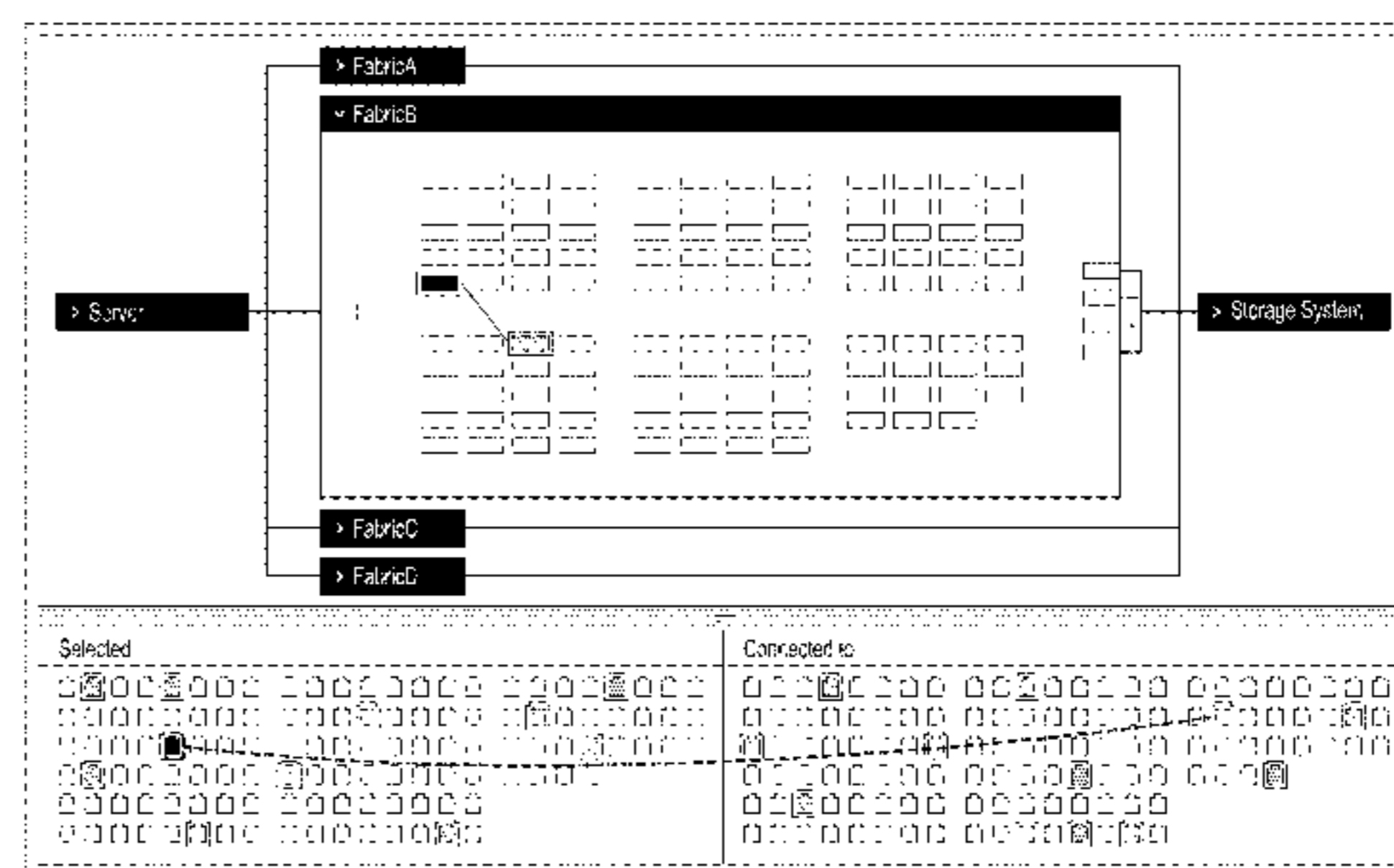
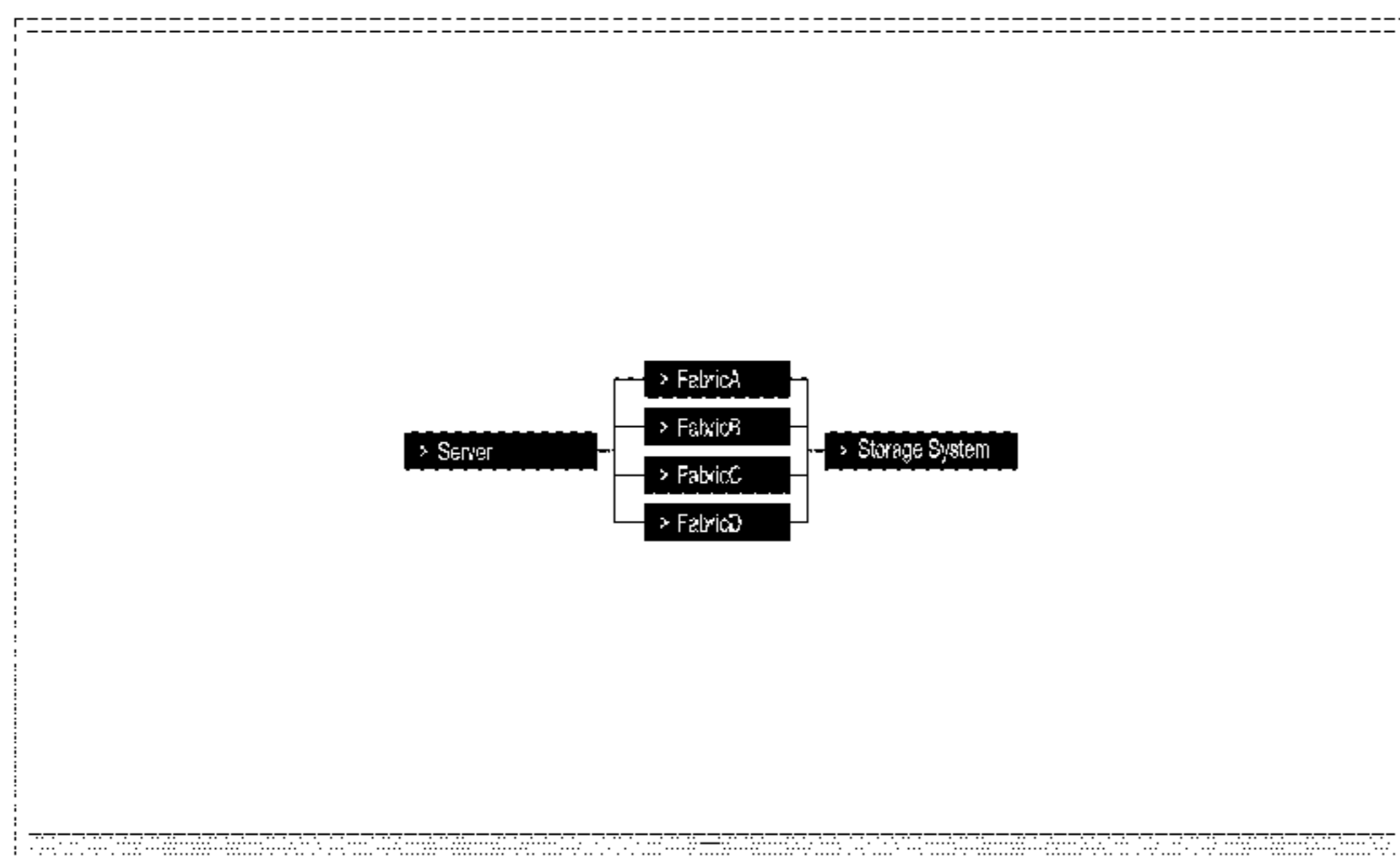
FIG. 2 is a second image thereof;

FIG. 3 is a third image thereof; and,

FIG. 4 is a fourth image thereof.

The broken lines in the Figures show portions of a display screen which form no part of the claimed design. The appearance of the images in the Figures sequentially transition between the images shown in FIGS. 1-4. The process or period in which one image transitions to another forms no part of the claimed design.

1 Claim, 4 Drawing Sheets



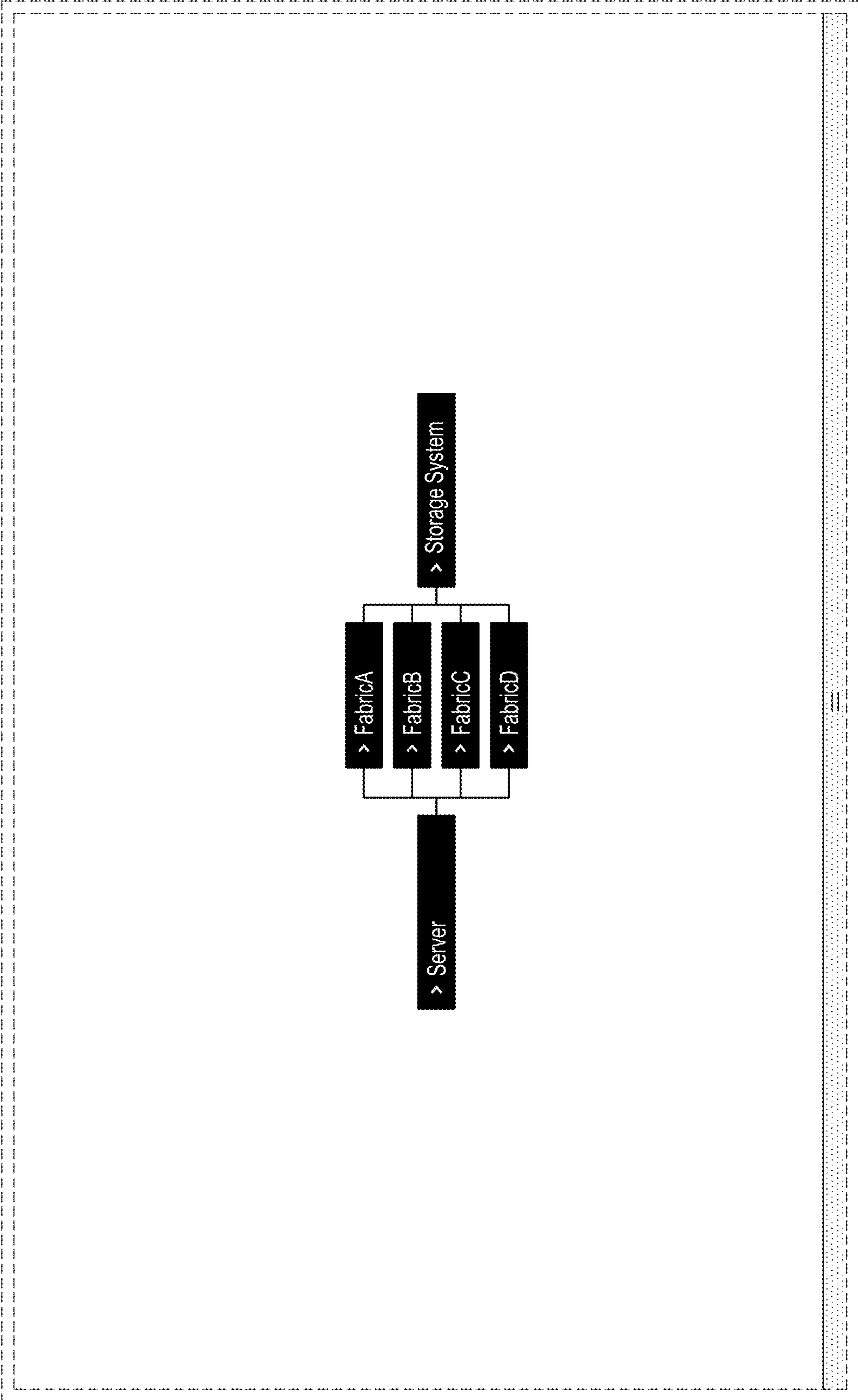


FIG. 1

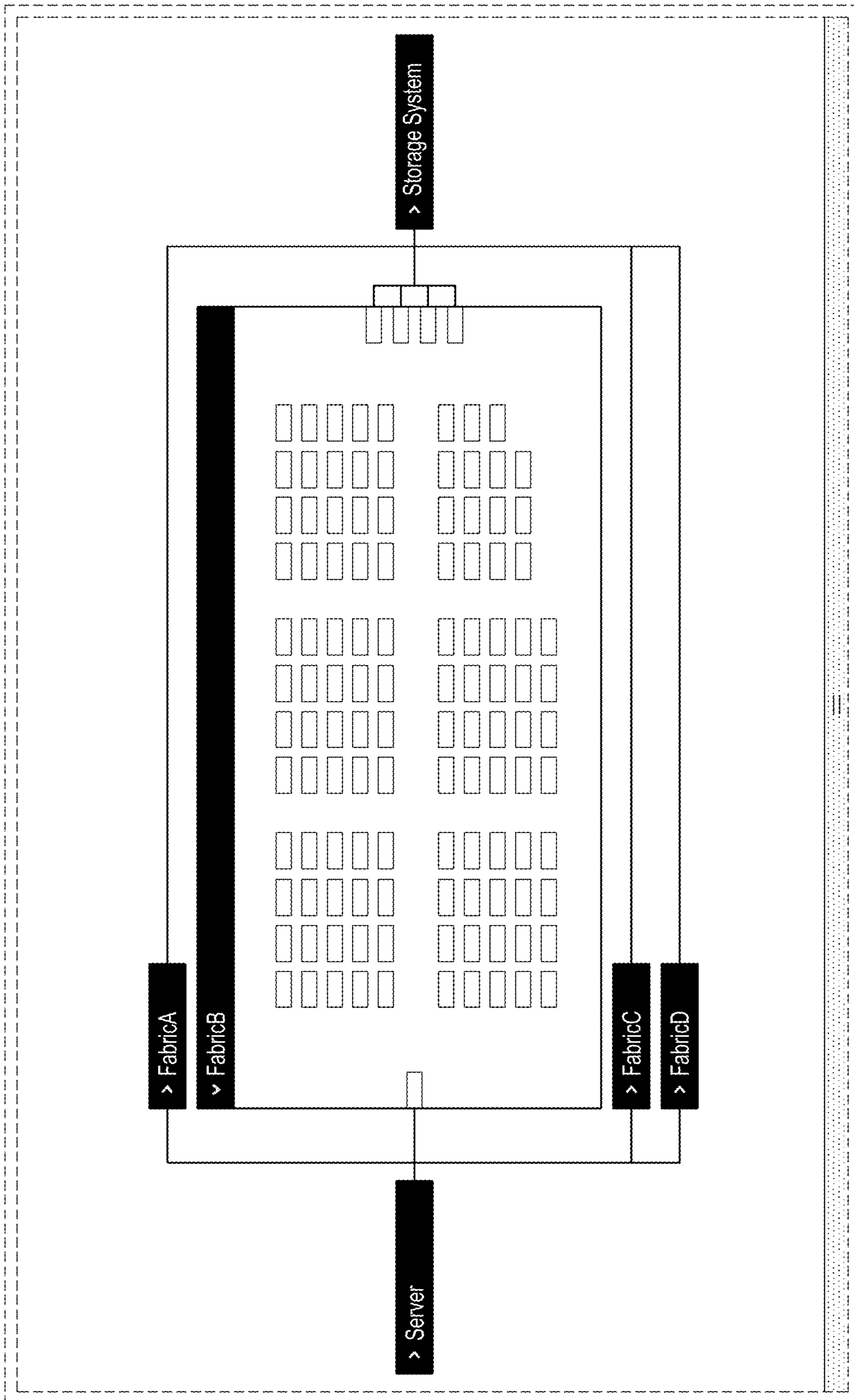


FIG. 2

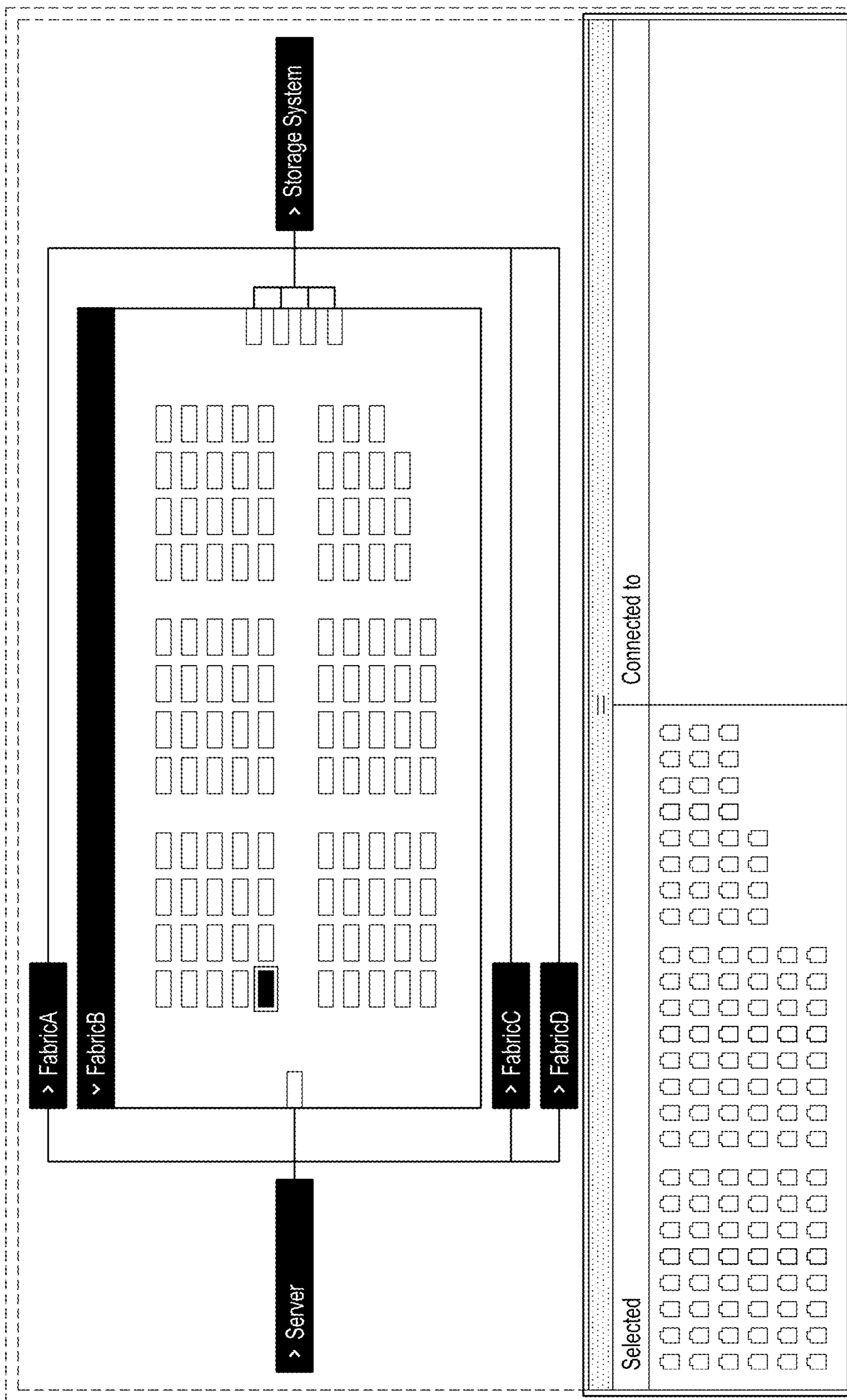


FIG. 3

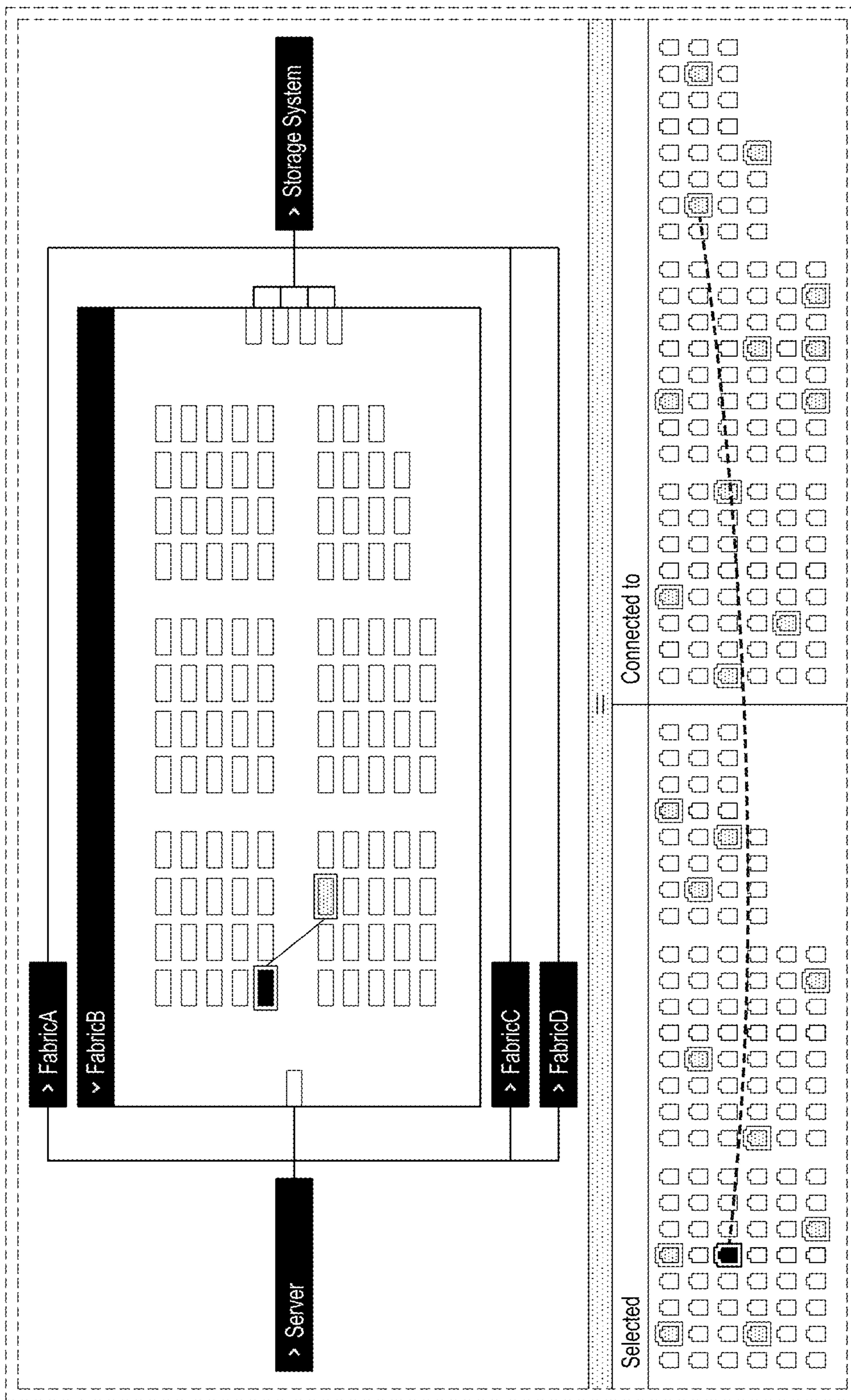


FIG. 4