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(12) **United States Design Patent** (10) **Patent No.:** **US D769,199 S**
Chen et al. (45) **Date of Patent:** ** Oct. 18, 2016

(54) **LIGHT-EMITTING DIODE UNIT**(71) Applicant: **EPISTAR CORPORATION**, Hsinchu (TW)(72) Inventors: **Chao Hsing Chen**, Hsinchu (TW); **Tsung Hsun Chiang**, Hsinchu (TW); **Chien Chih Liao**, Hsinchu (TW); **Wen Hung Chuang**, Hsinchu (TW); **Bo Jiun Hu**, Hsinchu (TW); **Tzu Yao Tseng**, Hsinchu (TW); **Jia Kuen Wang**, Hsinchu (TW); **Tsun Kai Ko**, Hsinchu (TW); **Chien Fu Shen**, Hsinchu (TW); **Kuan Yi Lee**, Hsinchu (TW); **Min Yen Tsai**, Hsinchu (TW)(73) Assignee: **EPISTAR CORPORATION**, Hsinchu (TW)(**) Term: **14 Years**(21) Appl. No.: **29/526,403**(22) Filed: **May 8, 2015**(30) **Foreign Application Priority Data**Nov. 10, 2014 (TW) 103306536
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CPC . H01L 25/167; H01L 25/0753; H01L 27/15;
H01L 27/156; H01L 31/02; H01L 33/00;
H01L 33/04; H01L 33/08; H01L 33/10;
H01L 33/20; H01L 33/38; H01L 33/42;
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See application file for complete search history.

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Primary Examiner — Selina Sikder

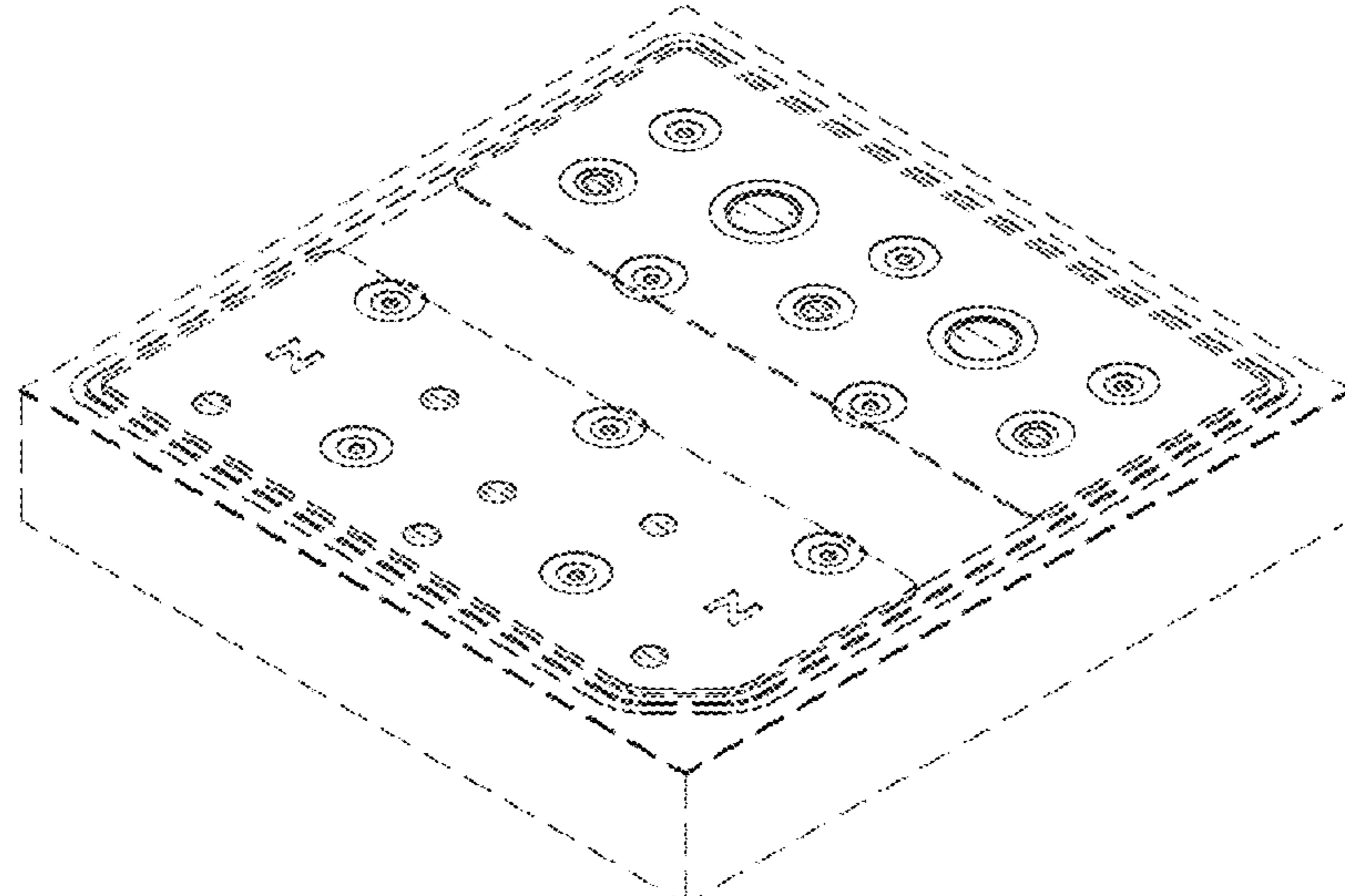
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(57) **CLAIM**

The ornamental design for a light-emitting diode unit, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a light-emitting diode unit showing our new design;
FIG. 2 is another perspective view thereof;
FIG. 3 is a front elevational view thereof;
FIG. 4 is a rear elevational view thereof;
FIG. 5 is a left side view thereof;
FIG. 6 is a right side view thereof;
FIG. 7 is a top plan view thereof; and,
FIG. 8 is a bottom plan view thereof.
The broken line showing is for the purpose of illustrating environmental structure only and forms no part of the claimed design.

1 Claim, 6 Drawing Sheets

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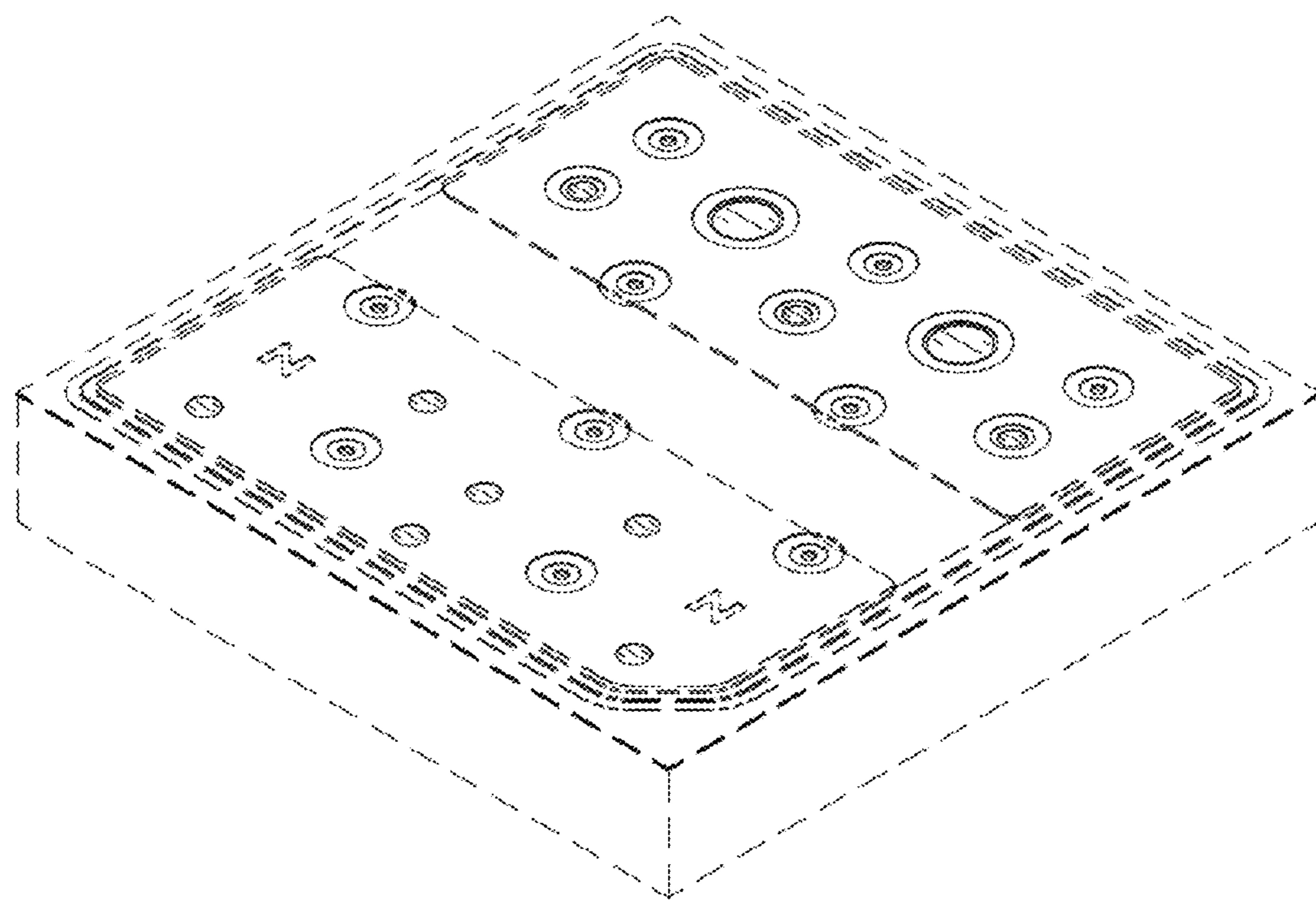


FIG. 1

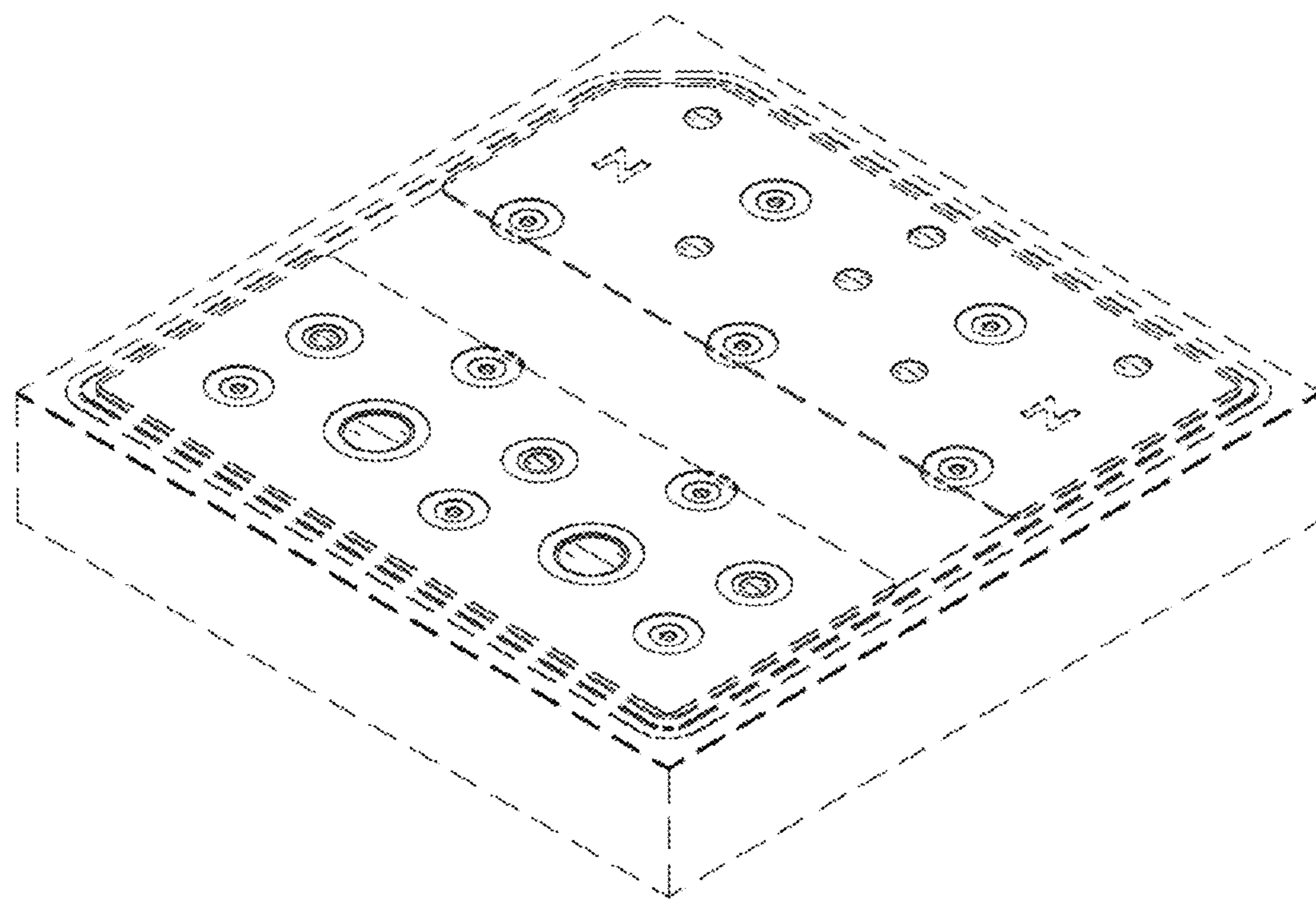


FIG. 2

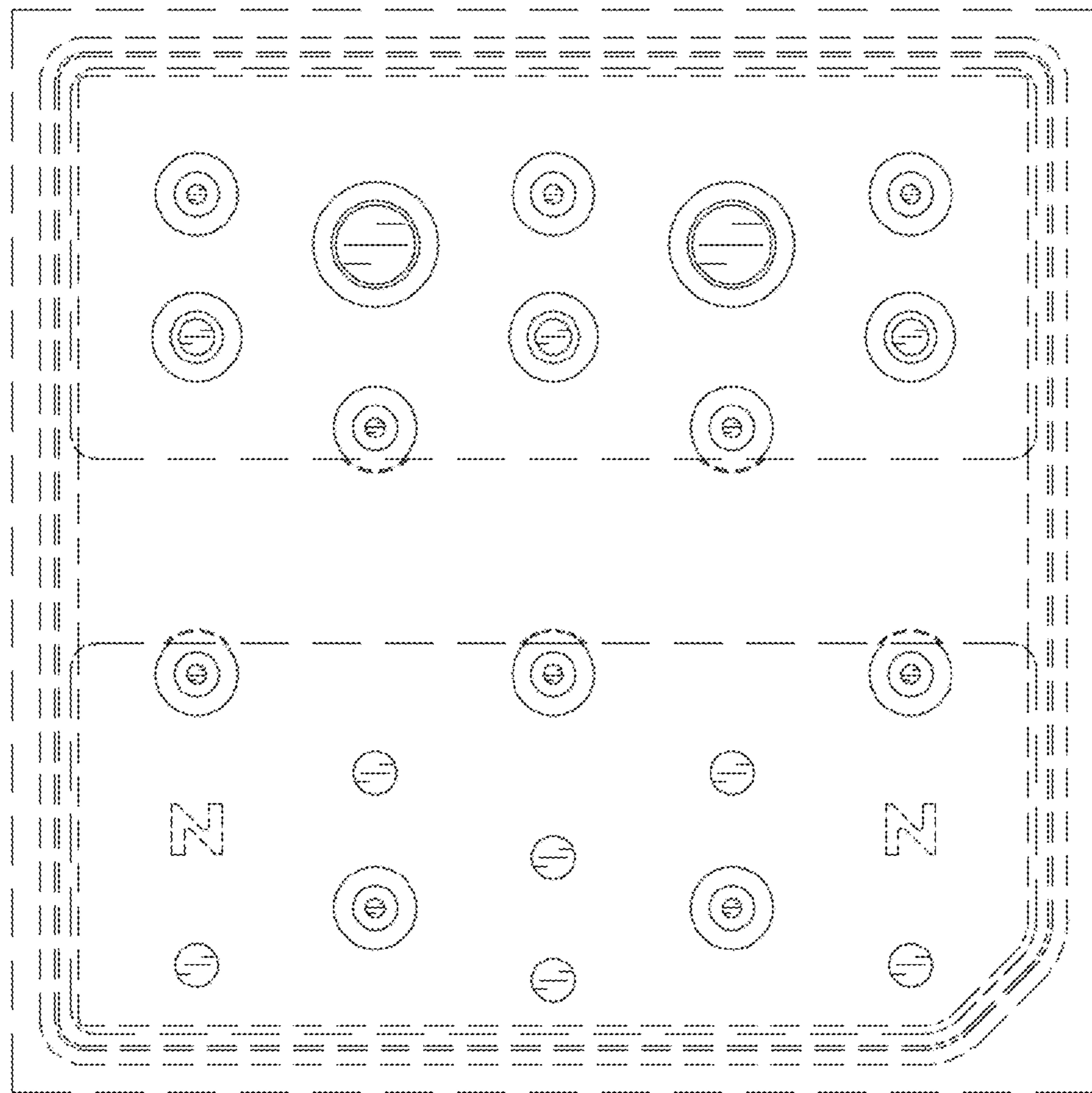


FIG. 3

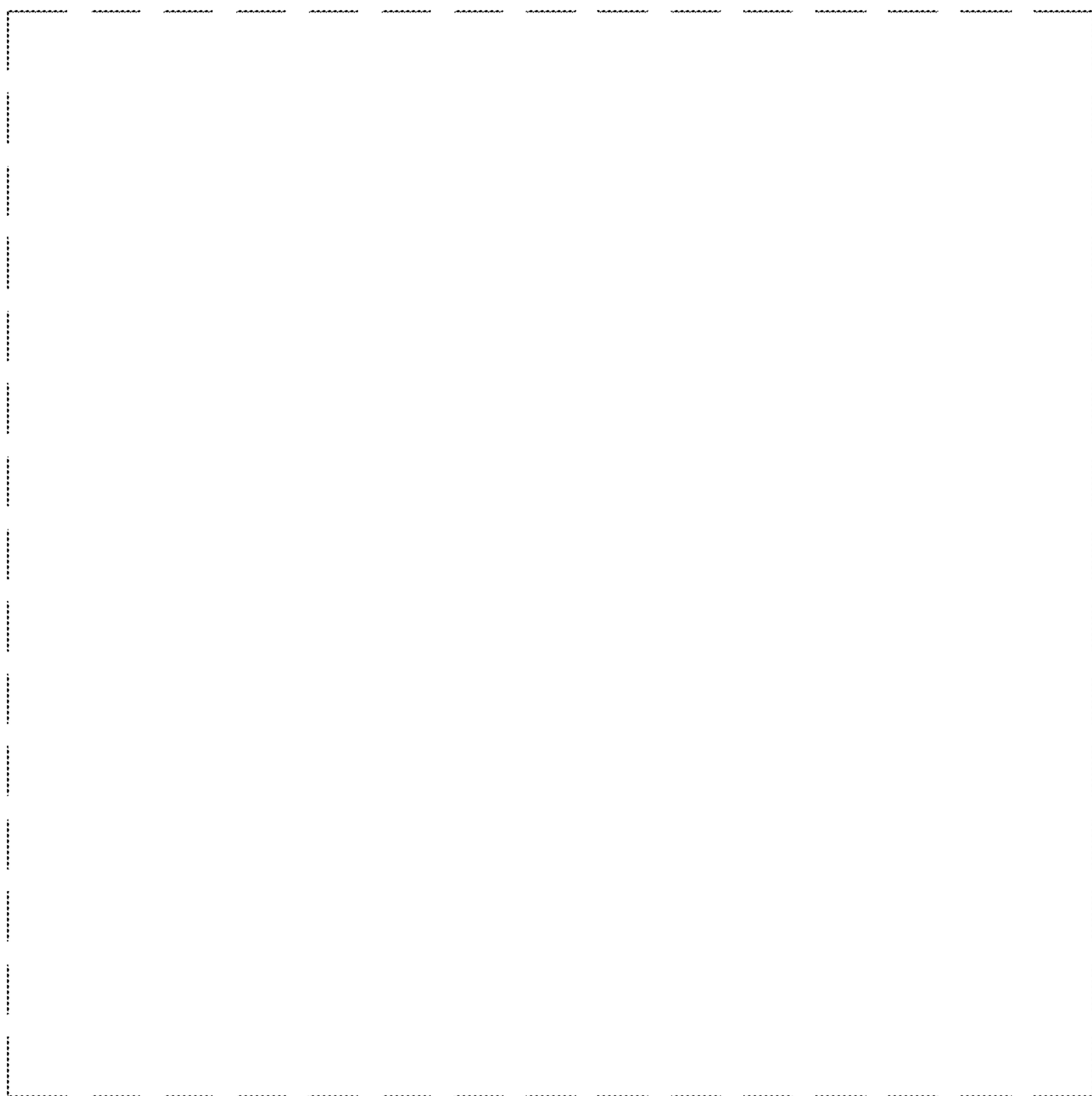


FIG. 4



FIG. 5

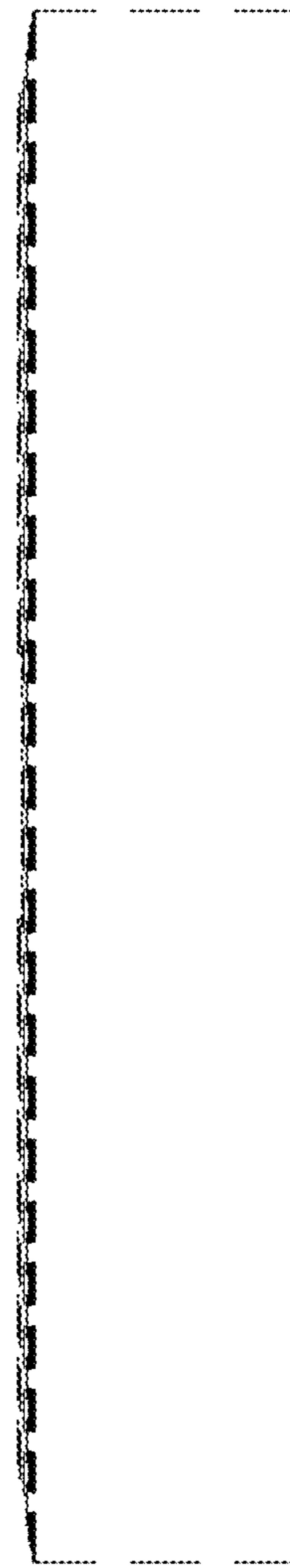


FIG. 6

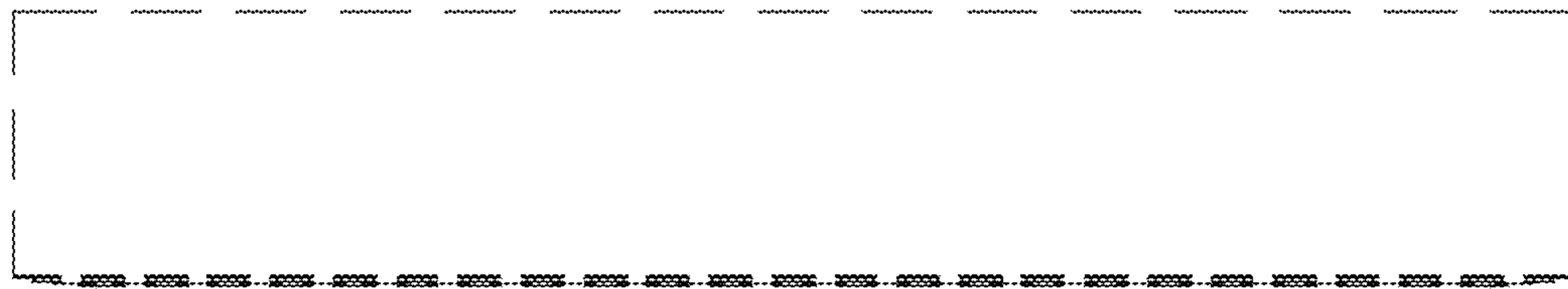


FIG. 7



FIG. 8