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(12) **United States Design Patent** (10) **Patent No.:** **US D768,724 S**
Akana et al. (45) **Date of Patent:** **** Oct. 11, 2016**

(54) **ELECTRONIC DEVICE**

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/496**

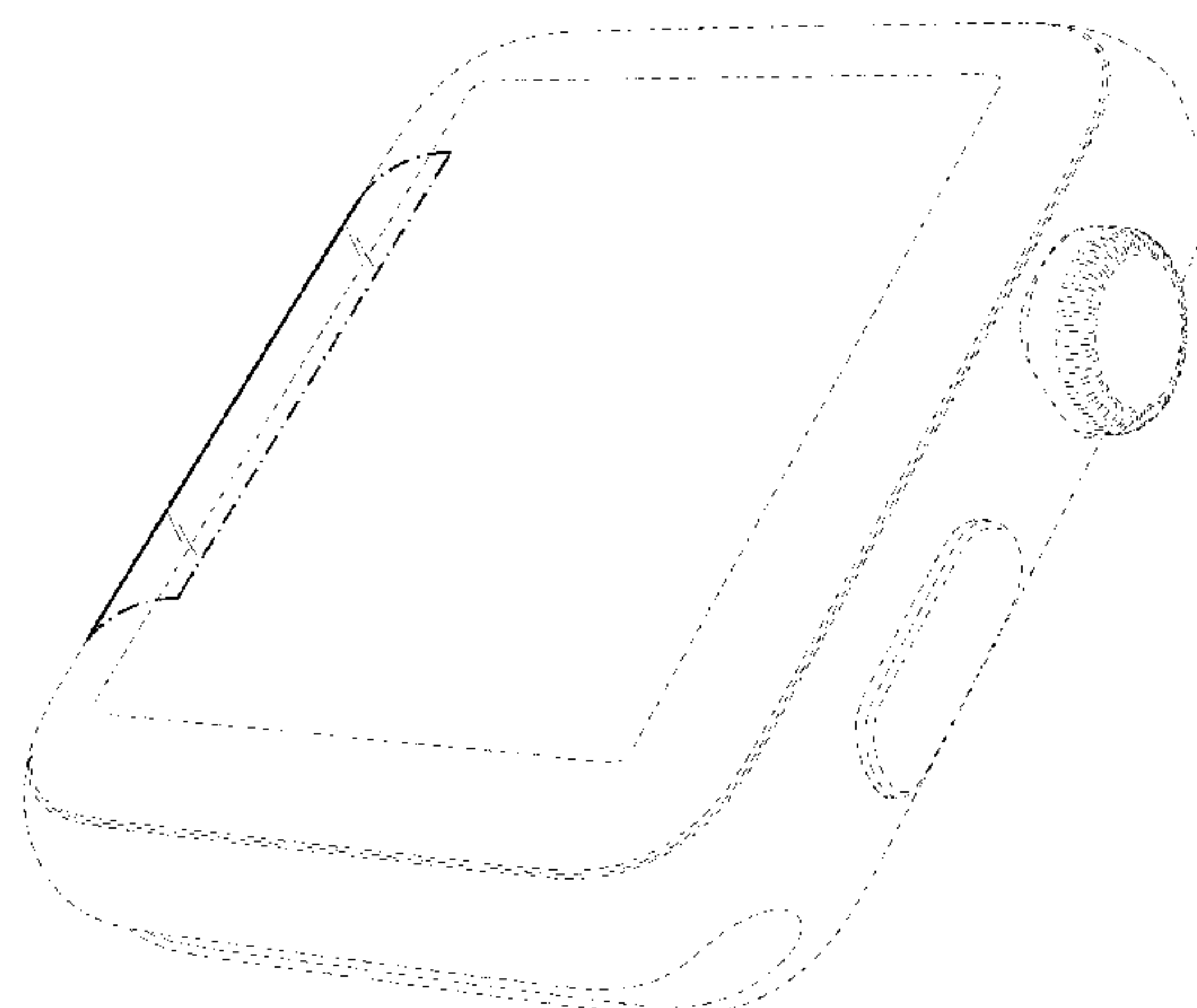
(58) **Field of Classification Search**

USPC D14/496, 401, 435, 474, 483, 217, 137, D14/138, 160, 168, 356, 203.1–203.8, 507; 345/156, 169, 173–179, 905; 715/727–729, 864; 710/1, 5, 8; 713/1, 713/600; 455/1.1, 1.7, 73, 344–347, 93, 95, 455/3.01–3.06, 550.1, 573.1; 370/342–344; 369/1, 2, 6–12; 463/43–47; 273/148 B
CPC A63F 9/24; A63F 13/00; G06F 9/24; G06F 12/00; G06F 19/00; H05K 5/00
See application file for complete search history.

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(57)

CLAIM

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a bottom front perspective view of an electronic device showing our new design;
FIG. 2 is a bottom rear perspective view thereof;
FIG. 3 is a front view thereof;
FIG. 4 is a rear view thereof;
FIG. 5 is a left side view thereof;
FIG. 6 is a right side view thereof;
FIG. 7 is a top view thereof;
FIG. 8 is a bottom view thereof; and,
FIG. 9 is a top front perspective reference view of the electronic device of the present invention showing our new design in an environment in which it may be used.
The dashed broken lines in the figures show portions of the electronic device and environment that form no part of the claimed design.
The dot-dash broken lines in the figures show boundaries that form no part of the claimed design.

1 Claim, 7 Drawing Sheets

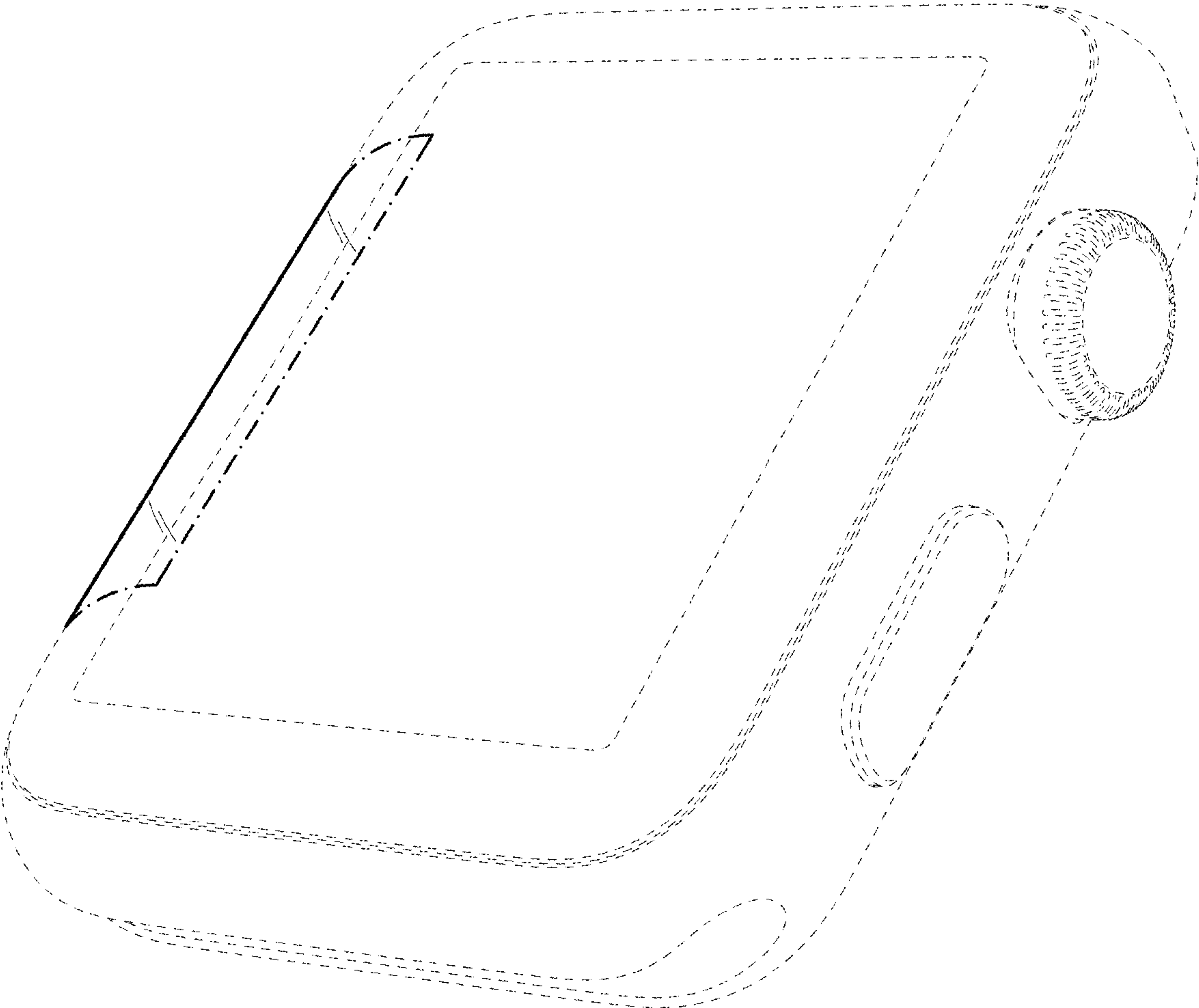


FIG. 1

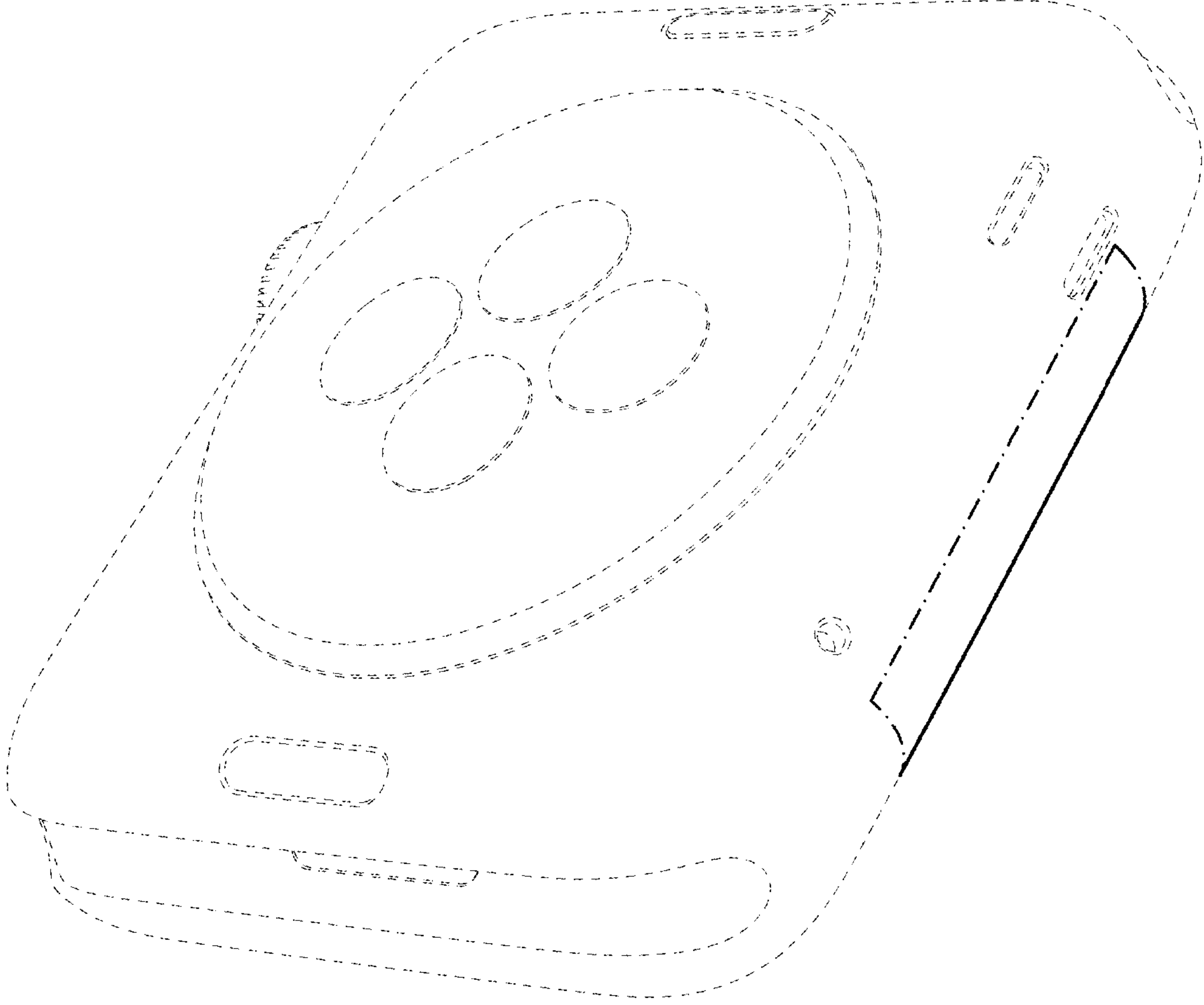


FIG. 2

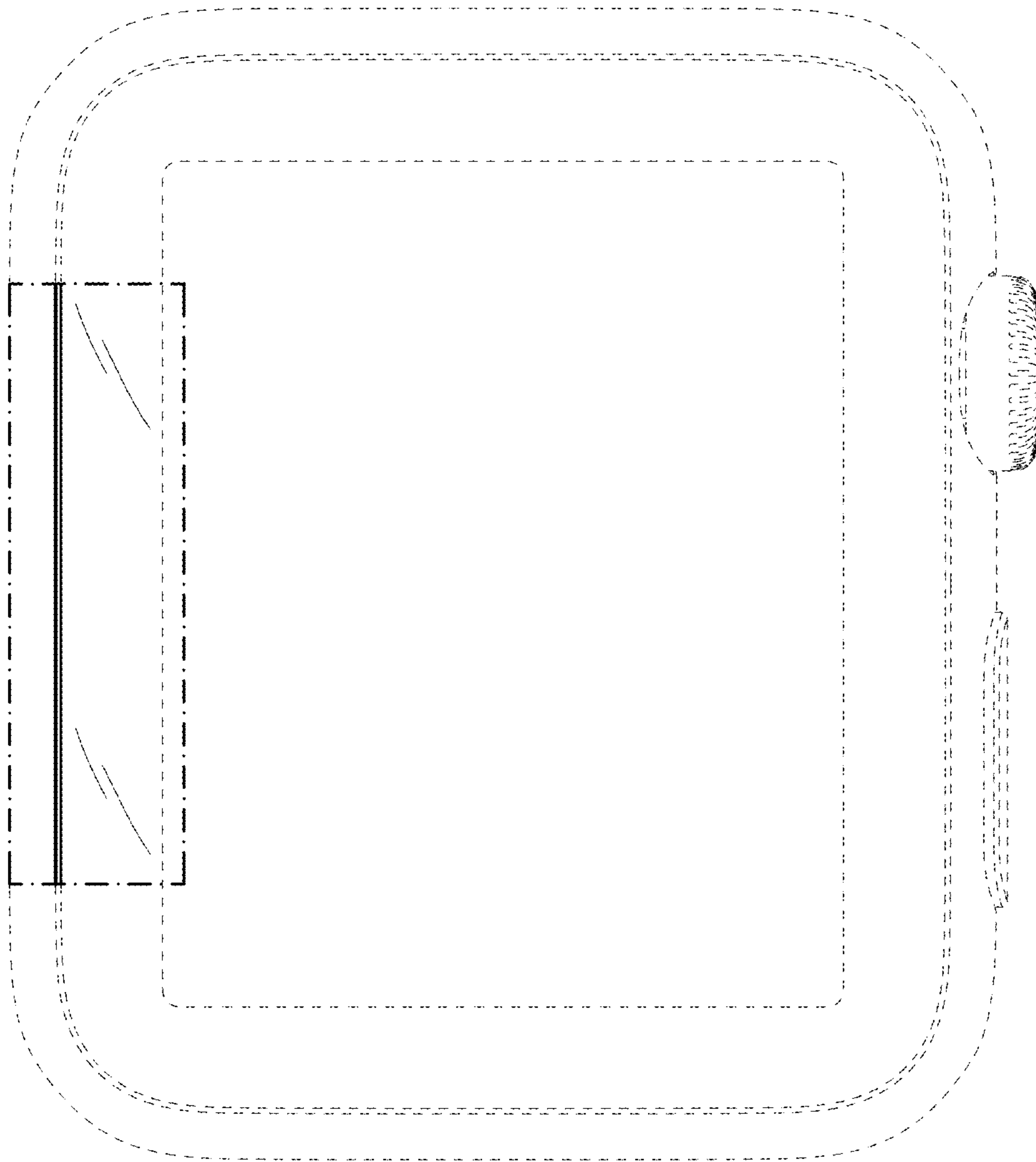


FIG. 3

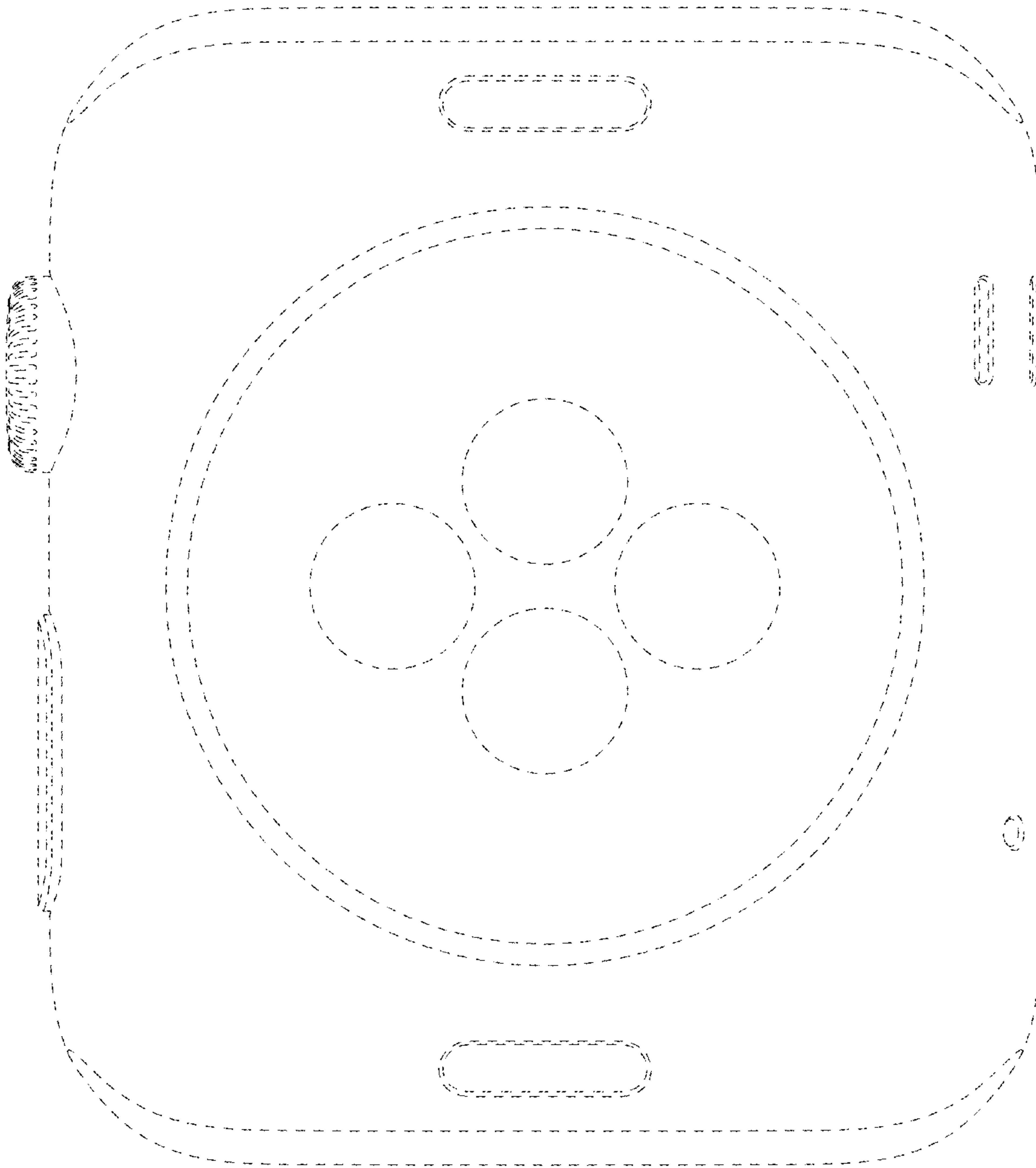


FIG. 4

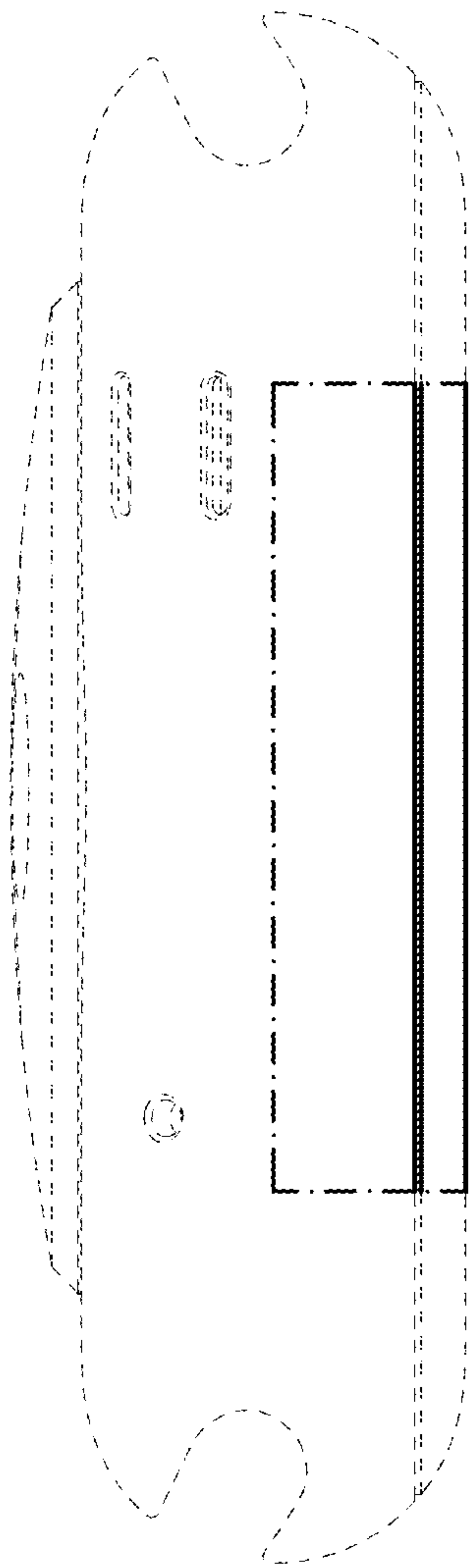


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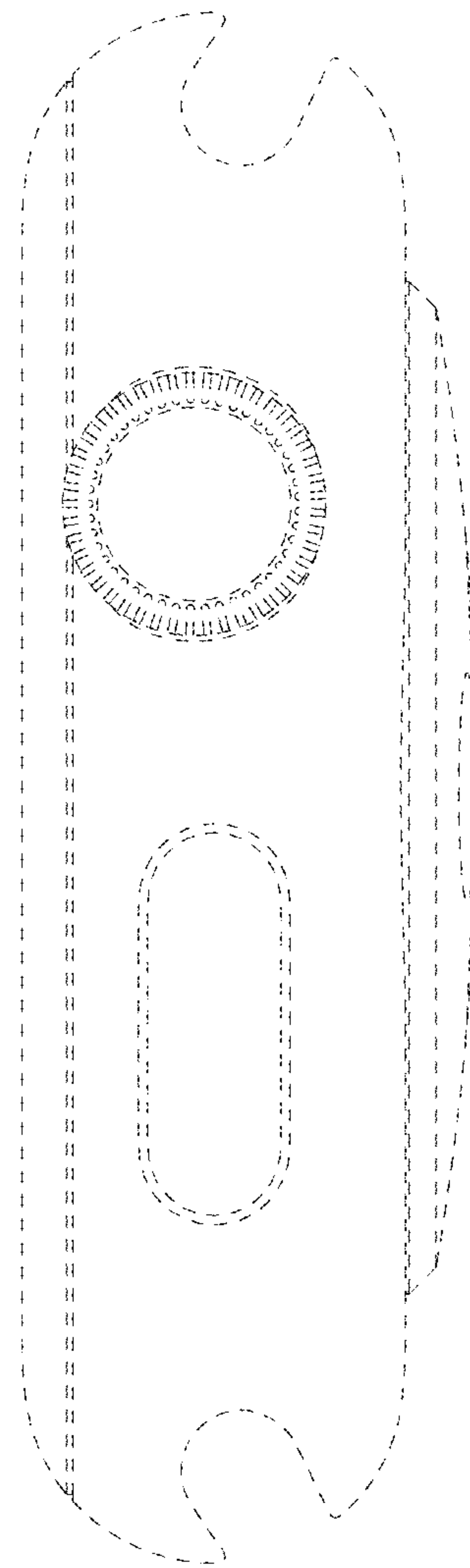


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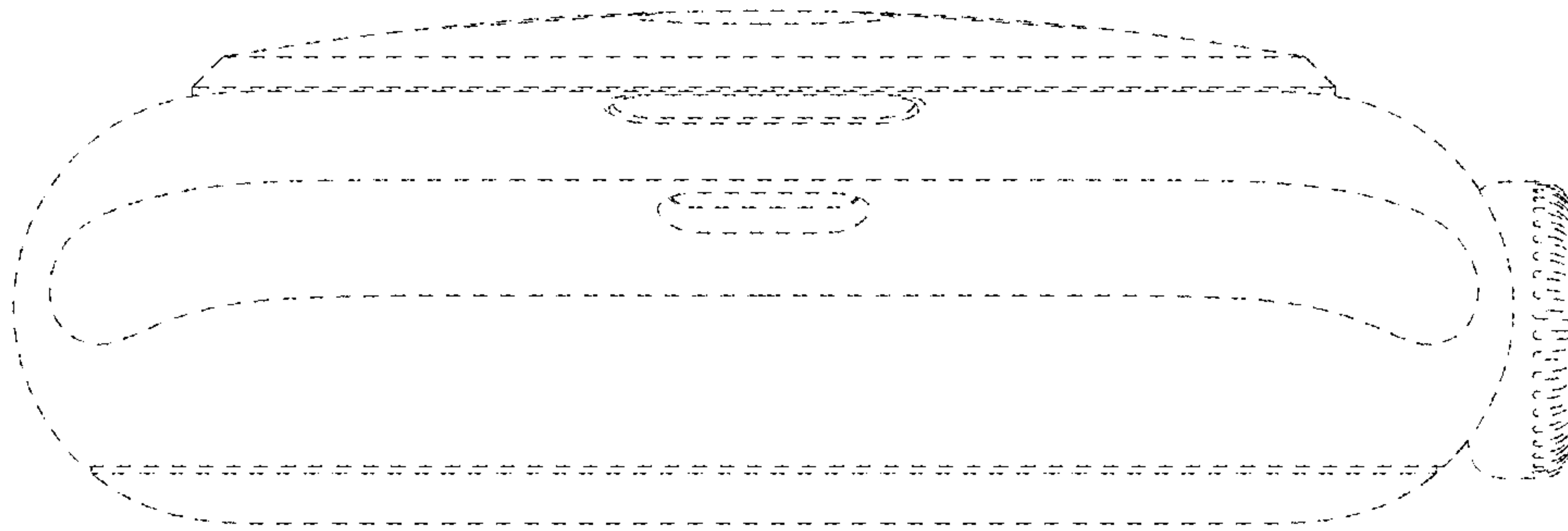


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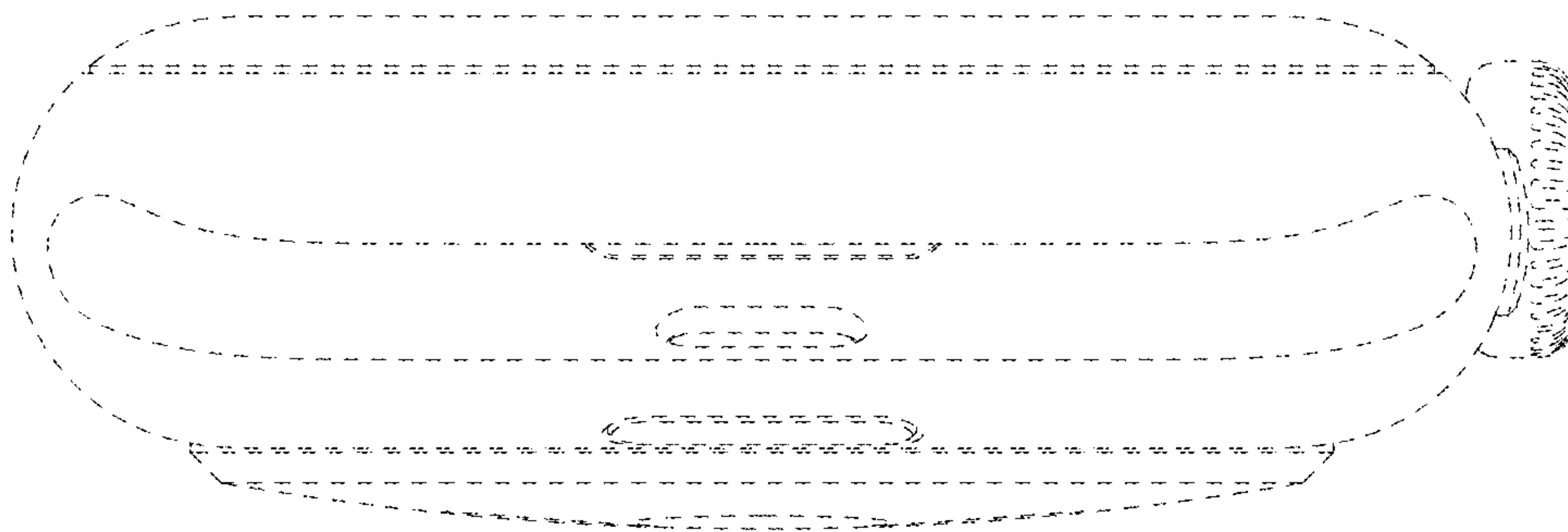


FIG. 8

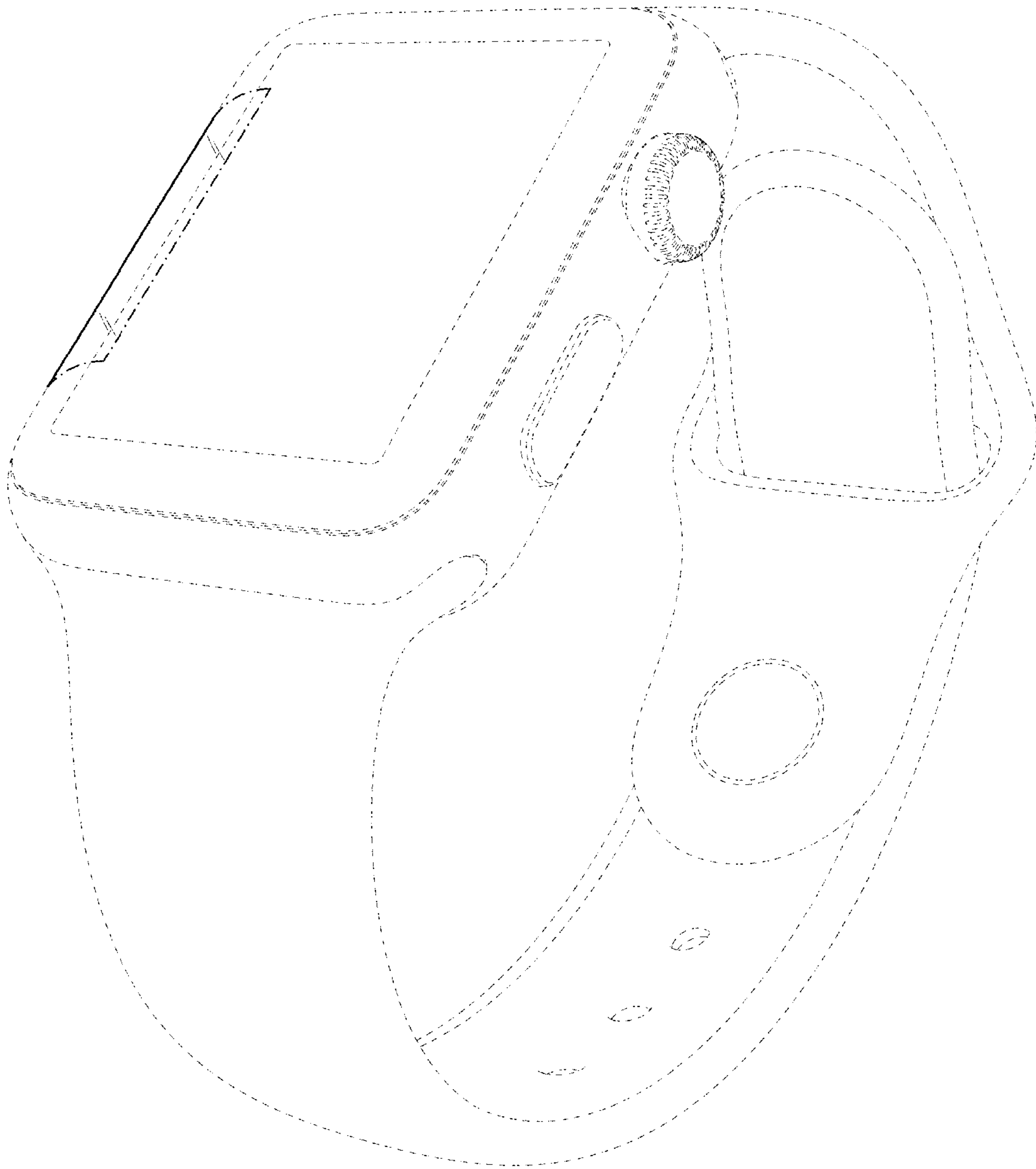


FIG. 9