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(12) **United States Design Patent** (10) **Patent No.:** **US D768,594 S**
Szeto (45) **Date of Patent:** **** Oct. 11, 2016**

(54) **ELECTRONIC DEVICE**

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(**) Term: **14 Years**

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(52) **U.S. Cl.**
USPC **D14/138 G**

(58) **Field of Classification Search**
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D14/247, 137, 341-347
CPC H04M 1/0202; H04M 1/0235; H04M
1/0237; H04M 1/0239; H04M 1/0266;
H04M 1/0268; H04M 1/027; H04M 1/0295
See application file for complete search history.

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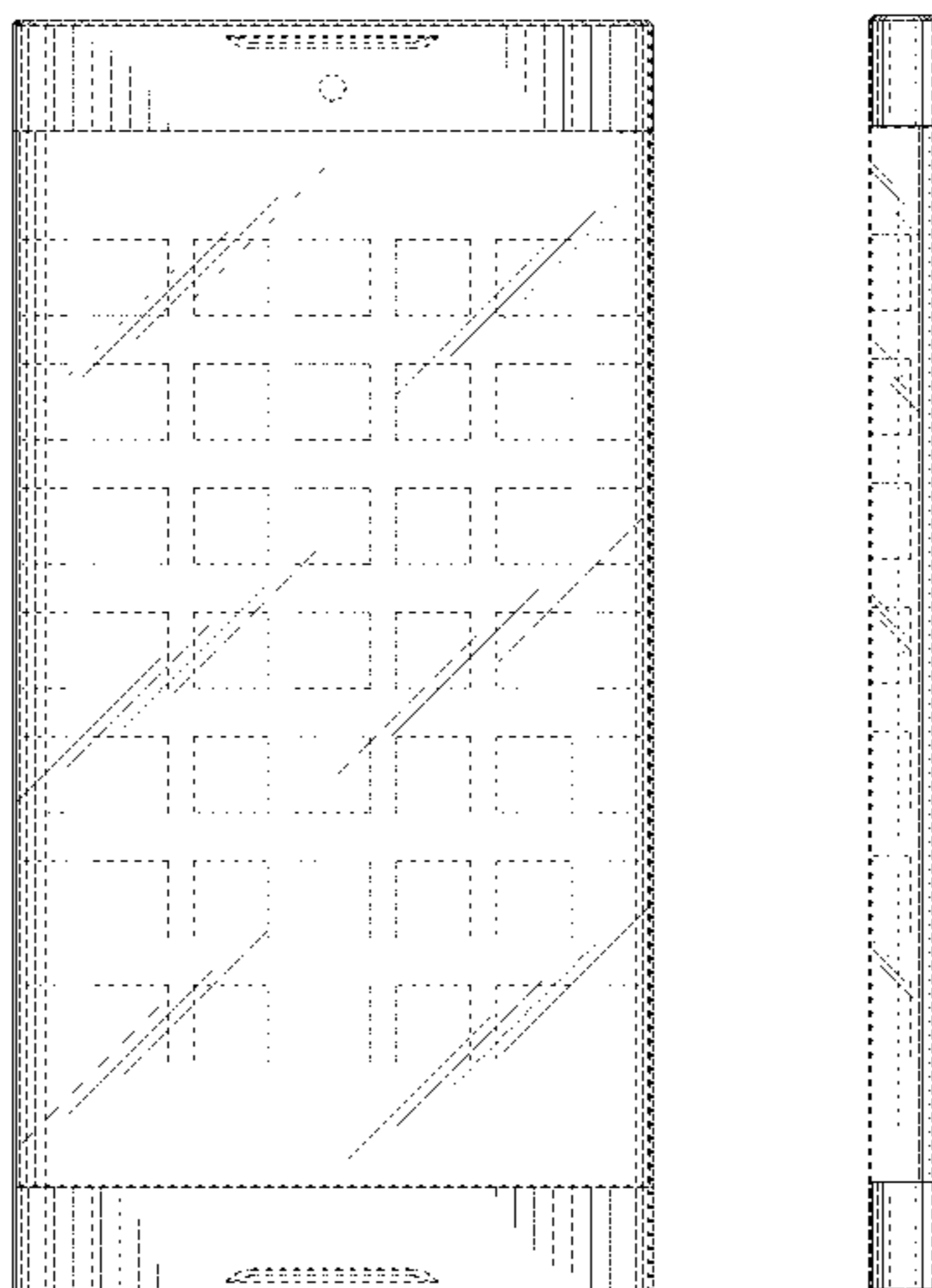
(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is top front perspective view of an electronic device showing my new design;
 FIG. 2 is a bottom rear perspective view thereof;
 FIG. 3 is front view thereof;
 FIG. 4 is a rear view thereof;
 FIG. 5 is a right side view thereof;
 FIG. 6 is a left side view thereof;
 FIG. 7 is a top view thereof;
 FIG. 8 is a bottom view thereof;
 FIG. 9 is a top front perspective view thereof showing on screen environment in a state of operation;
 FIG. 10 is a bottom rear perspective view thereof showing on screen environment in a state of operation;
 FIG. 11 is front view thereof showing on screen environment in a state of operation;
 FIG. 12 is a right side view thereof showing on screen environment in a state of operation; and,
 FIG. 13 is a left side view thereof showing on screen environment in a state of operation.
 The broken lines present both in FIGS. 1 to 8 and corresponding ones of FIGS. 9 to 13 represent the bounds of the claimed design. The additional broken lines in FIGS. 9 to 13 show on screen environment in a state of operation. The broken lines form no part of the claimed design.

1 Claim, 10 Drawing Sheets



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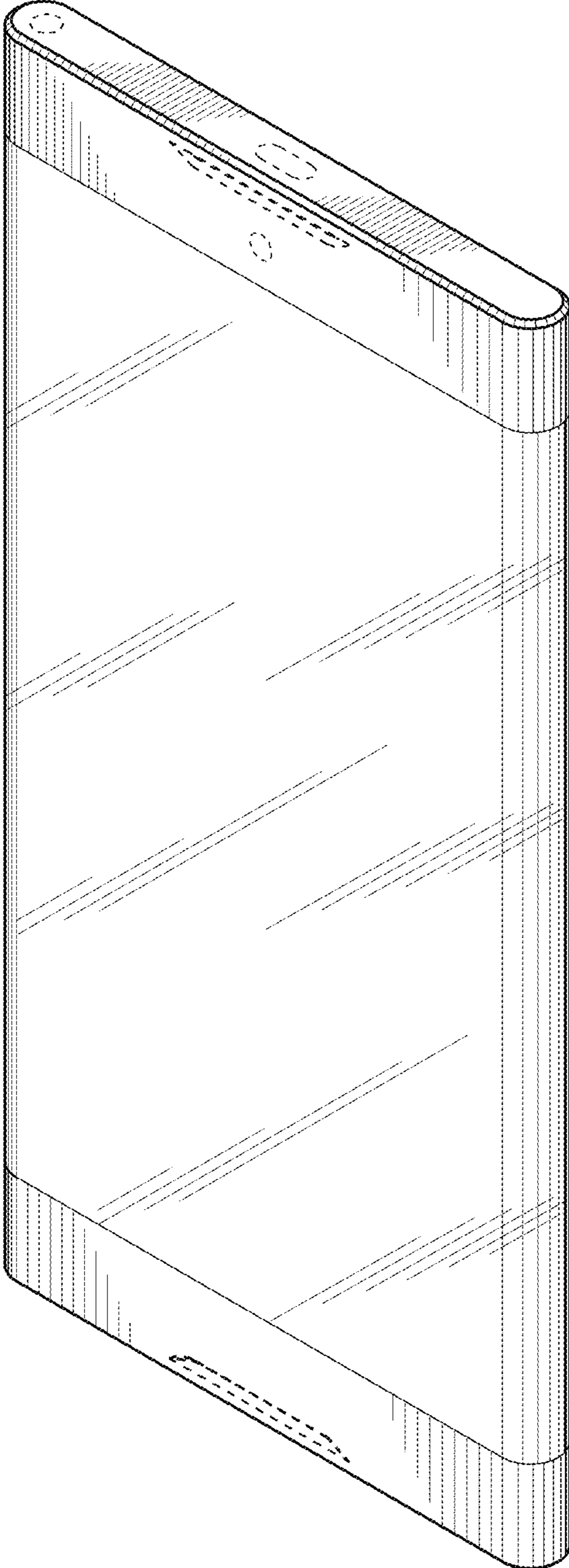


FIG.1

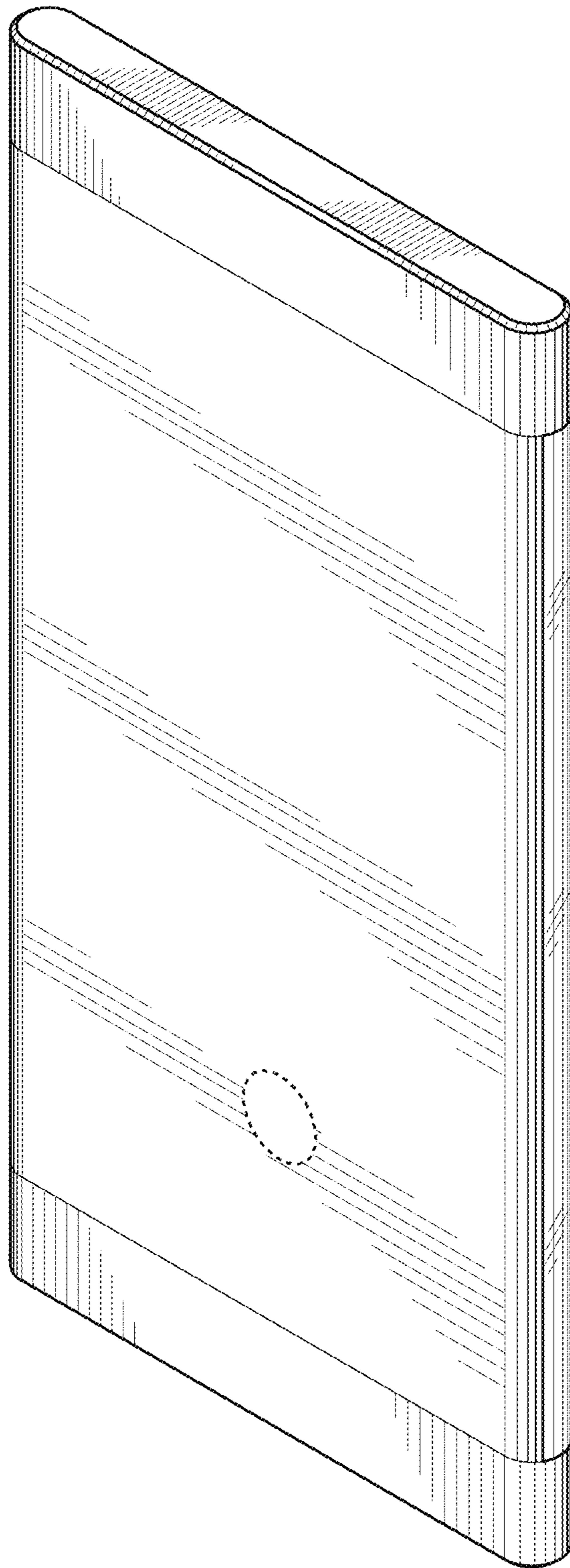


FIG.2

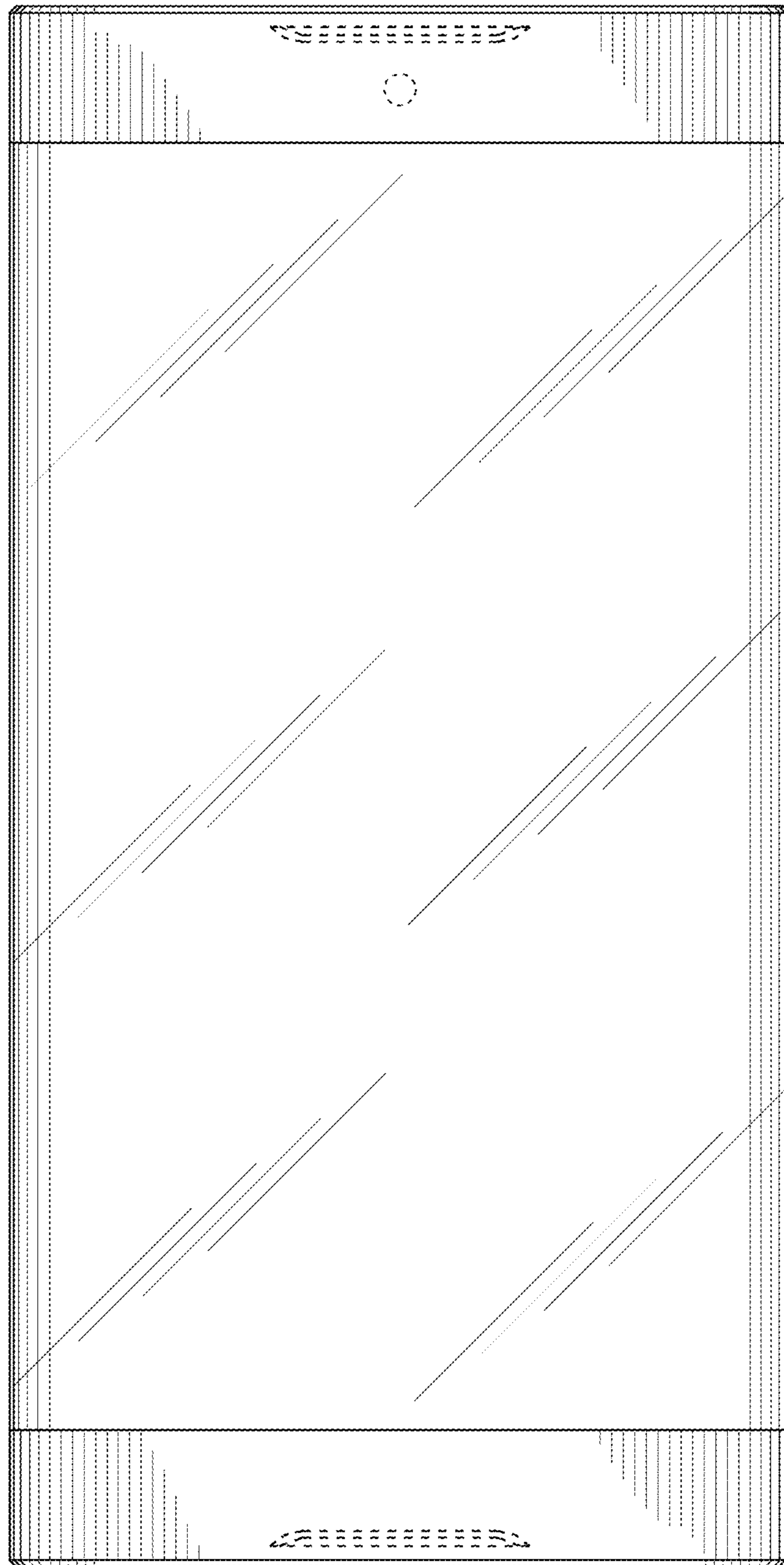


FIG.3

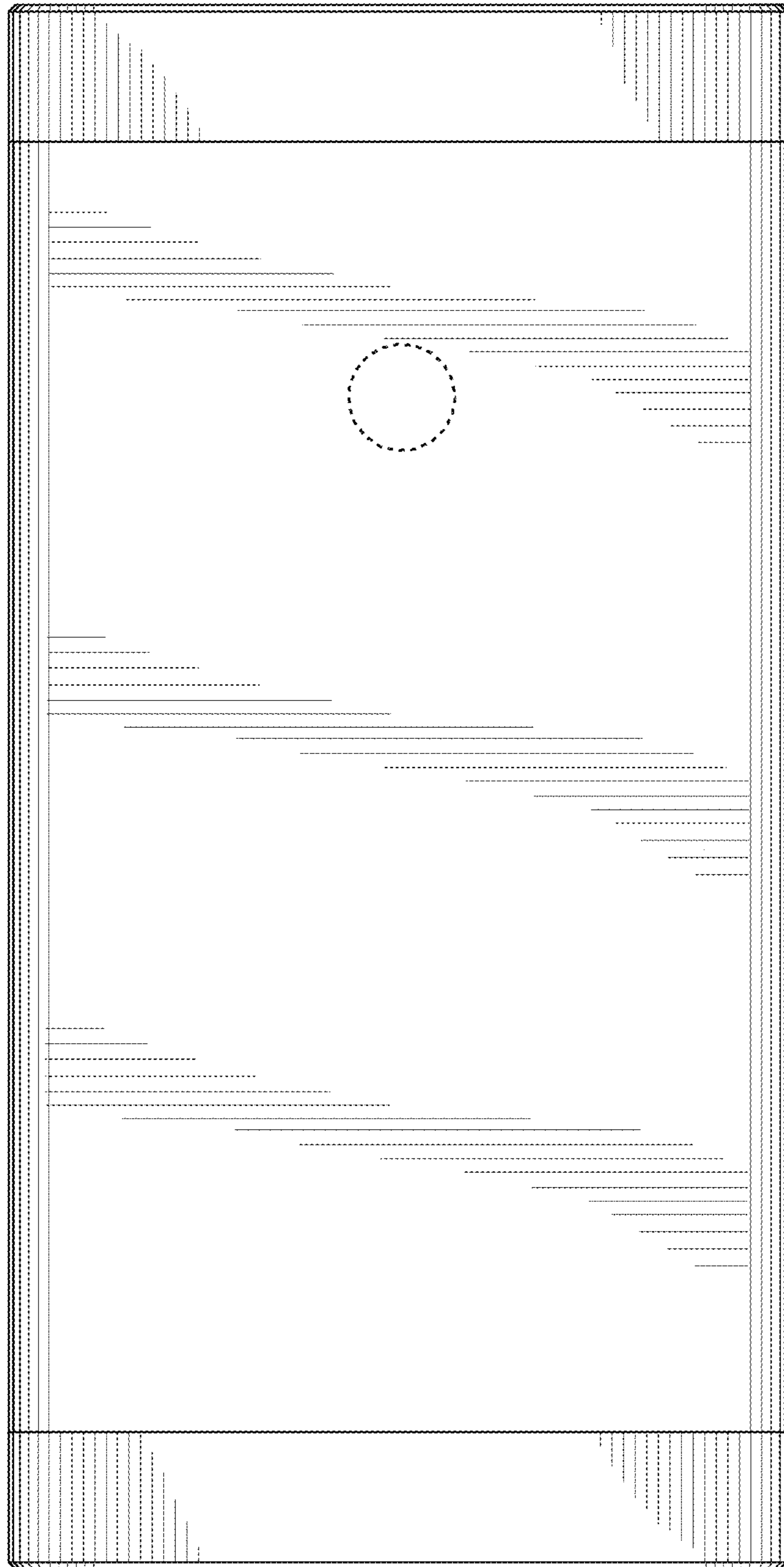


FIG. 4

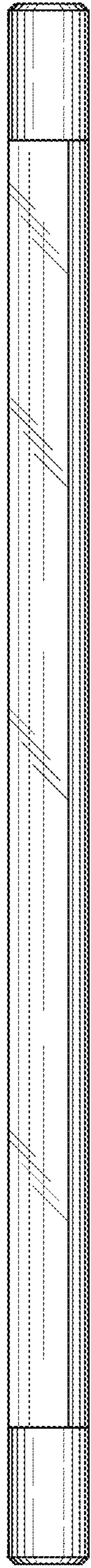


FIG. 5

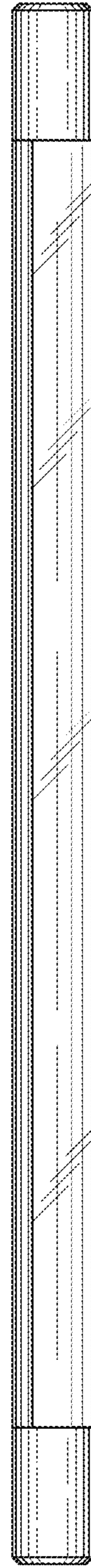


FIG. 6

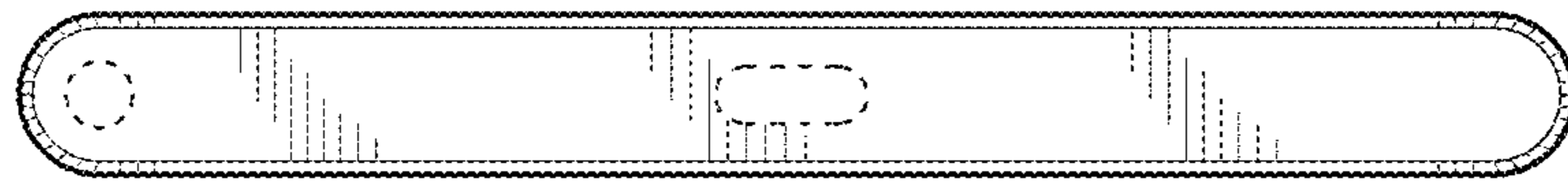


FIG. 7

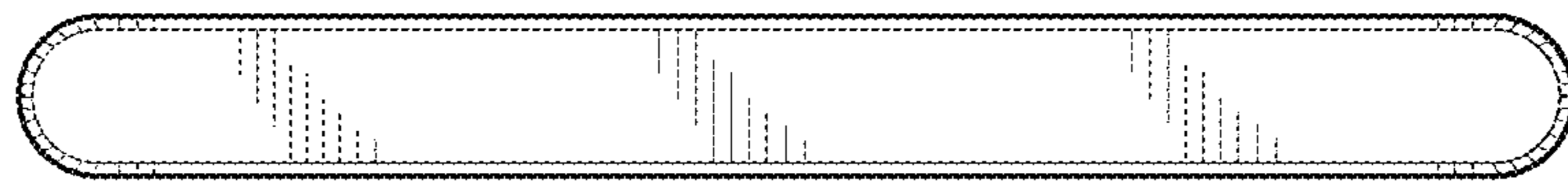


FIG. 8

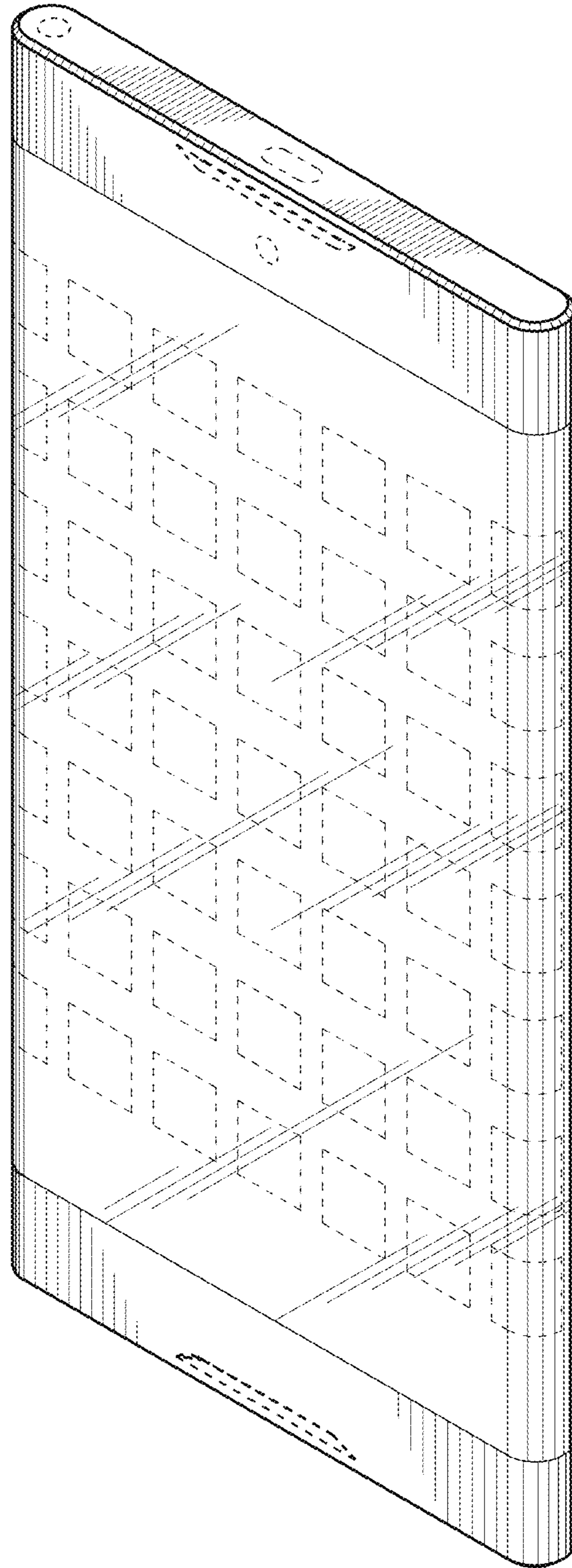


FIG. 9

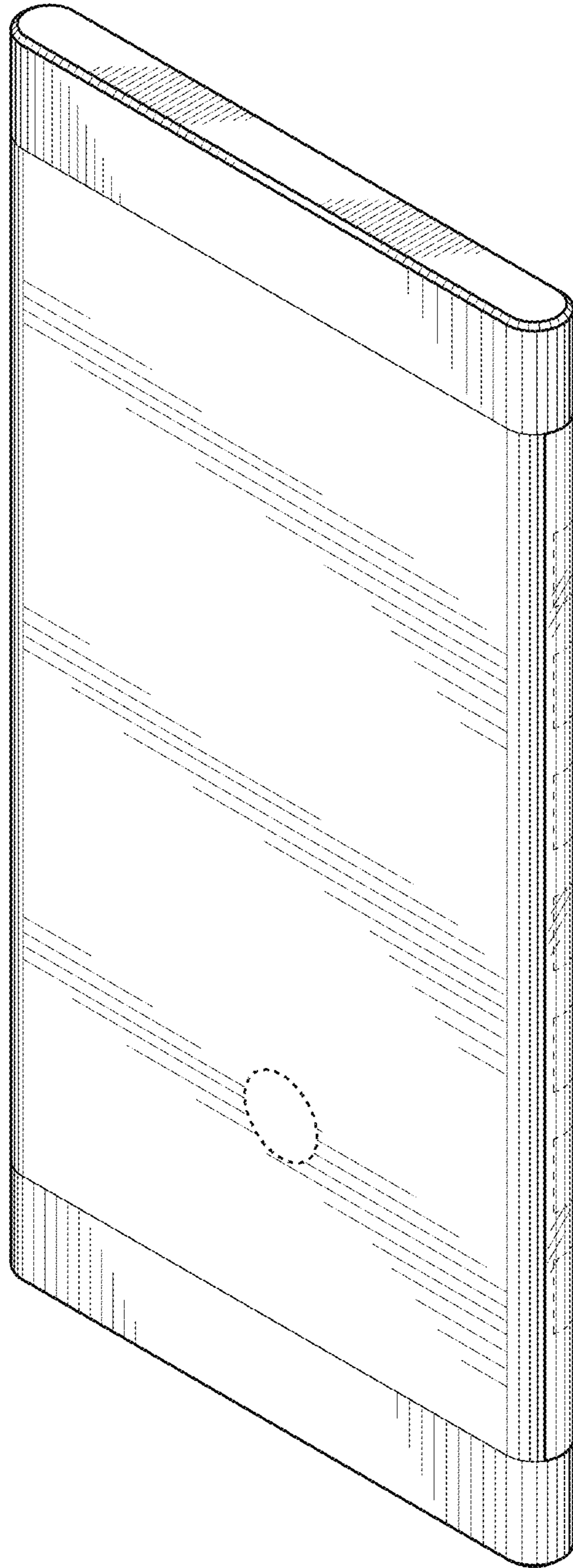


FIG.10

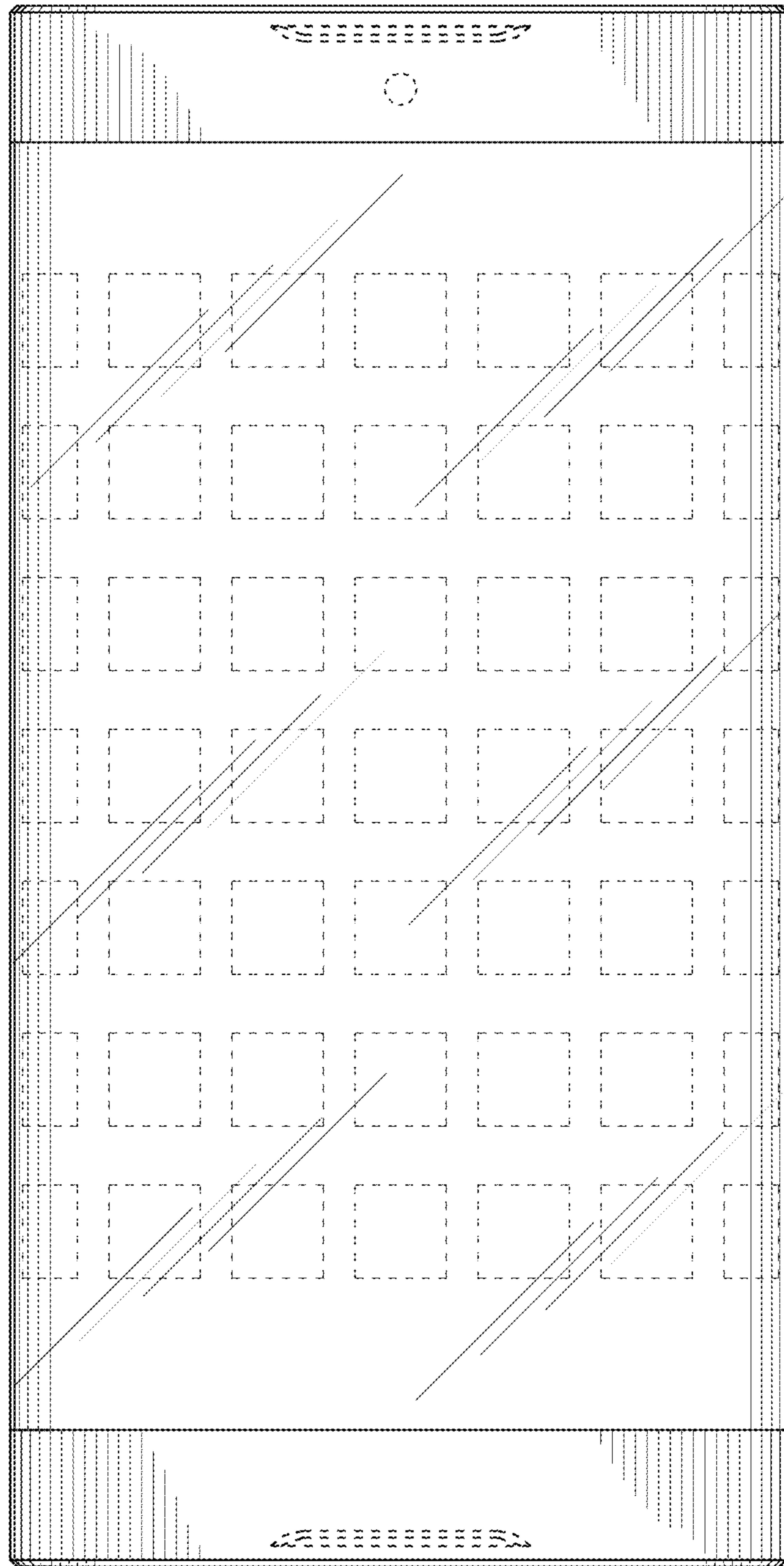


FIG. 11

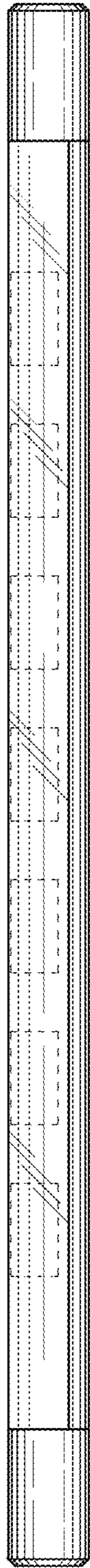


FIG. 12

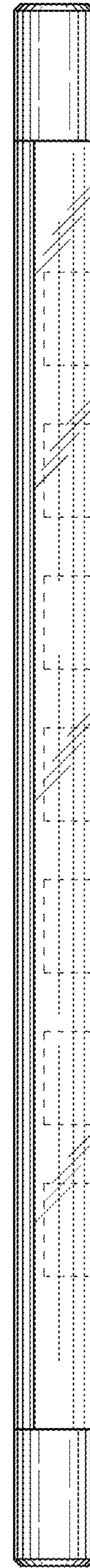


FIG. 13