



US00D767563S

(12) **United States Design Patent** (10) **Patent No.:** **US D767,563 S**
Satzger et al. (45) **Date of Patent:** **** Sep. 27, 2016**

(54) **ELECTRONIC TABLET DEVICE**
(71) Applicant: **Intel Corporation**, Santa Clara, CA (US)
(72) Inventors: **Douglas Satzger**, Santa Clara, CA (US); **David Collins**, San Francisco, CA (US); **Gadi Amit**, San Mateo, CA (US); **Yoshikazu Hoshino**, San Francisco, CA (US); **Maura Hoven**, San Francisco, CA (US); **Jinwoo Kim**, Buena Park, CA (US)

7,203,529 B2 * 4/2007 Kim H04N 1/2112
348/E7.079
D570,347 S * 6/2008 Kataoka D14/341
D604,716 S * 11/2009 Oh D14/203.3
D638,395 S * 5/2011 Finnegan D14/203.3
D649,526 S * 11/2011 Hu D14/203.7
D652,418 S * 1/2012 Rashid D14/341
D668,649 S * 10/2012 Burke D14/341
D678,271 S * 3/2013 Chiu D14/341
D714,287 S * 9/2014 Takahashi D14/341
D732,014 S * 6/2015 Miyake D14/258
D735,717 S * 8/2015 Lam D14/341

* cited by examiner

(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

Primary Examiner — Prabhakar Deshmukh

(74) *Attorney, Agent, or Firm* — Patent Capital Group

(**) Term: **14 Years**

(21) Appl. No.: **29/501,764**

(57) **CLAIM**

The ornamental design for an electronic tablet device, as shown and described.

(22) Filed: **Sep. 8, 2014**

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/341**

DESCRIPTION

(58) **Field of Classification Search**
USPC D14/496, 203.3, 203, 7, 341–347,
D14/138 R, 138 AA, 138 C, 138 G, 218, 248;
273/148 B; 463/1, 29–35, 46, 47;
345/156, 901, 905; 379/433.04,
379/433.01, 916; 361/679.55, 679.56;
455/556.1, 556.2, 566, 575.1, 90.3
CPC H04M 1/00; H04M 11/066; H04M
2201/38; G06F 15/00; A63F 13/00; A63F
13/09

FIG. 1 is a front perspective view of an electronic tablet device showing our new design;
FIG. 2 is a front view thereof;
FIG. 3 is a rear view thereof;
FIG. 4 is a side view thereof;
FIG. 5 is another side view thereof;
FIG. 6 is a top view thereof;
FIG. 7 is a bottom view thereof; and,
FIG. 8 is an enlarged perspective view of the camera configuration shown in FIG. 3.

See application file for complete search history.

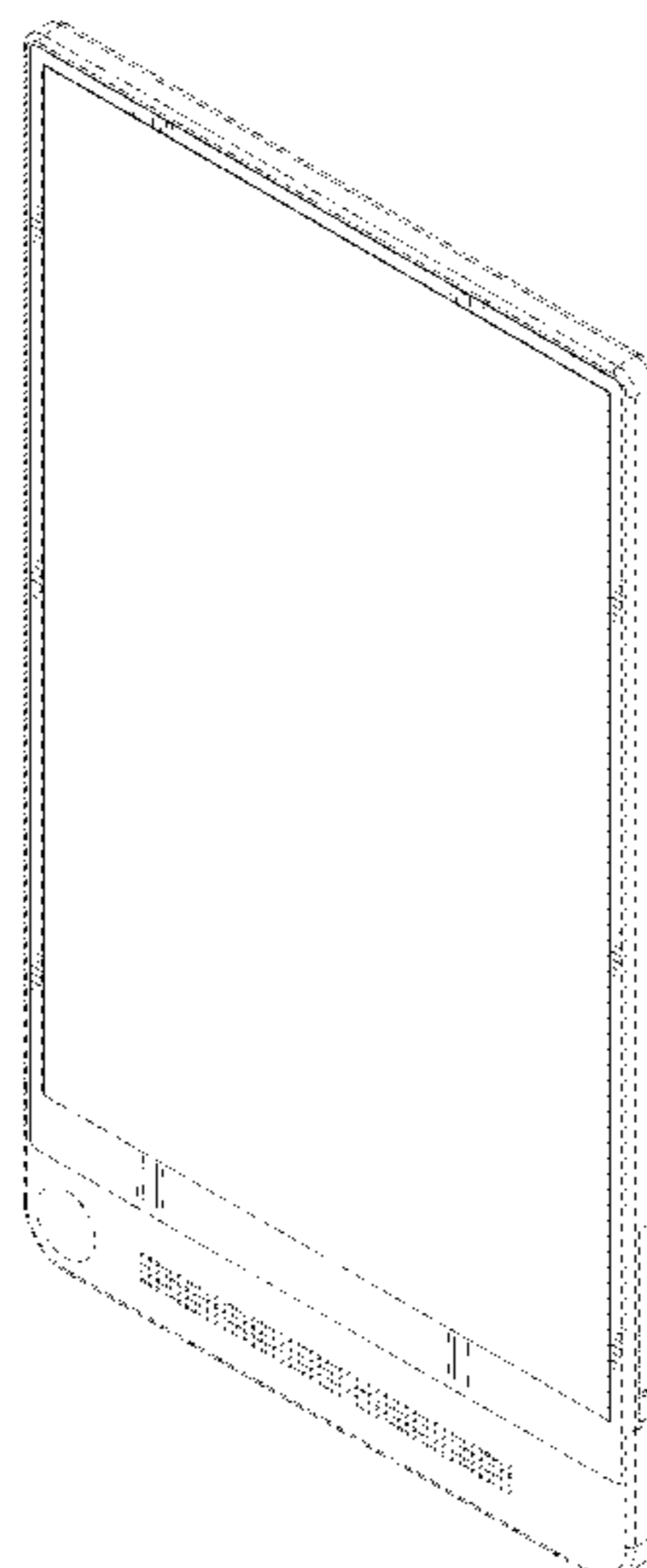
The broken lines immediately adjacent to the shaded areas represent the boundaries of the claimed design. None of the broken lines form part of the claimed design.

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,028,765 A * 2/2000 Swindler B60R 11/0252
174/138 G
D529,045 S * 9/2006 Shin D14/203.7

1 Claim, 6 Drawing Sheets



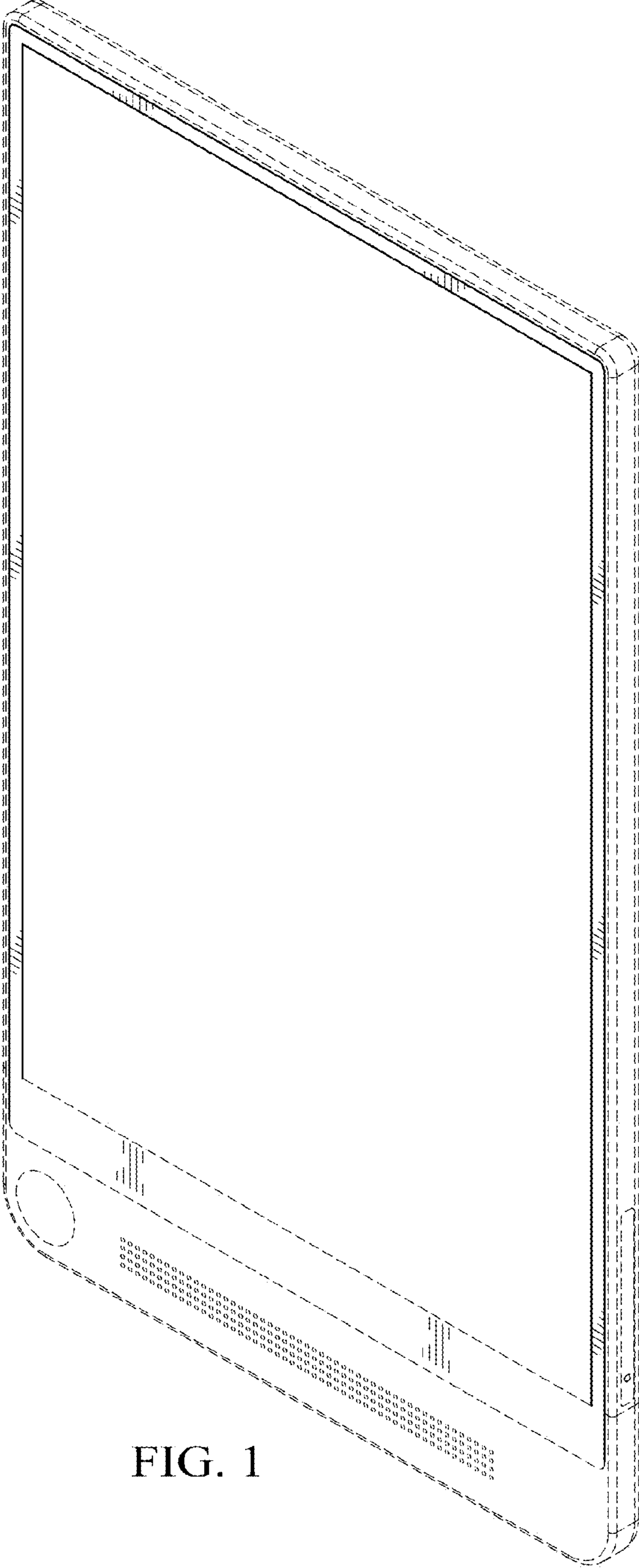


FIG. 1

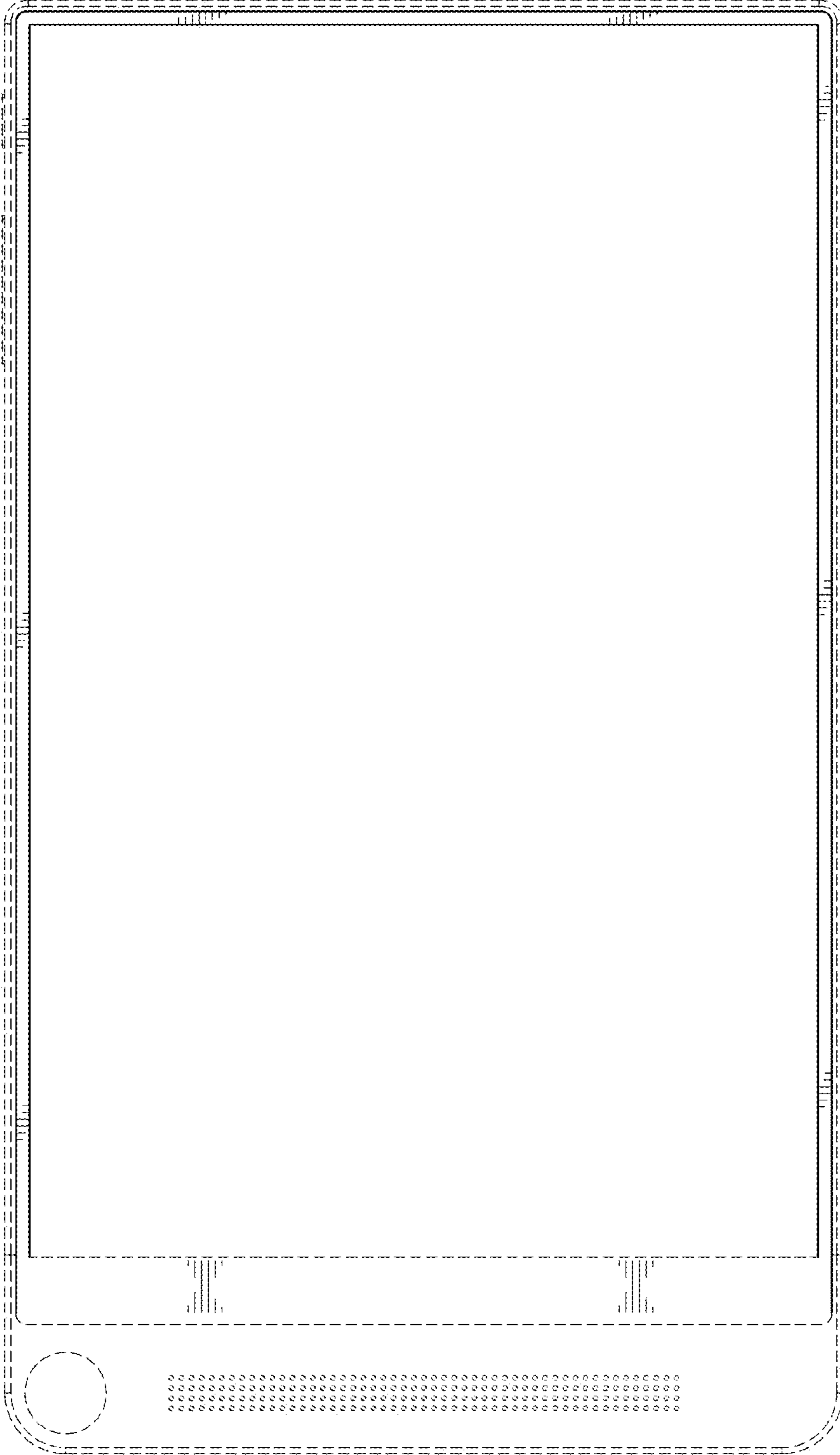


FIG. 2

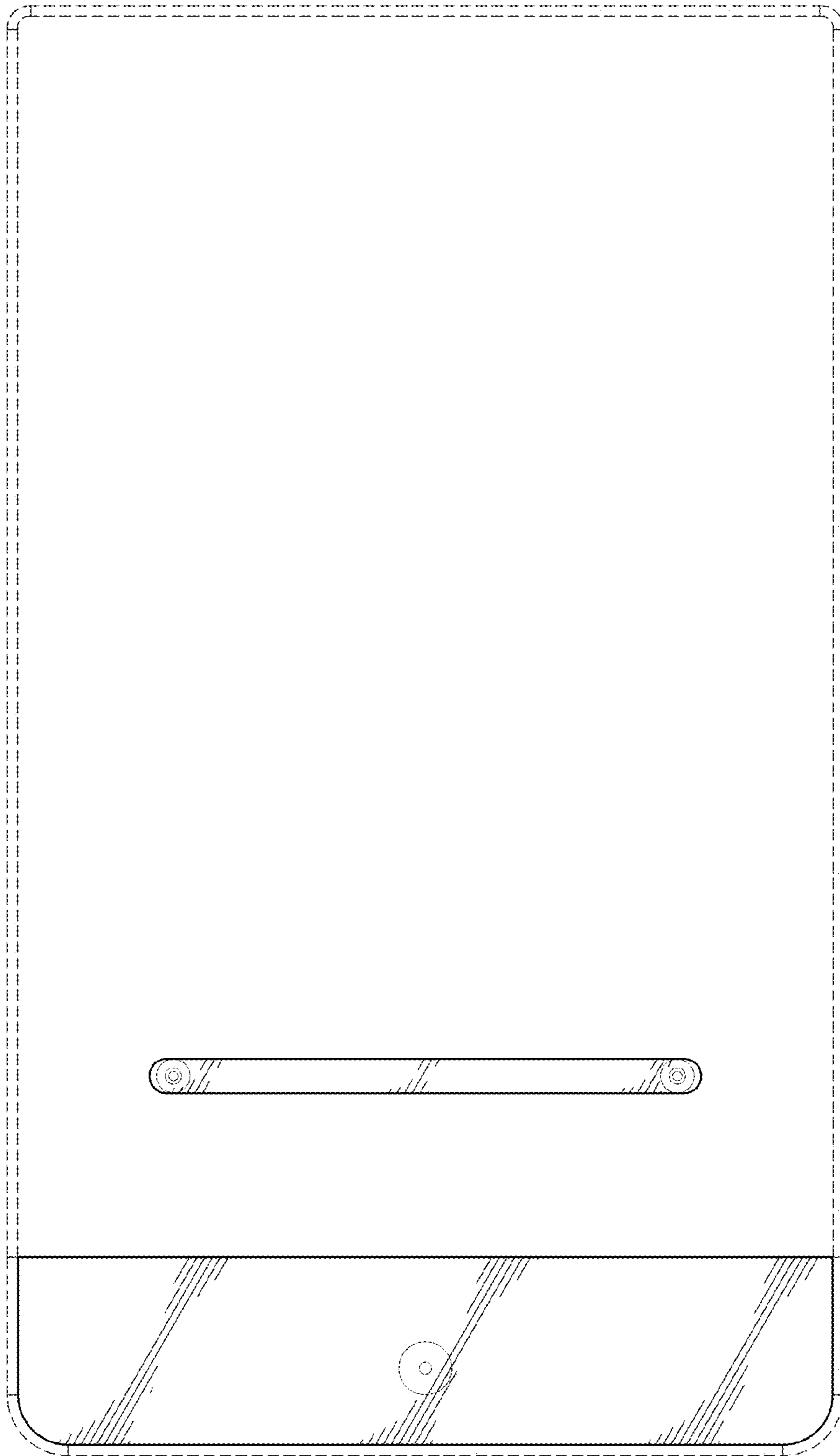


FIG. 3

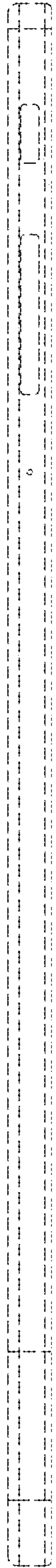


FIG. 4

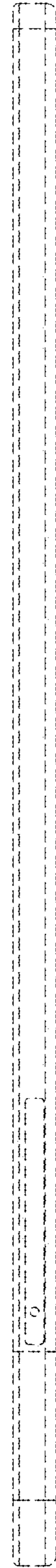


FIG. 5

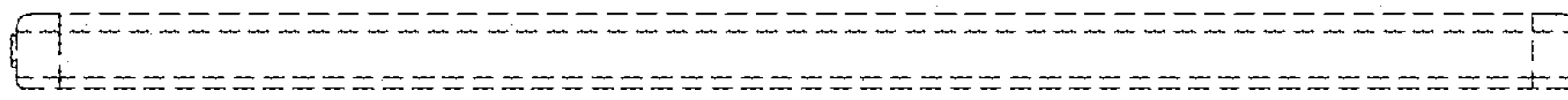


FIG. 6



FIG. 7

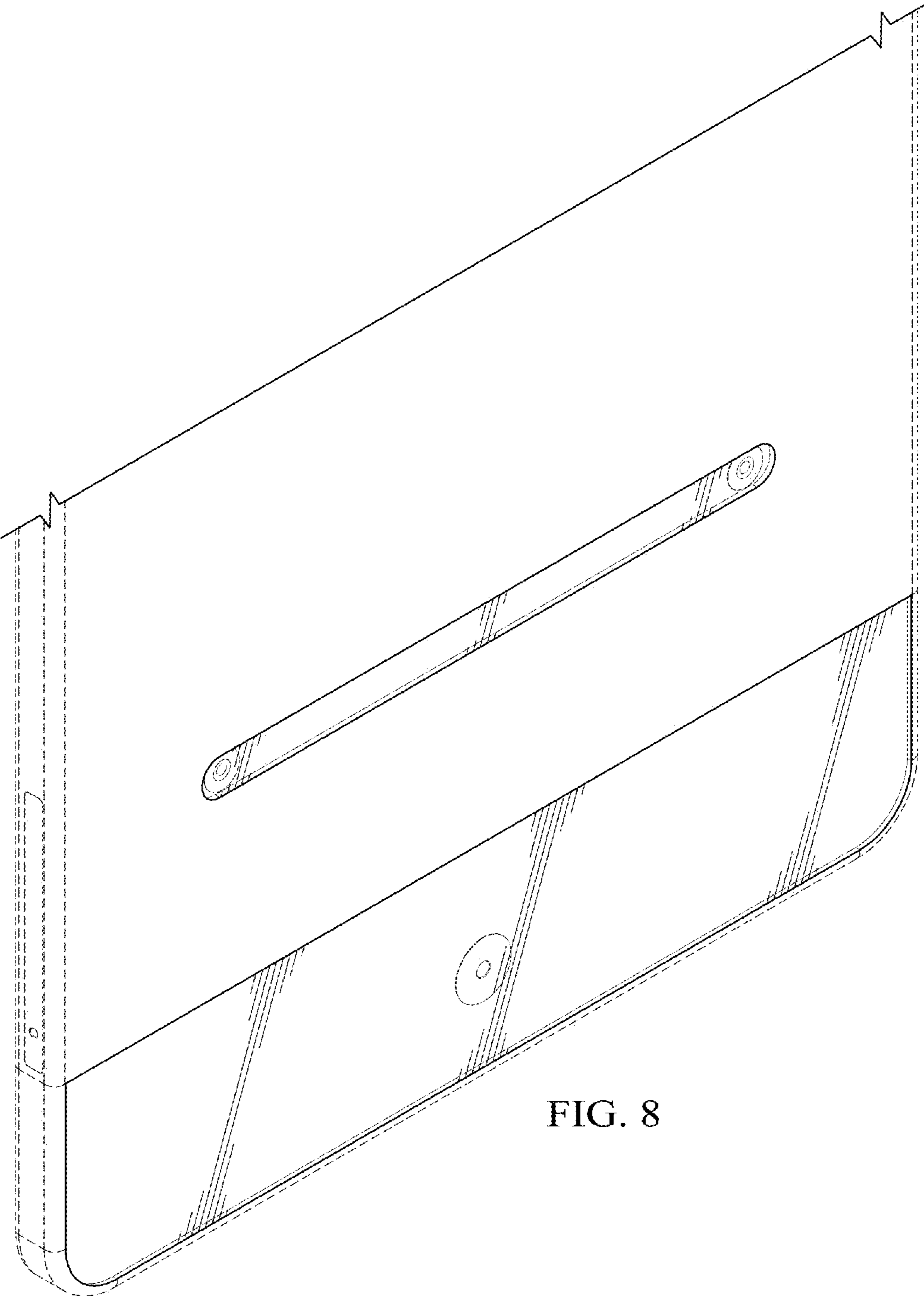


FIG. 8