



US00D766233S

(12) **United States Design Patent** (10) **Patent No.:** **US D766,233 S**
Lam et al. (45) **Date of Patent:** **** Sep. 13, 2016**

(54) **ELECTRONIC DEVICE**

(56) **References Cited**

(71) Applicant: **Intel Corporation**, Santa Clara, CA (US)

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(72) Inventors: **Hue Lam**, Portland, OR (US); **George Daskalakis**, Forest Grove, OR (US); **Kimi Jensen**, Beaverton, OR (US); **Ky Nguyen**, Portland, OR (US)

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(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/535,050**

(22) Filed: **Aug. 3, 2015**

Related U.S. Application Data

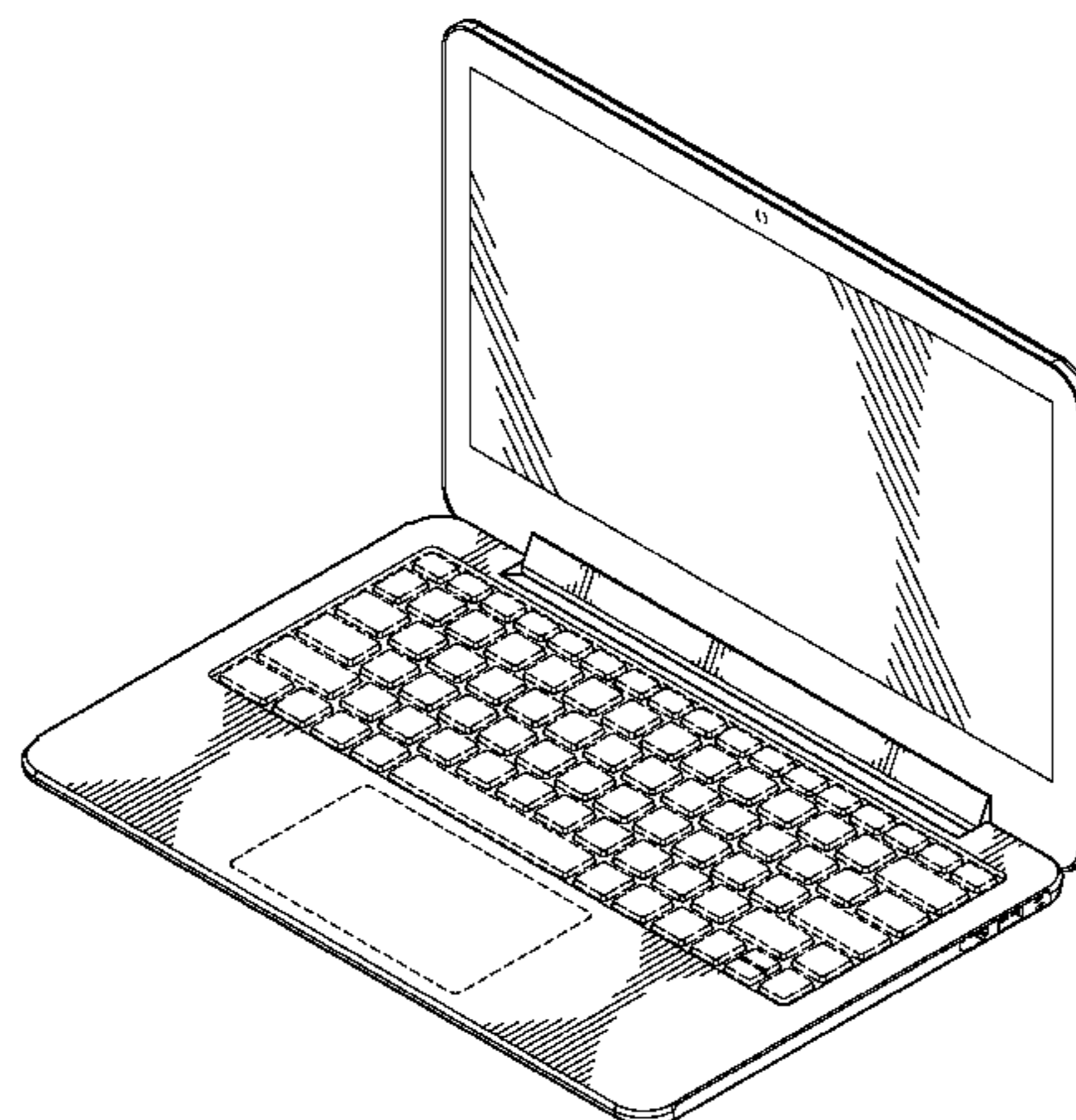
(63) Continuation of application No. 29/441,013, filed on Dec. 29, 2012, now Pat. No. Des. 735,717.

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/315**

(58) **Field of Classification Search**
USPC D14/315-327; D18/1, 2, 7, 11;
235/145 A, 145 R; 341/22, 23; 345/104,
345/156, 168, 169, 173; 361/679.08,
361/679.09, 679.11, 679.26, 679.27
CPC G06F 1/16; G06F 1/1616; G06F 1/1626;
G06F 1/1632; G06F 1/1613; G06F 1/1601;
G06F 1/162; G06F 1/1654; G06F 1/1681;
G06F 1/1618; H01R 35/02; H03M 11/00;
G09B 13/04; H03K 17/94; G09G 5/00;
F16M 11/10; H05K 5/00; H04M 1/0237
See application file for complete search history.

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(57) **CLAIM**
 The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic device shown in an opened position;
 FIG. 2 is a front view thereof;
 FIG. 3 is a back view thereof;
 FIG. 4 is a side view thereof;
 FIG. 5 is another side view thereof;
 FIG. 6 is a top view thereof;
 FIG. 7 is a bottom view thereof;
 FIG. 8 is a perspective view thereof wherein the hinge is shown in an alternate position and the display panel is shown detached from the base and inverted allowing the device to be used as a tablet;
 FIG. 9 is a front view thereof;
 FIG. 10 is a back view thereof;
 FIG. 11 is a side view thereof;
 FIG. 12 is another side view thereof;
 FIG. 13 is a top view thereof;
 FIG. 14 is a bottom view thereof;
 FIG. 15 is a perspective view thereof in a closed position;
 FIG. 16 is a front view thereof;
 FIG. 17 is a back view thereof;
 FIG. 18 is a side view thereof;
 FIG. 19 is another side view thereof;
 FIG. 20 is a top view thereof;
 FIG. 21 is a bottom view thereof;
 FIG. 22 is a perspective view of the display panel thereof shown separately for clarity of illustration only;
 FIG. 23 is a front view thereof;
 FIG. 24 is a back view thereof;
 FIG. 25 is a side view thereof;
 FIG. 26 is another side view thereof;
 FIG. 27 is a top view thereof;
 FIG. 28 is a bottom view thereof;
 FIG. 29 is another perspective view thereof;
 FIG. 30 is a perspective view of the base thereof shown separately for clarity of illustration only;
 FIG. 31 is a front view thereof;
 FIG. 32 is a back view thereof;
 FIG. 33 is a side view thereof;
 FIG. 34 is another side view thereof;
 FIG. 35 is a top view thereof; and,
 FIG. 36 is a bottom view thereof.
 The broken lines shown in the figures represent unclaimed portions of the electronic device and form no part of the claimed design.

1 Claim, 26 Drawing Sheets

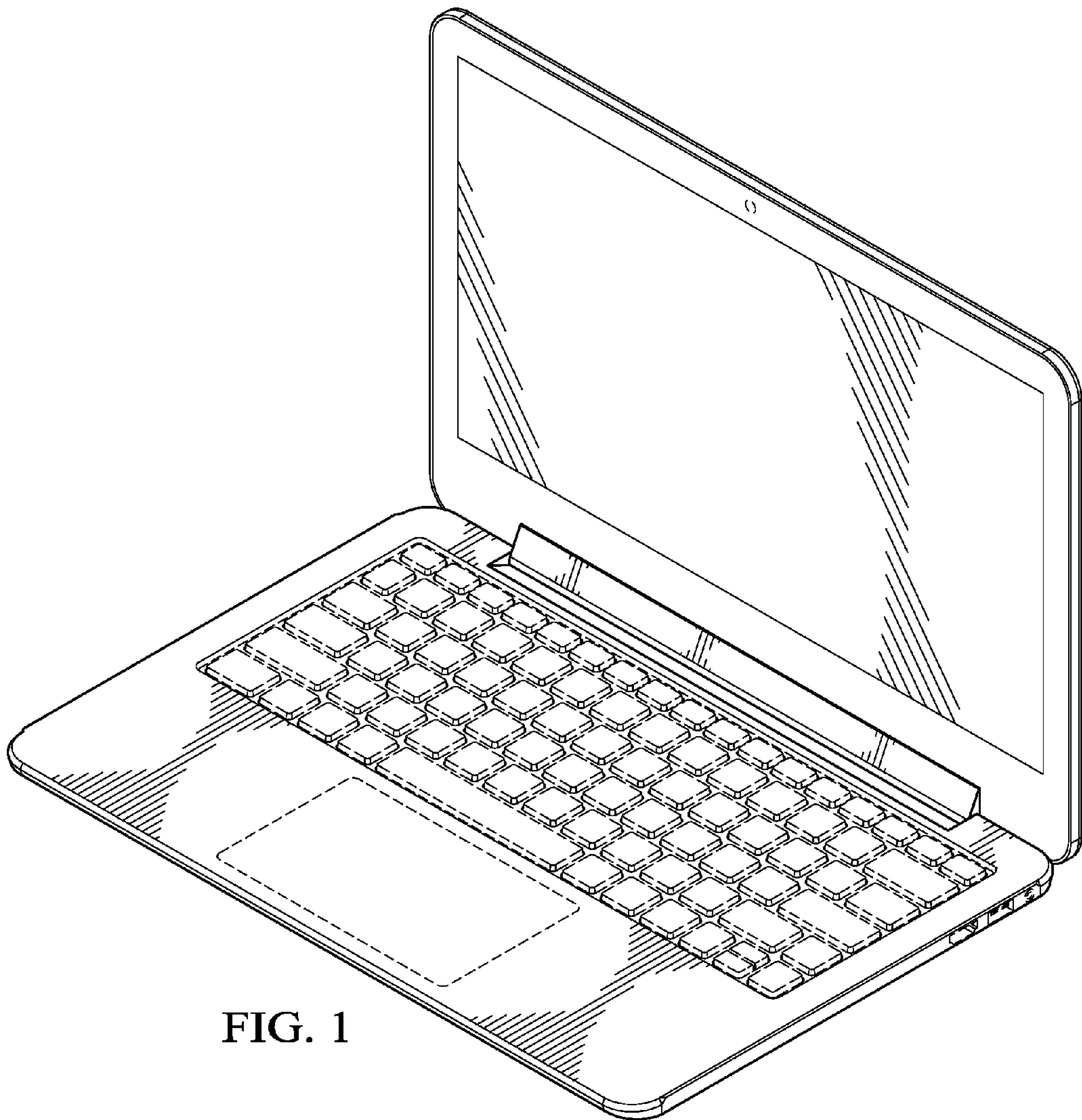


FIG. 1

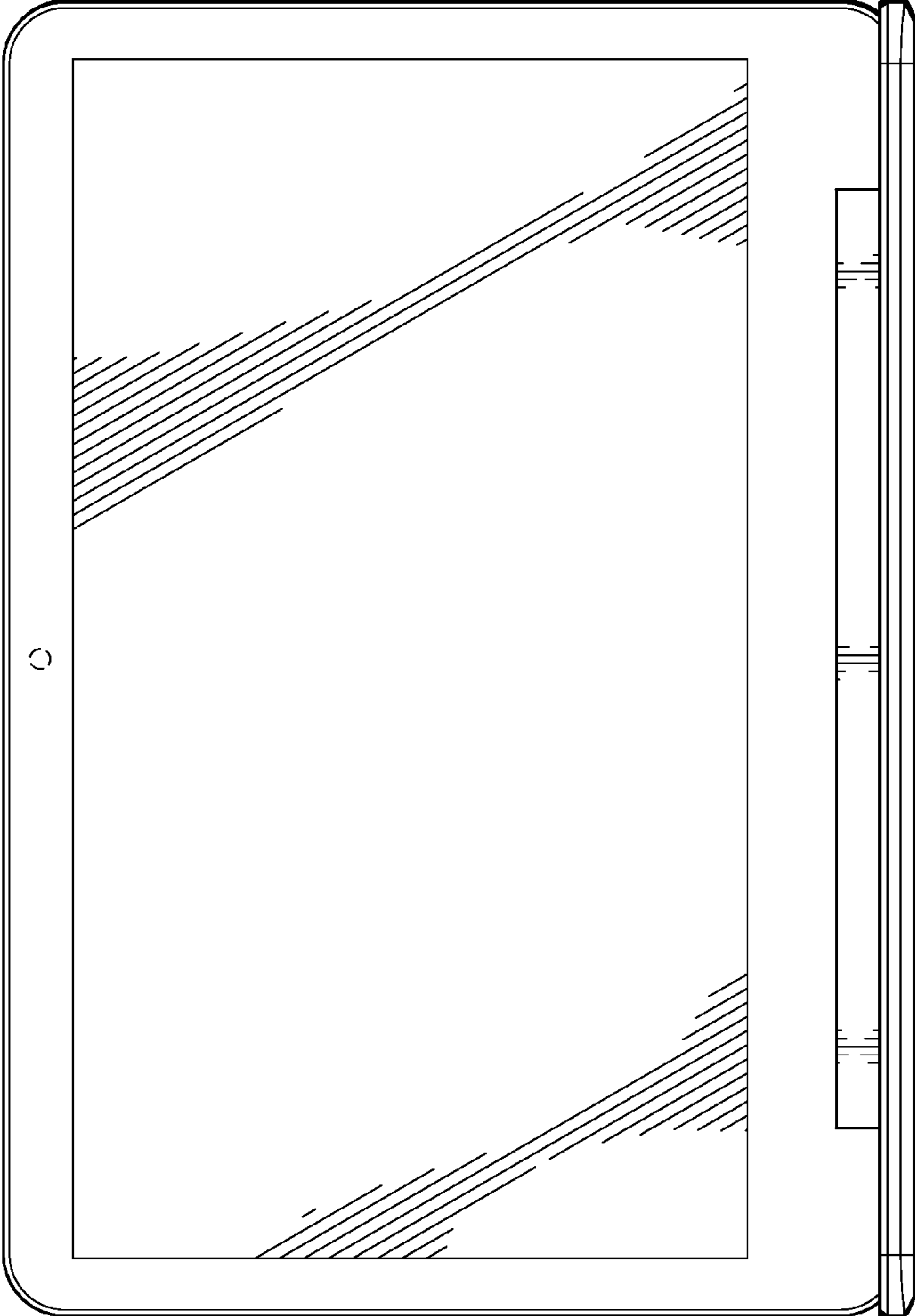


FIG. 2

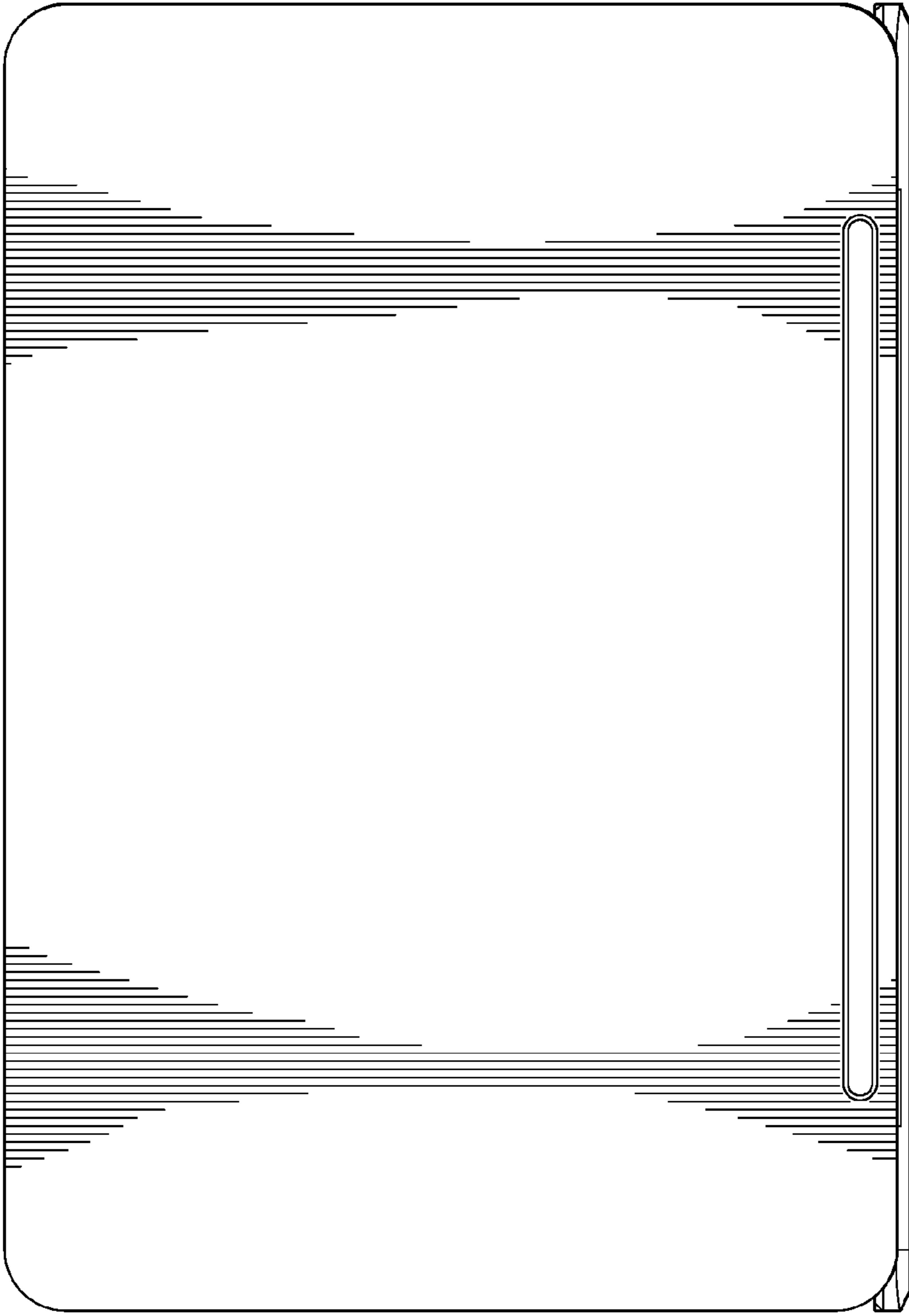


FIG. 3

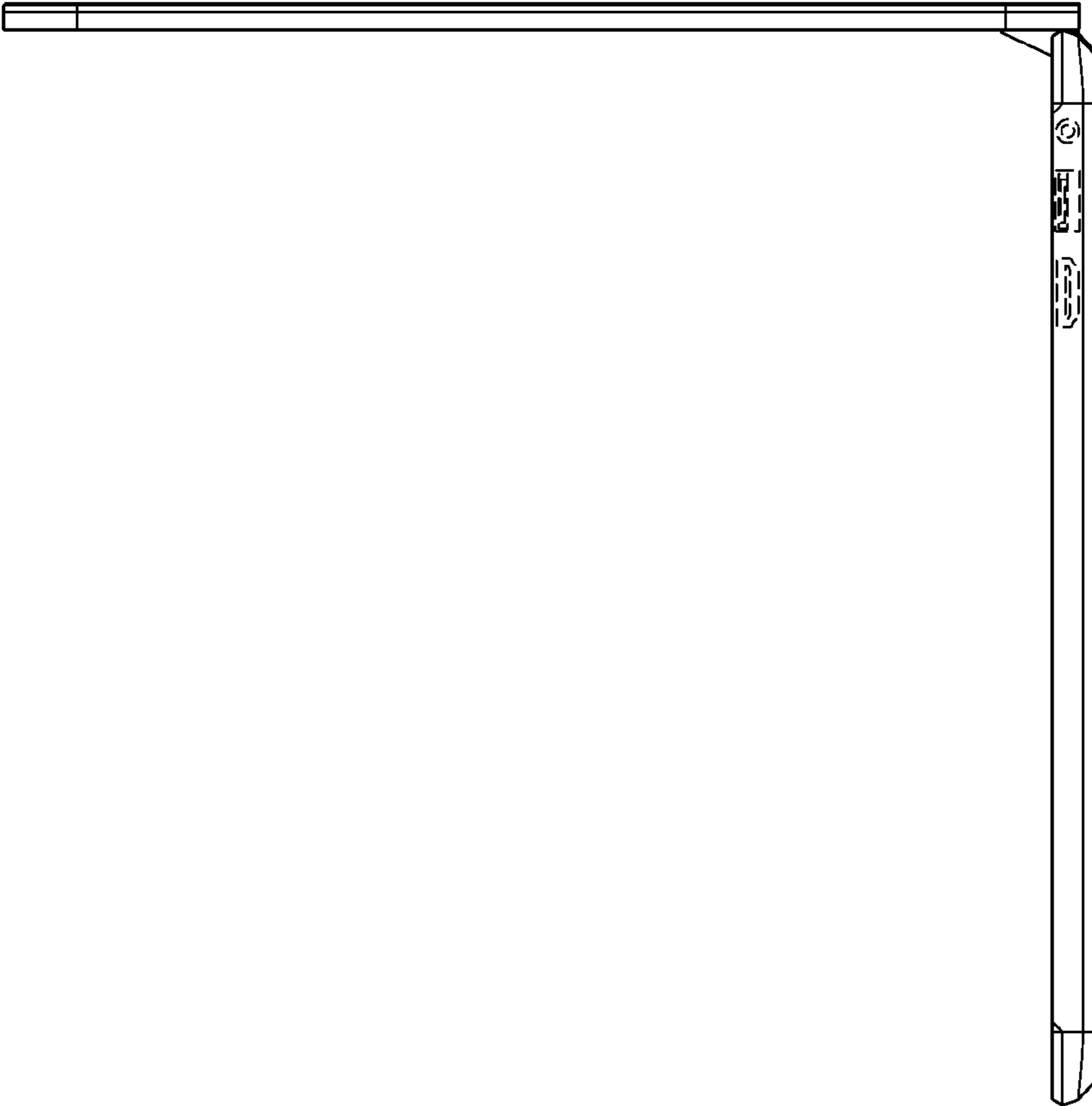


FIG. 4

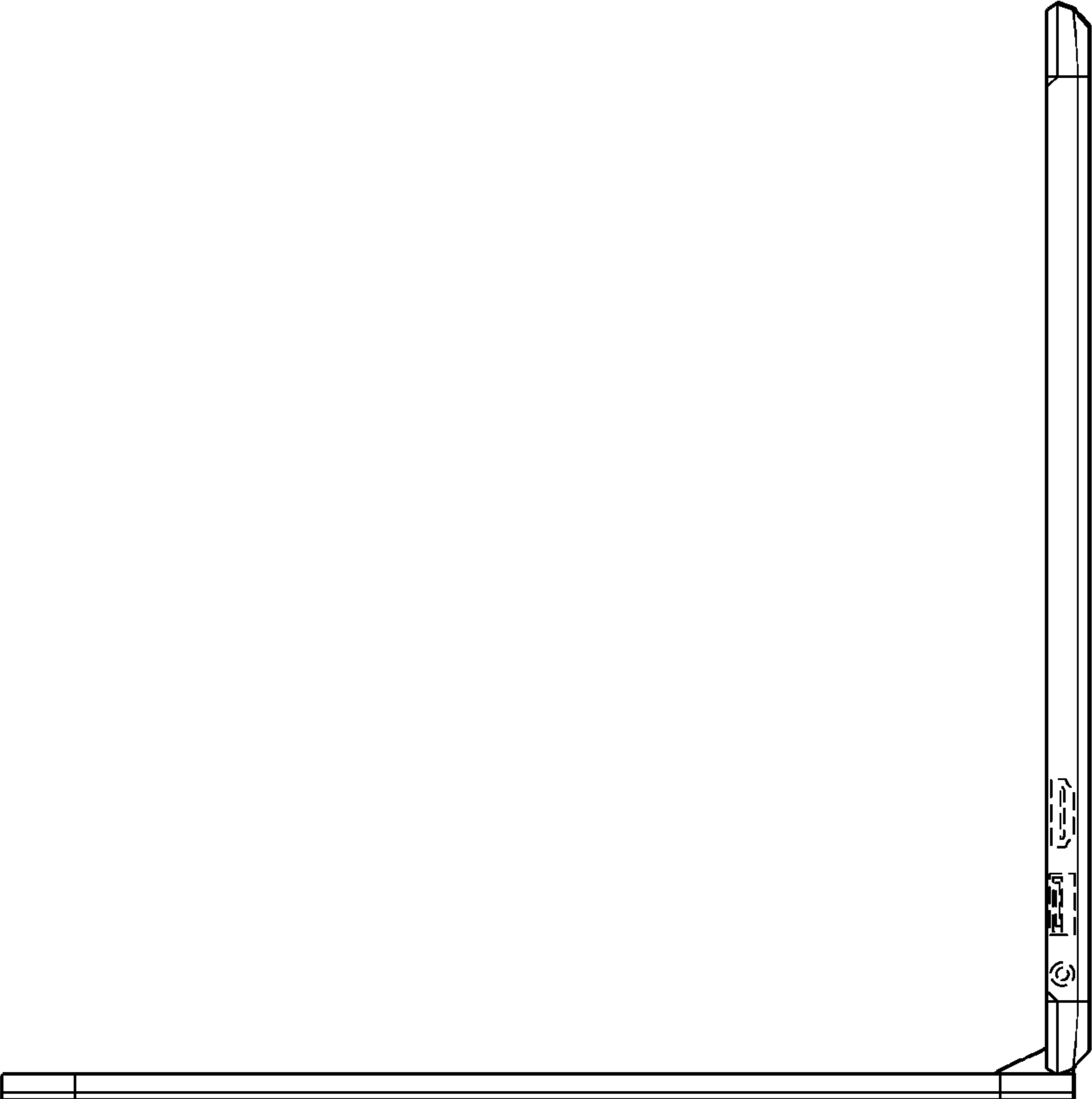


FIG. 5

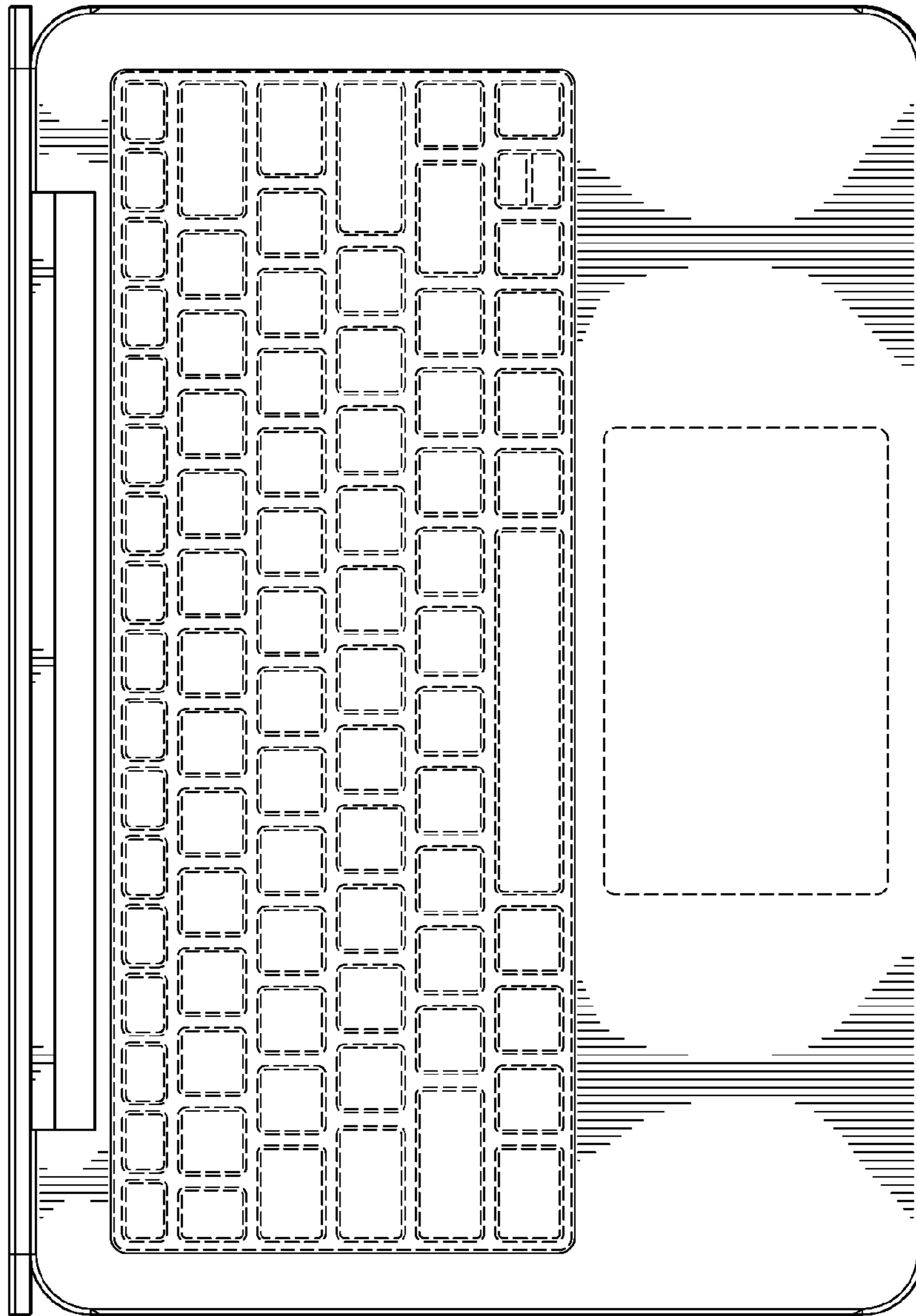


FIG. 6

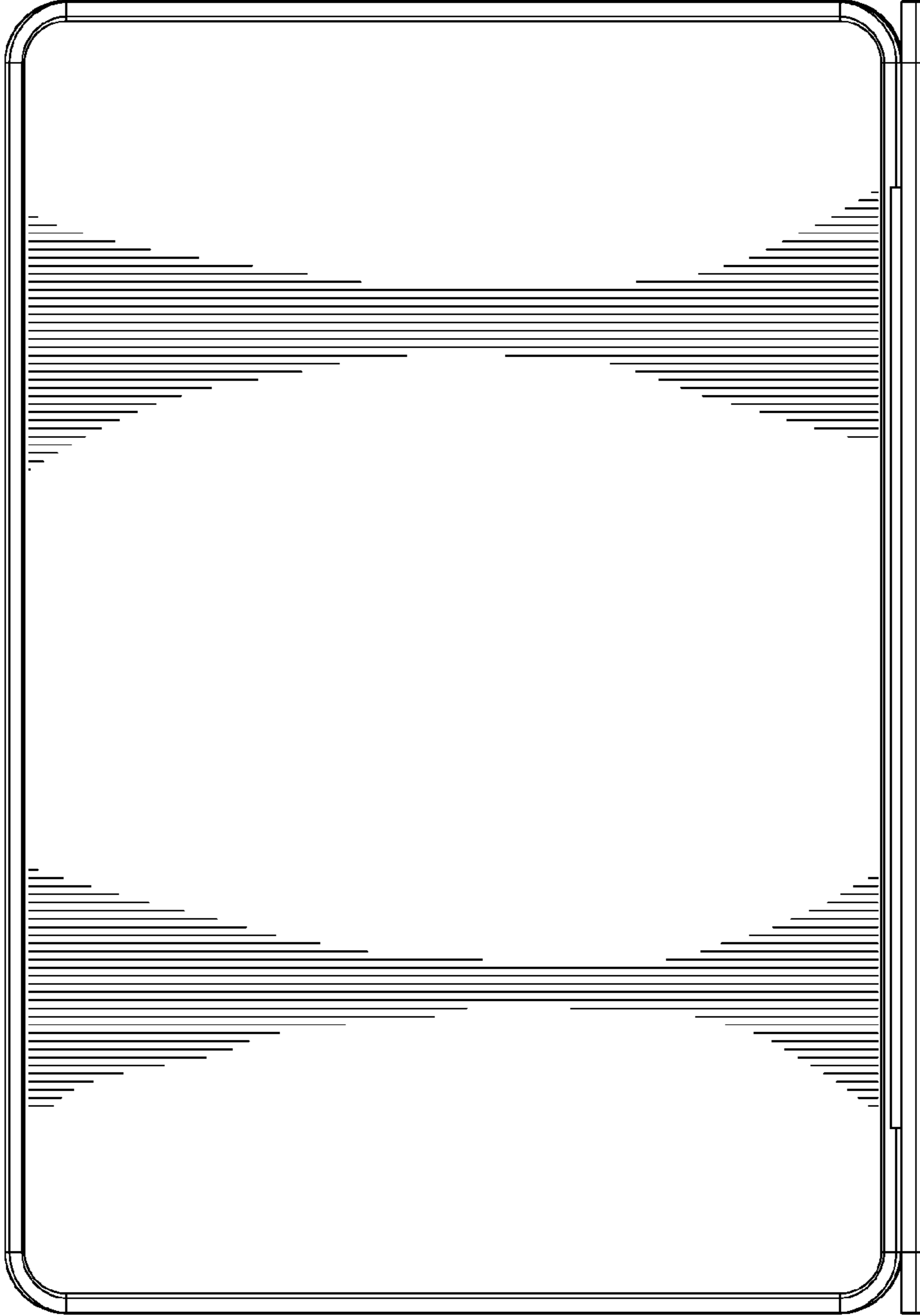


FIG. 7

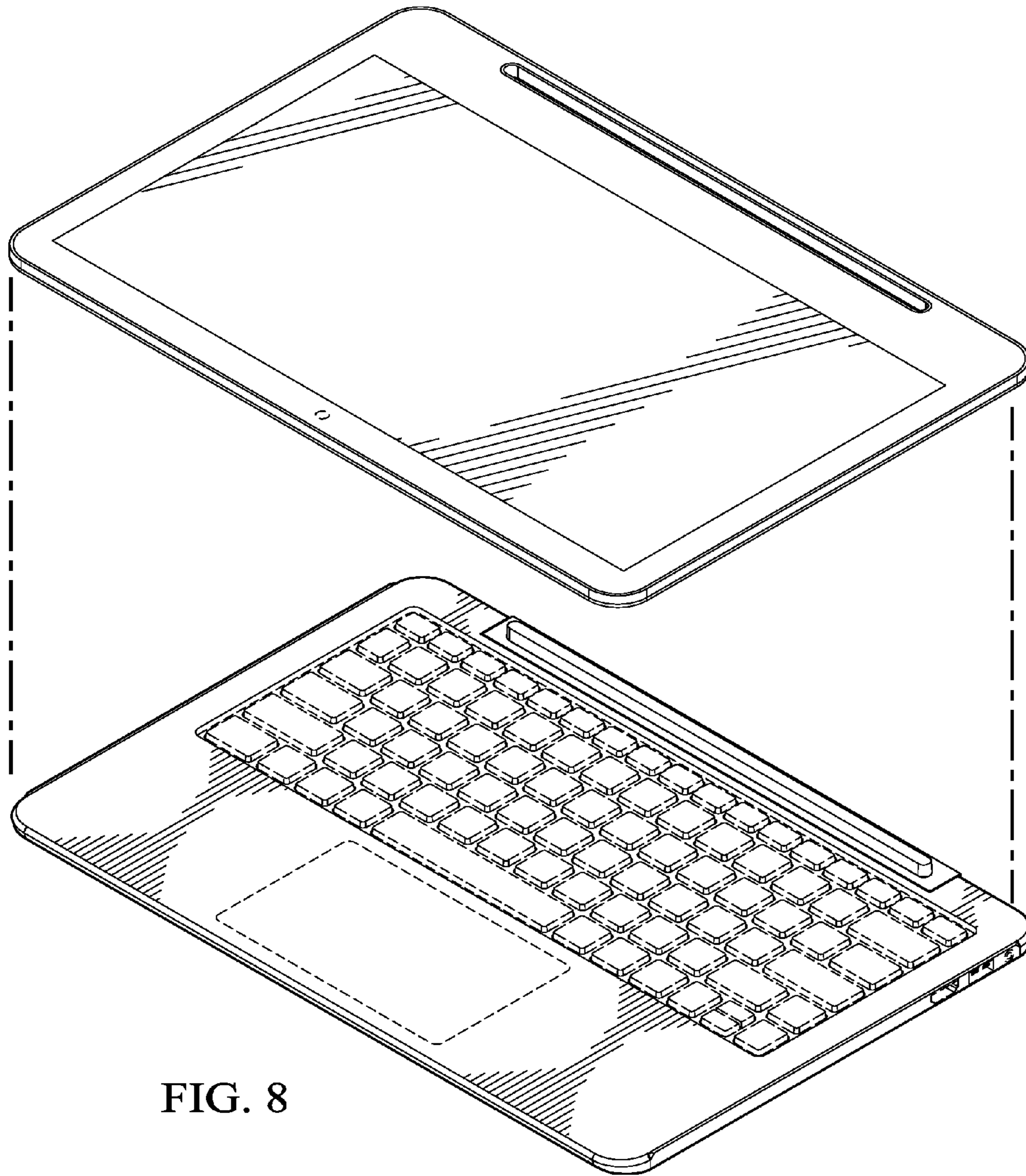


FIG. 8

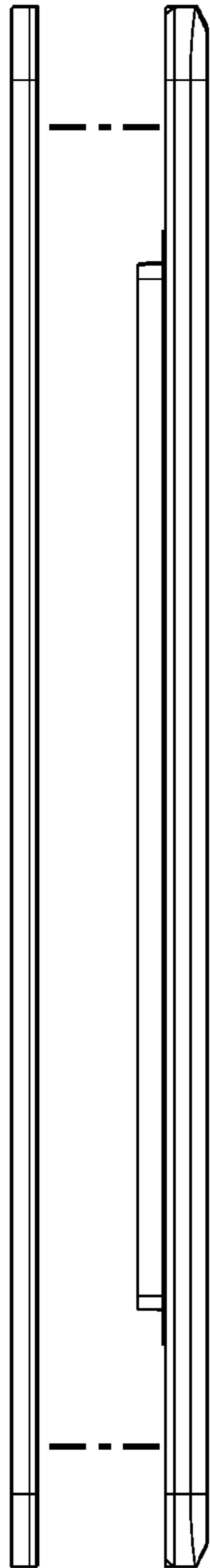


FIG. 9

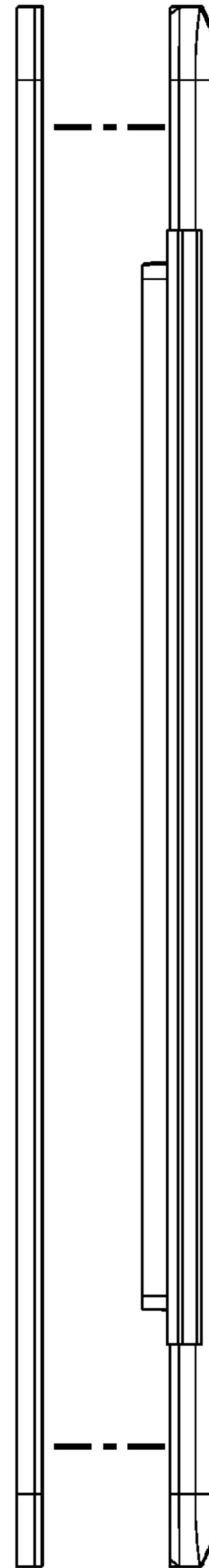


FIG. 10

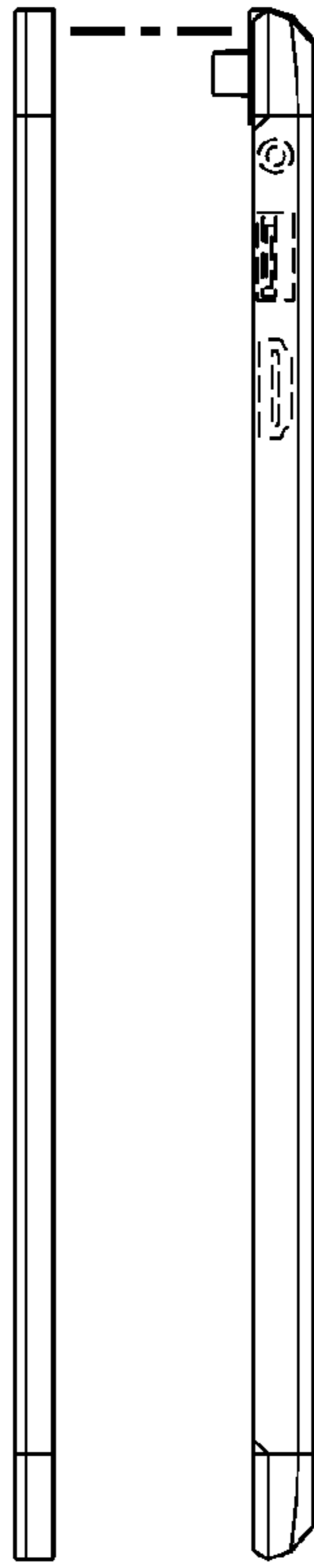


FIG. 11

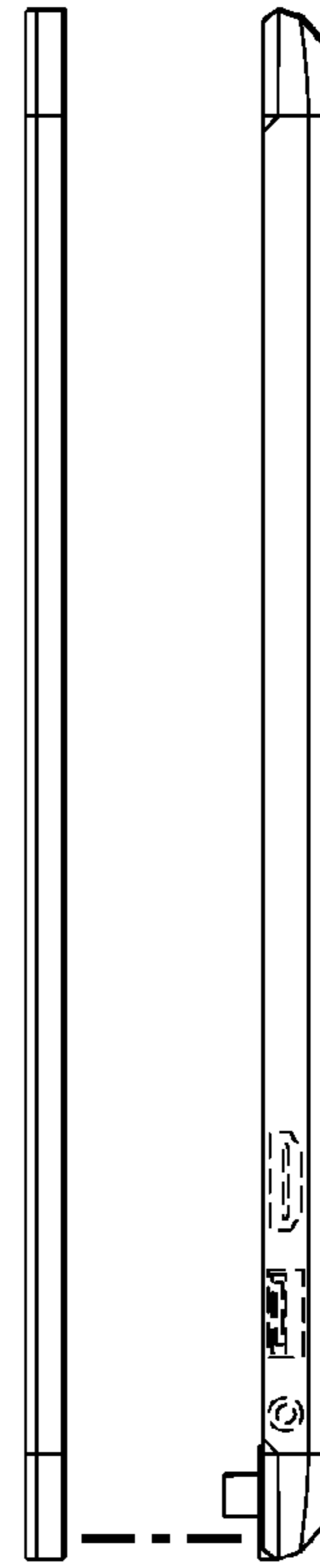


FIG. 12

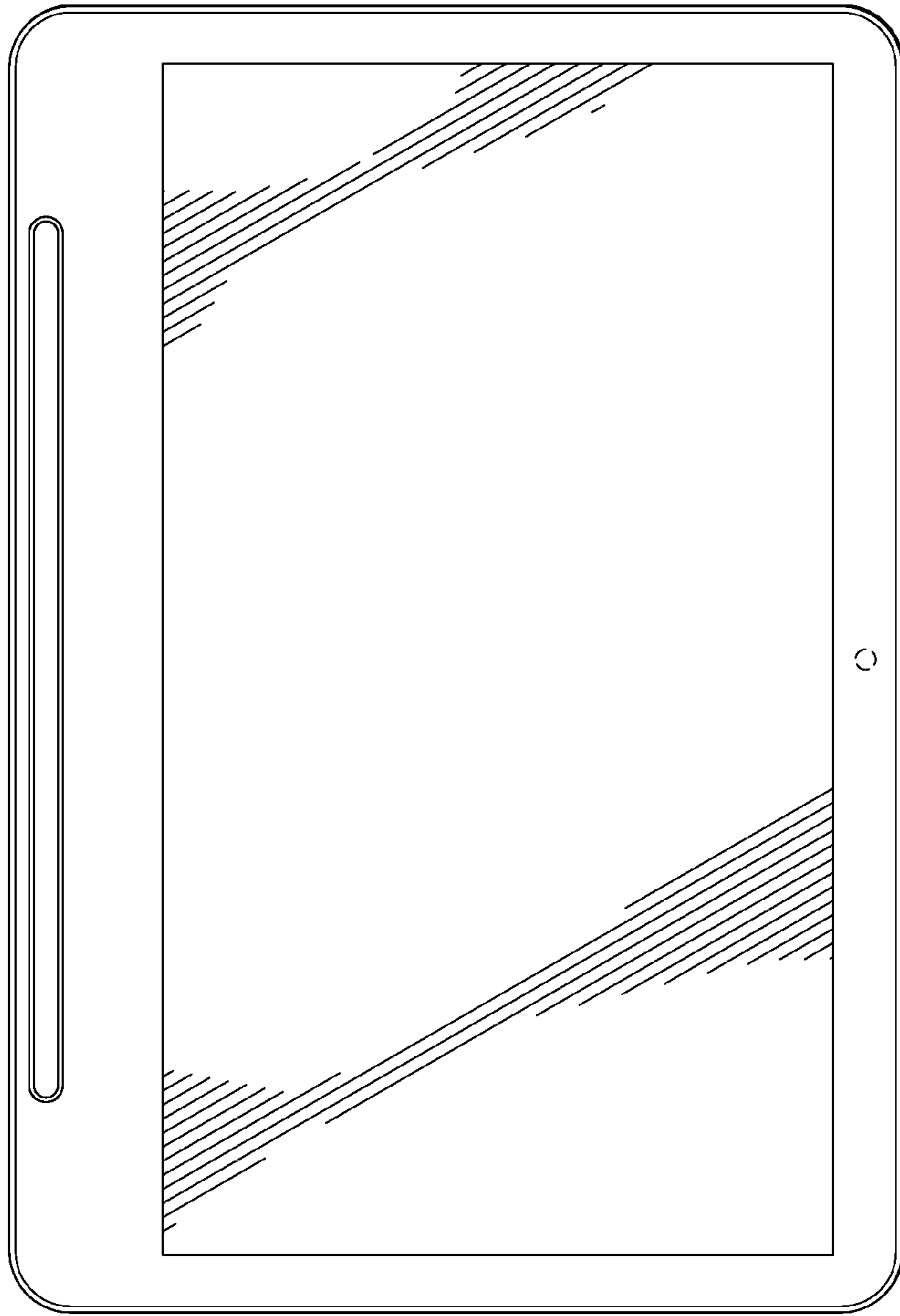


FIG. 13

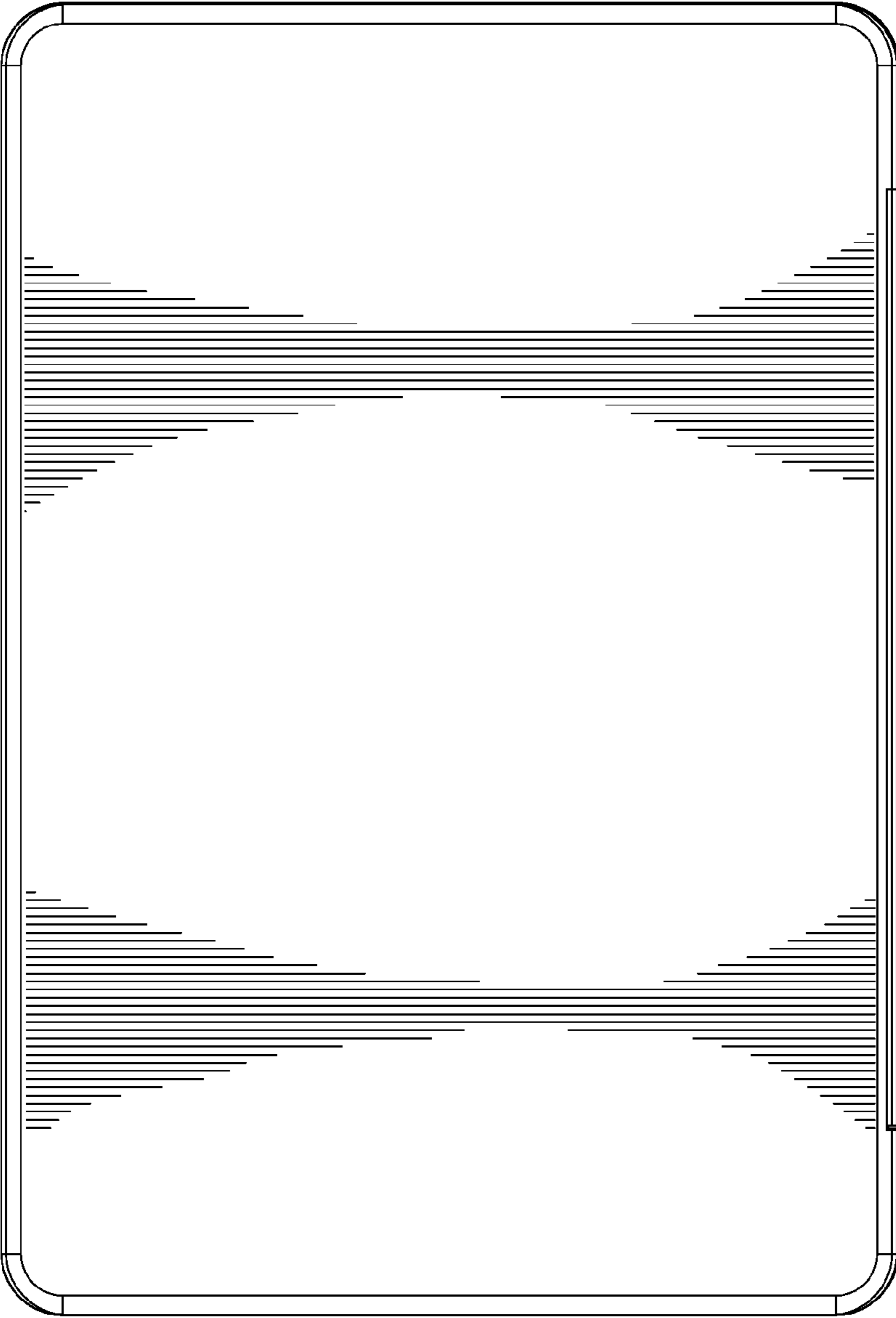


FIG. 14

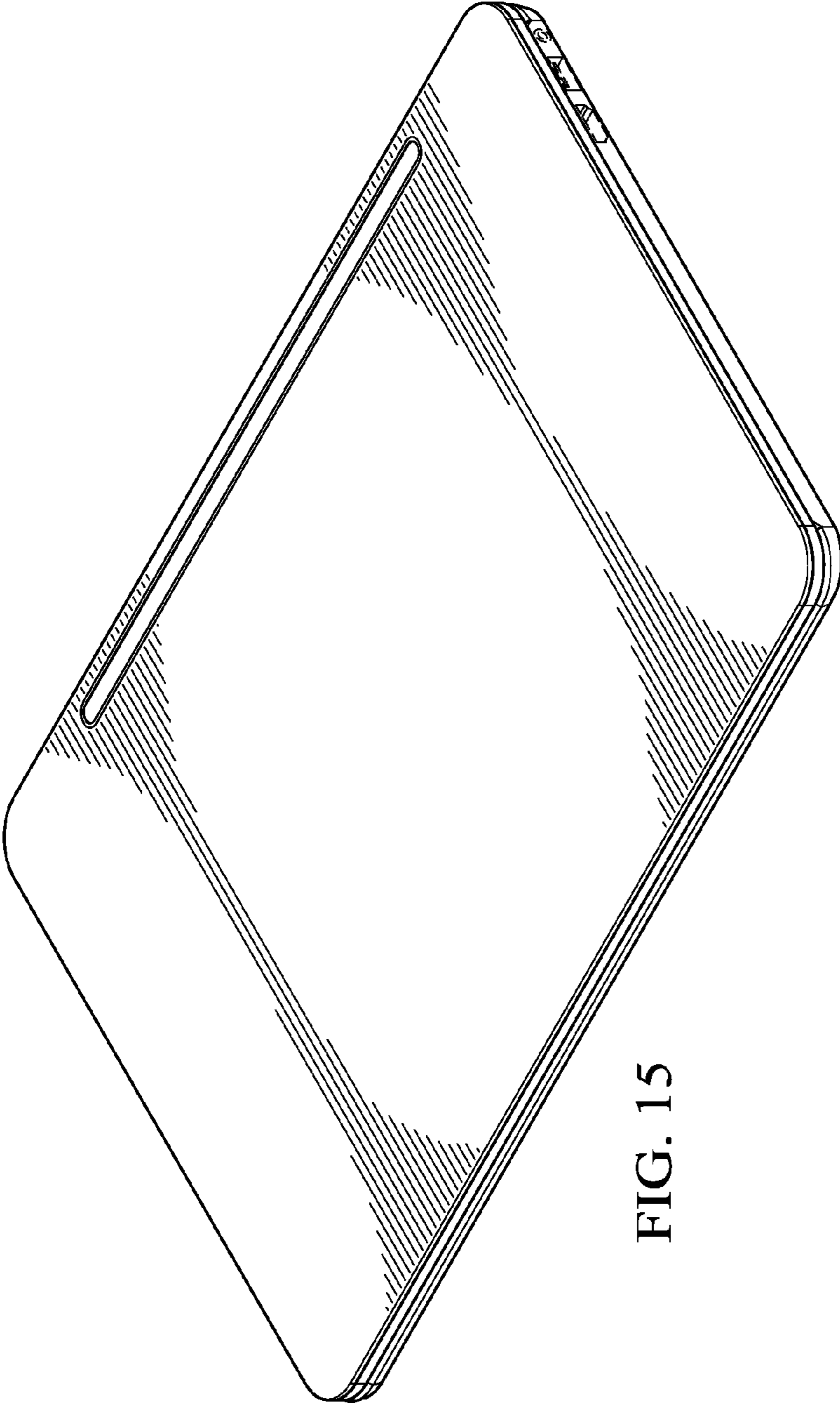


FIG. 15



FIG. 16



FIG. 17



FIG. 18

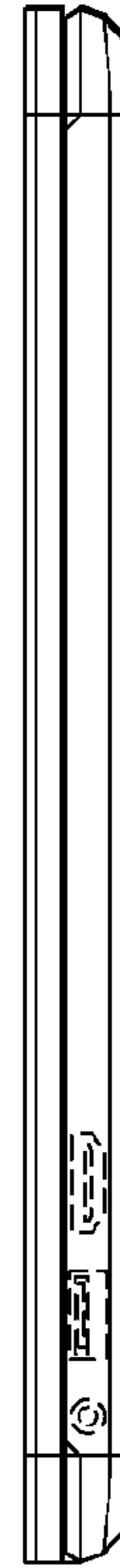


FIG. 19

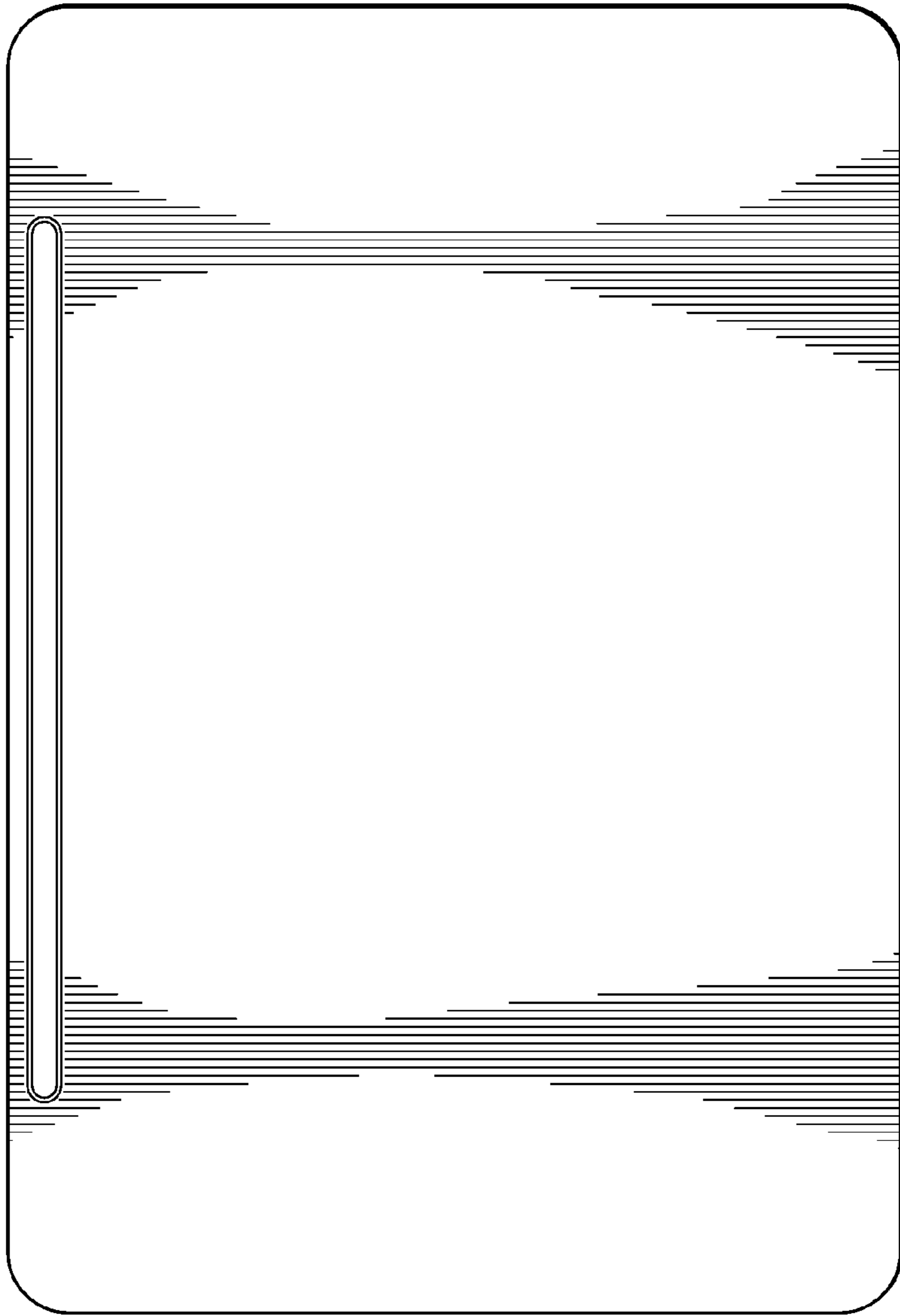


FIG. 20

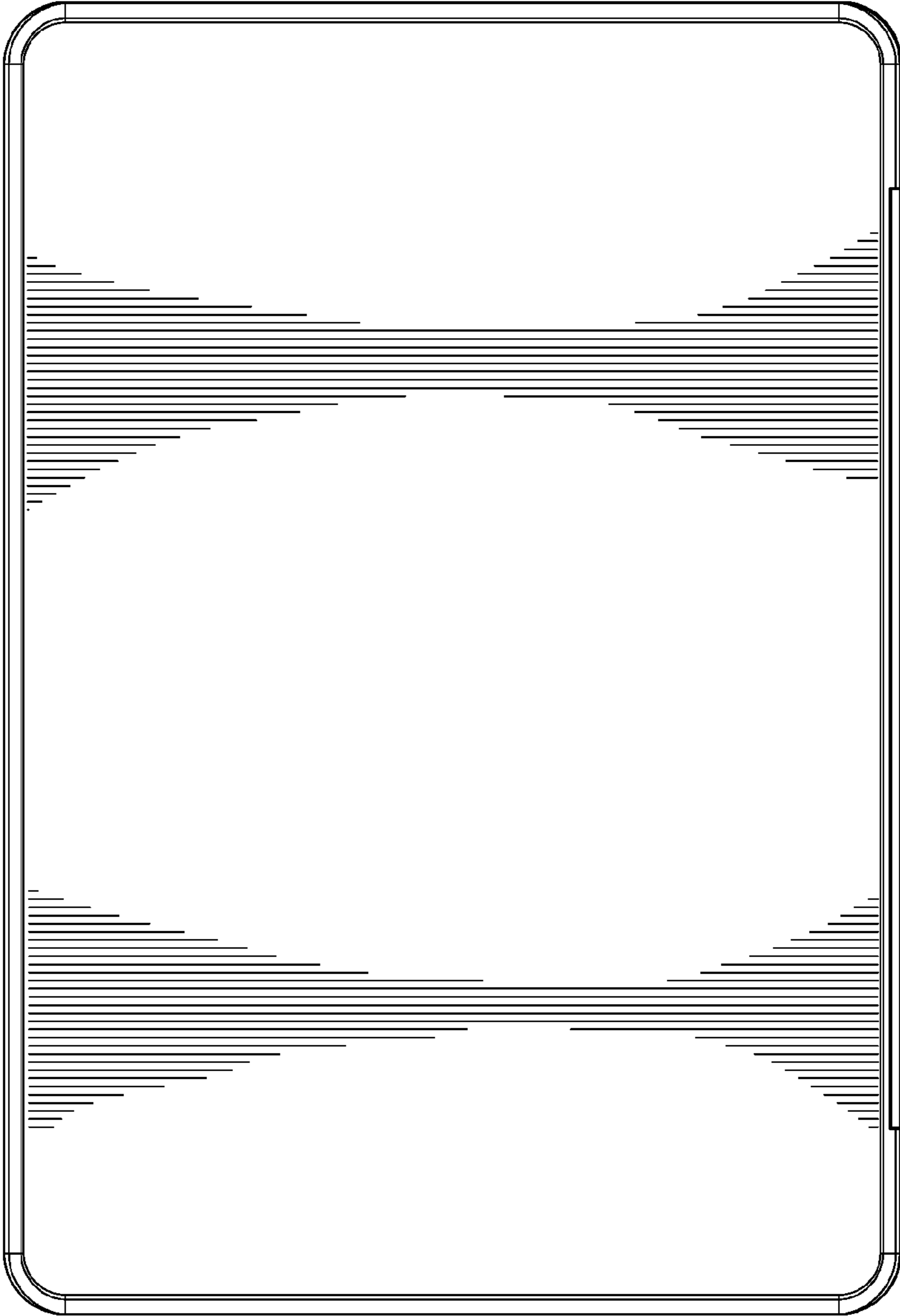


FIG. 21

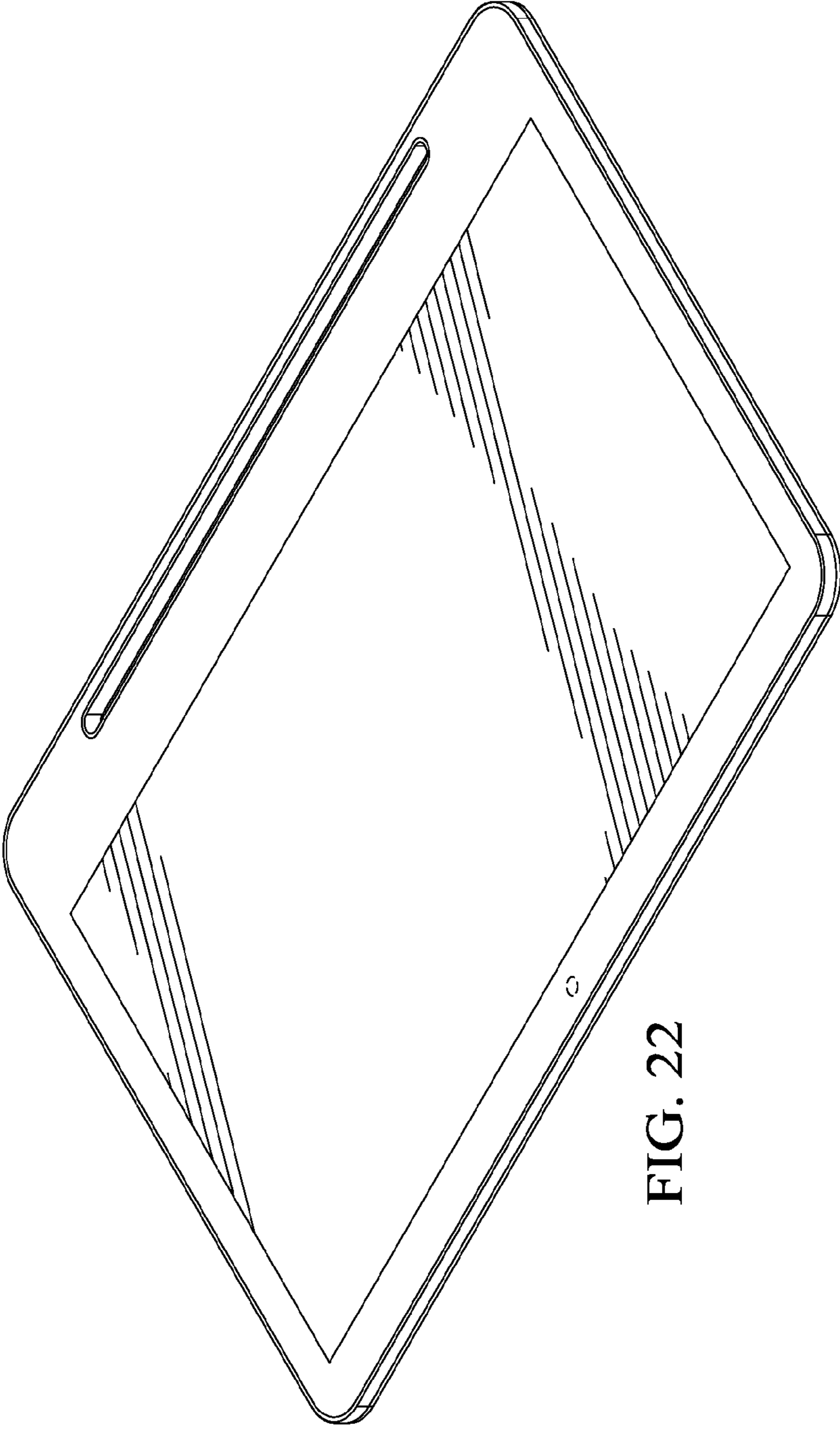


FIG. 22



FIG. 23



FIG. 24

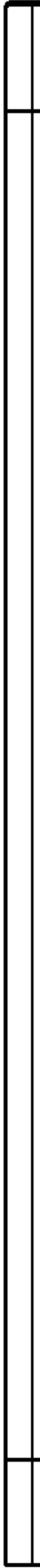


FIG. 25



FIG. 26

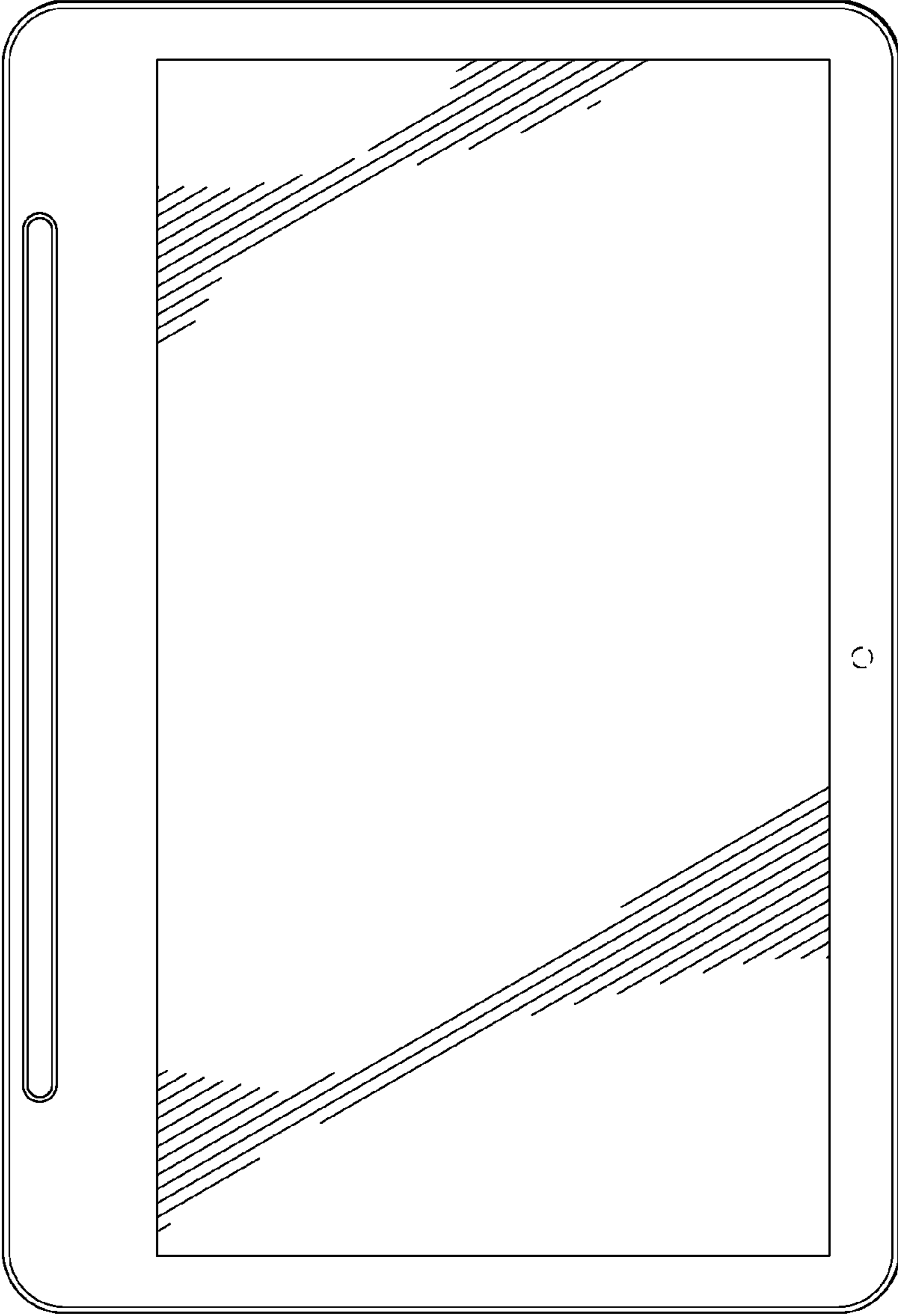


FIG. 27

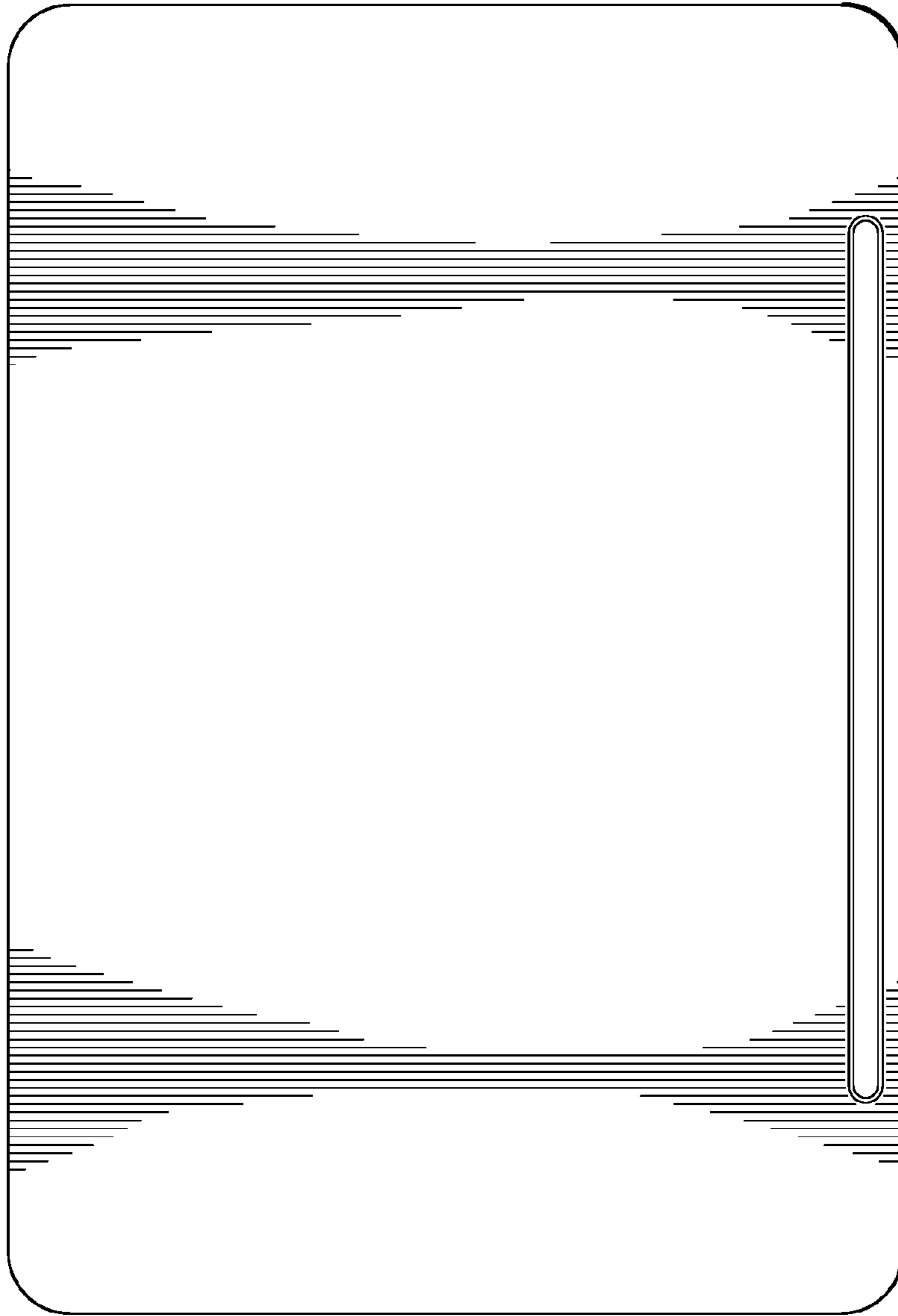


FIG. 28

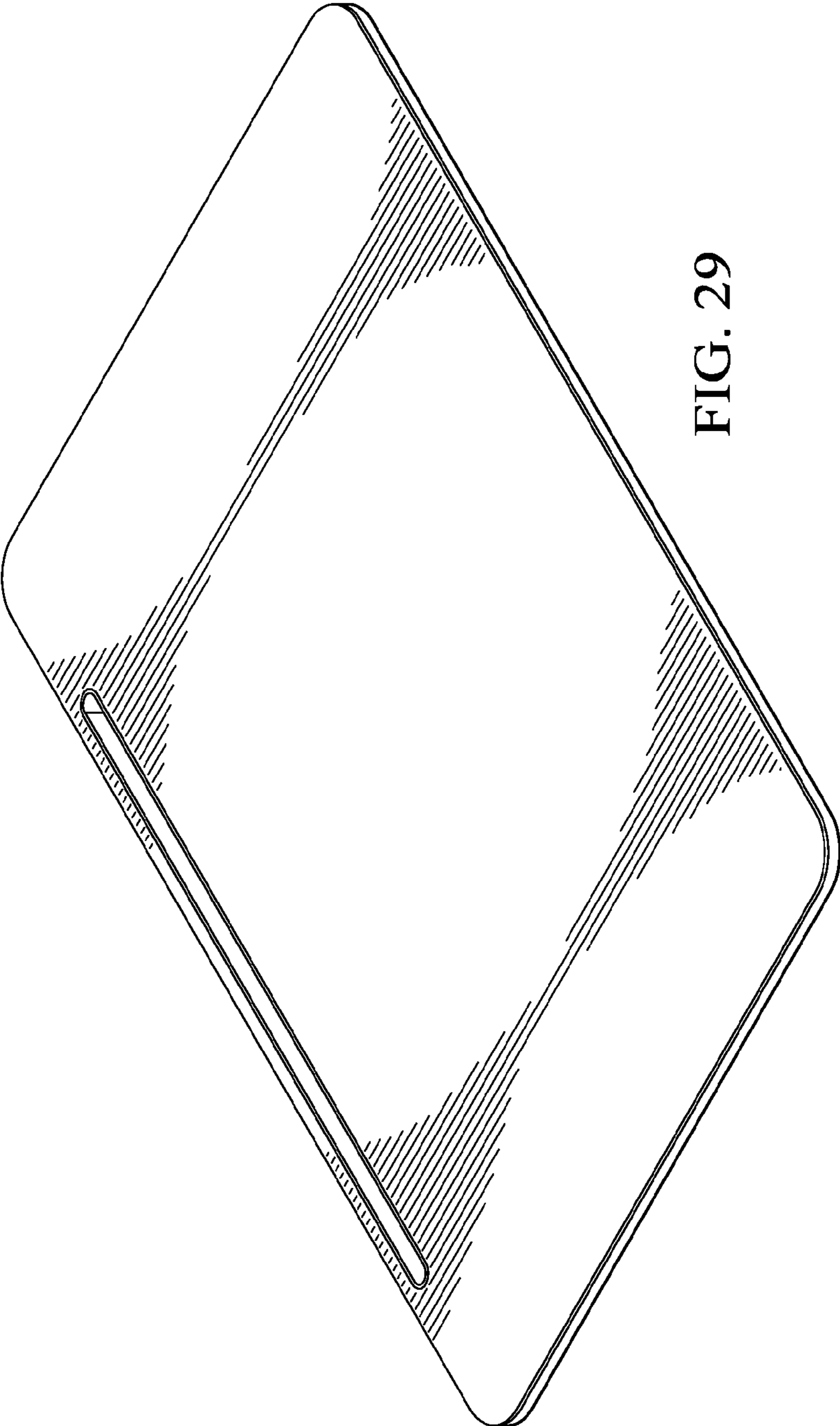


FIG. 29

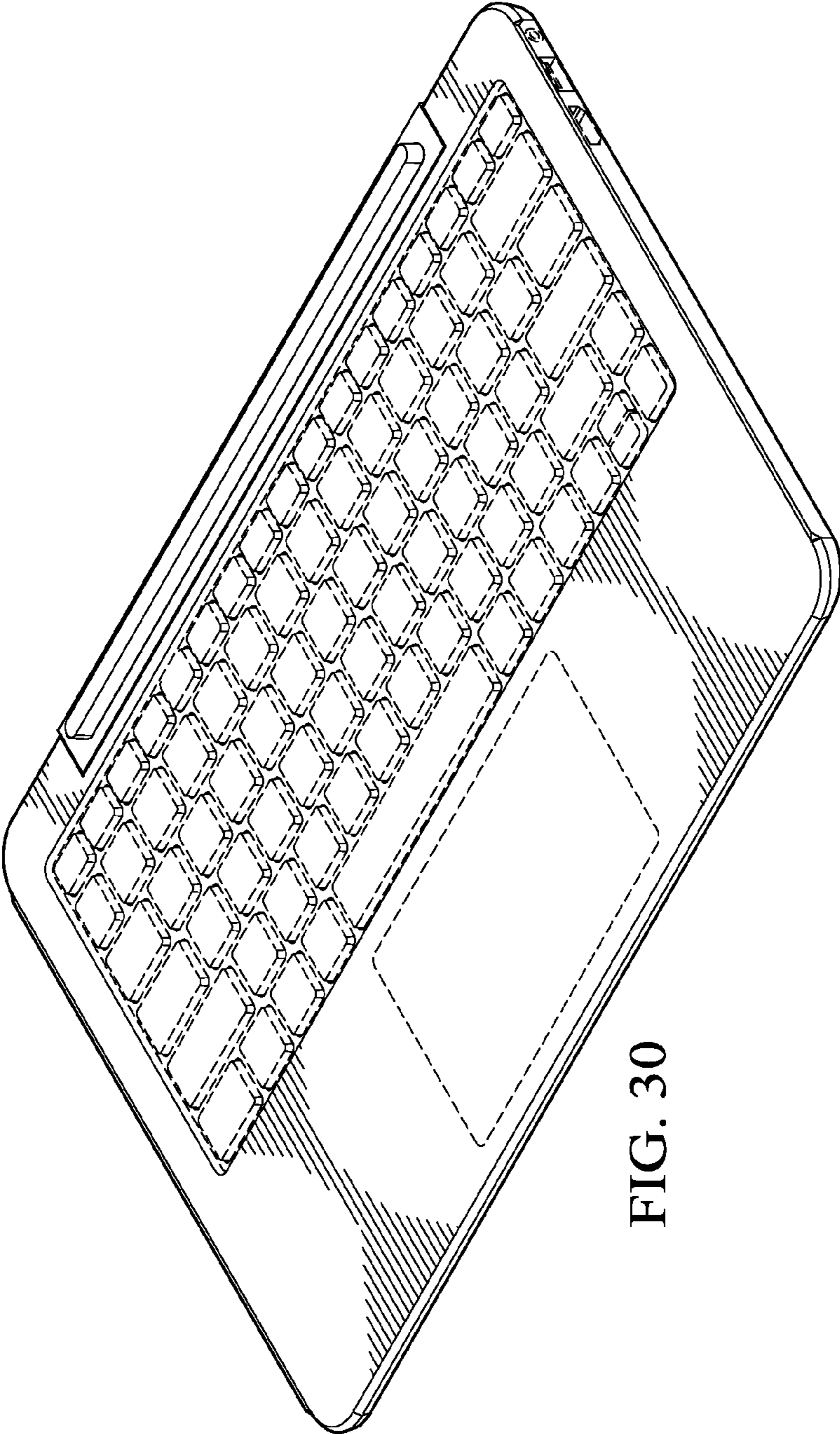


FIG. 30

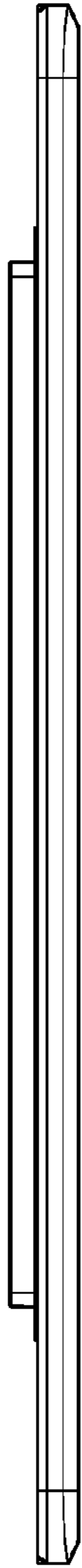


FIG. 31

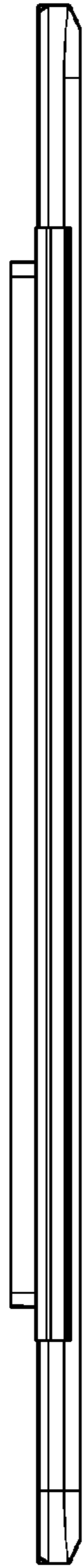


FIG. 32

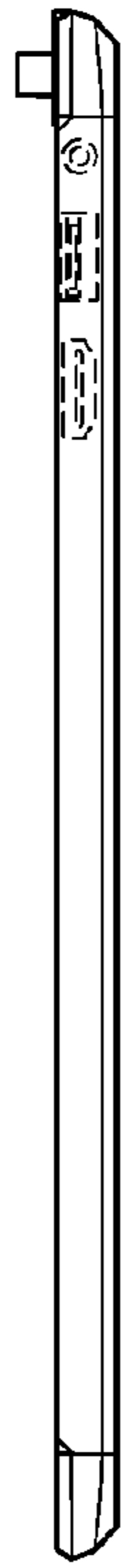


FIG. 33



FIG. 34

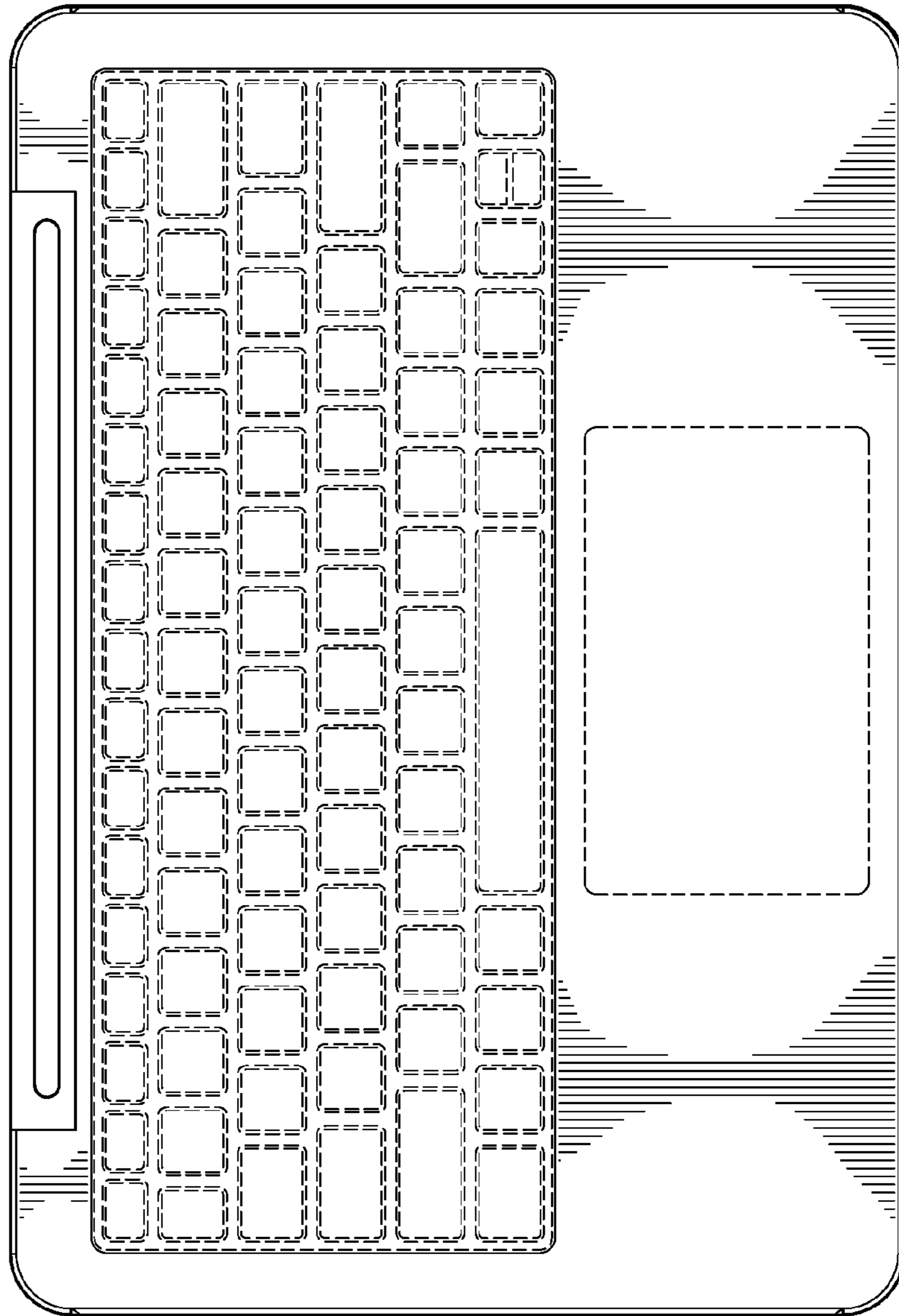


FIG. 35

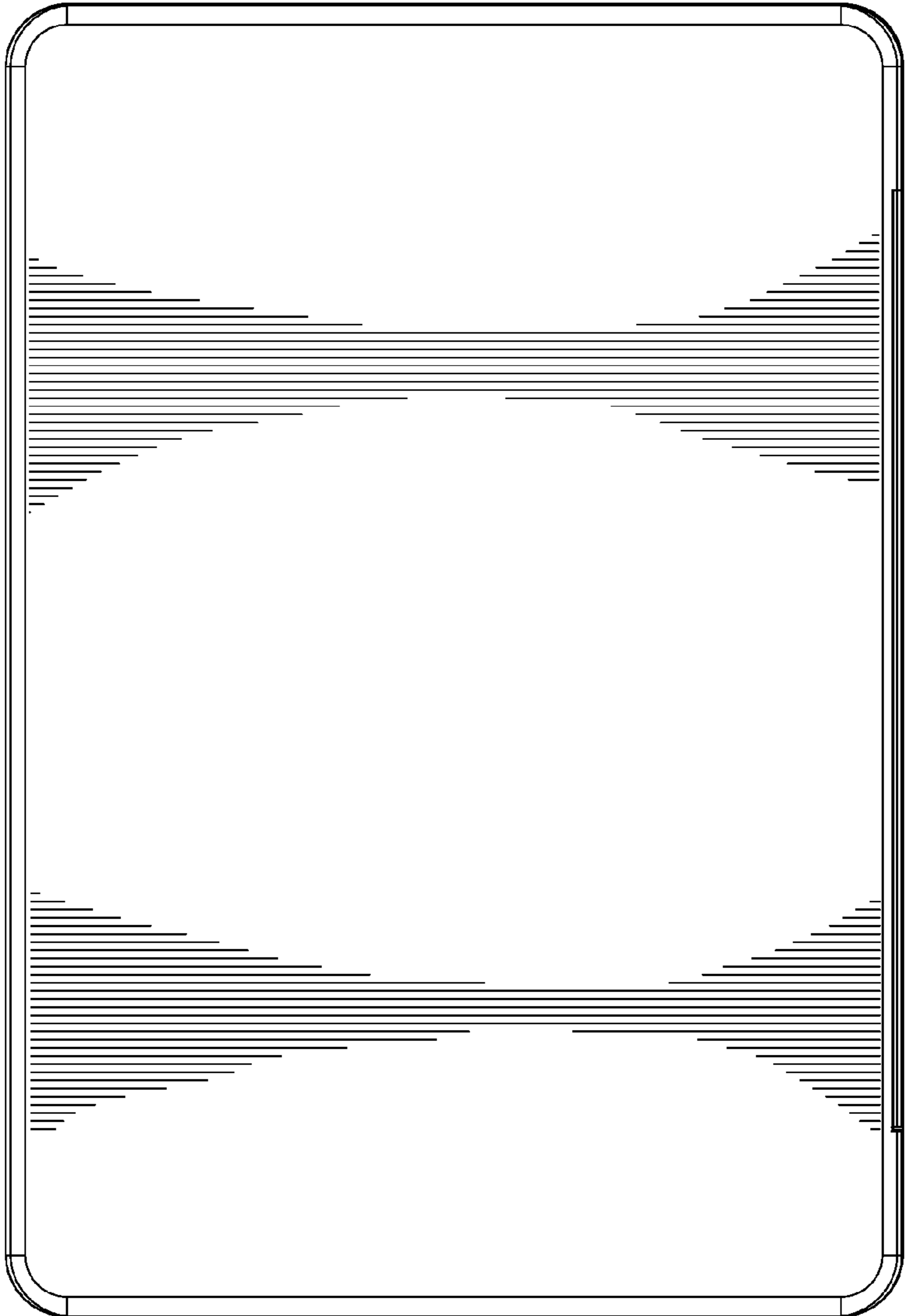


FIG. 36