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(12) **United States Design Patent** (10) **Patent No.:** **US D765,342 S**
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(54) **CHIP**
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(73) Assignee: **FAM, Kontich (BE)**
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(30) **Foreign Application Priority Data**

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(51) **LOC (10) Cl.** **01-01**

(52) **U.S. Cl.**
USPC **D1/125**

(58) **Field of Classification Search**
USPC D1/100–130, 199; 426/94, 104, 138, 426/144, 391, 549, 808, 143, 559, 560; D7/388, 359, 409; D6/606; D25/138, D25/38.1, 100, 114; 119/710; D9/456; 428/182; 521/79; 72/301

CPC A23L 1/1641; A23L 1/005; A23G 3/54; A23G 3/50; A21D 13/0025; A21D 13/0029
See application file for complete search history.

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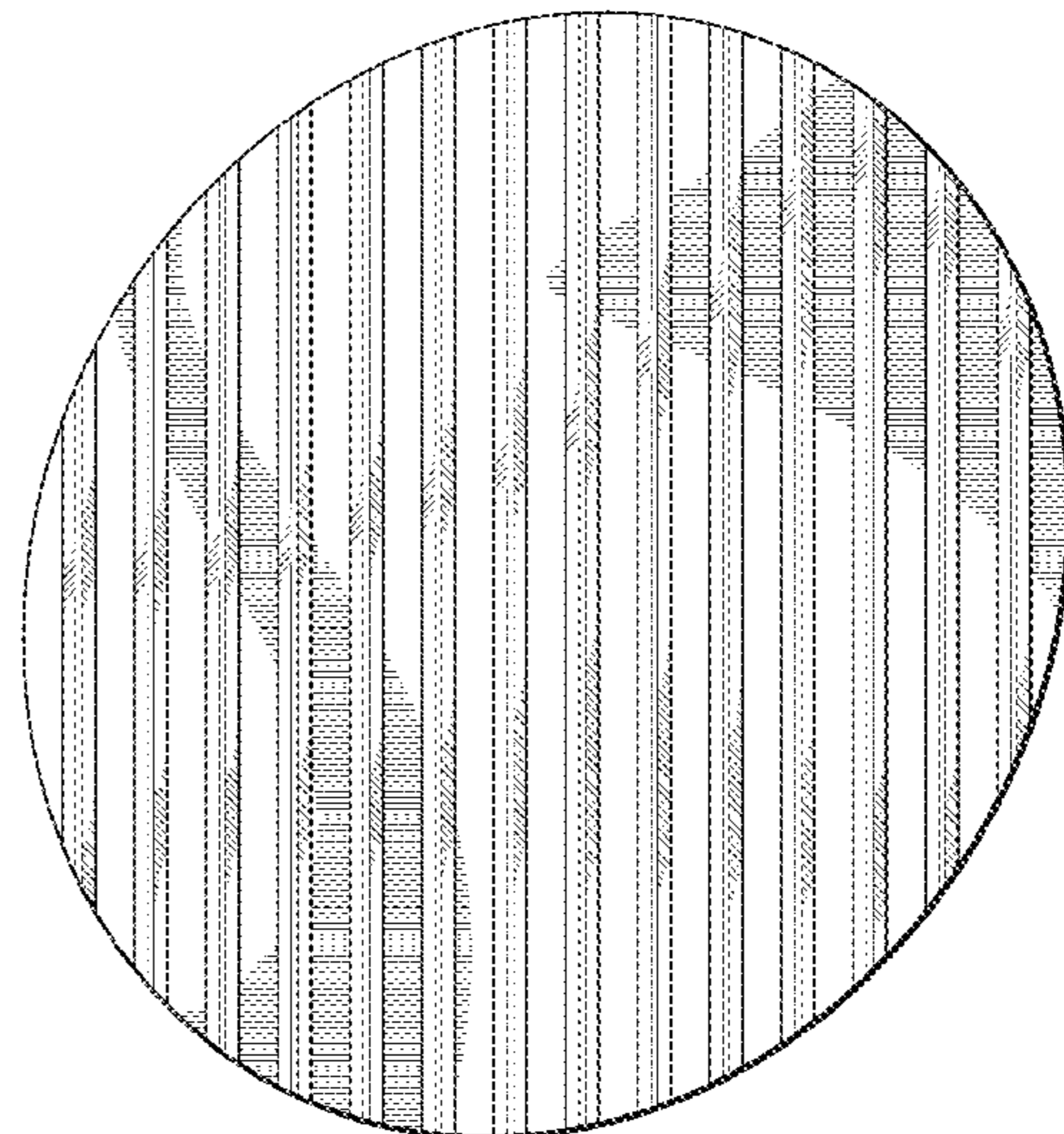
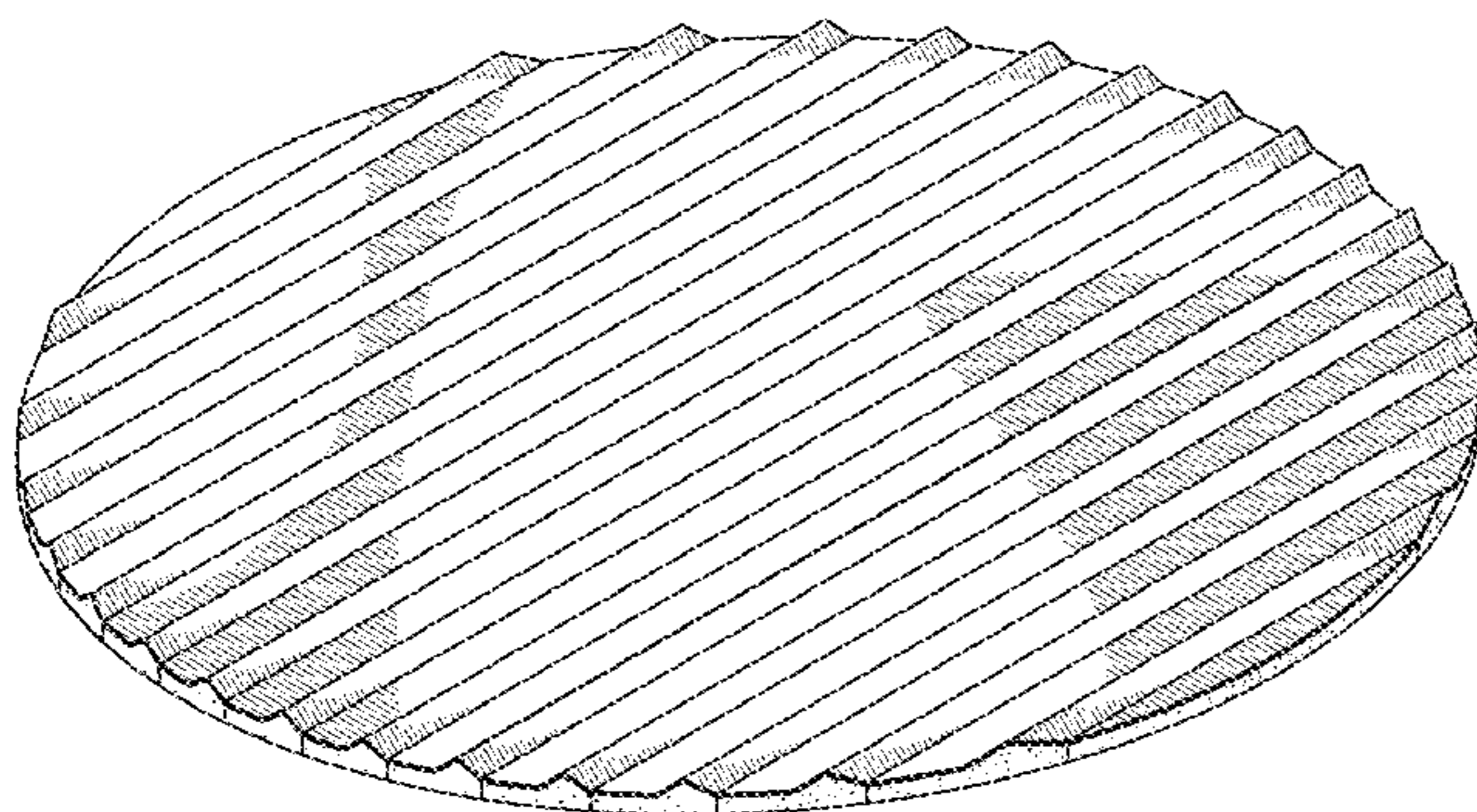
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(57) **CLAIM**
The ornamental design for a chip, as shown and described.

DESCRIPTION

FIG. 1 is an isometric view of front, top, and right side of a new ornamental design for a chip;
FIG. 2 is a top view of the chip of FIG. 1;
FIG. 3 is a bottom view of the chip of FIG. 1;
FIG. 4 is a right side elevational view of the chip of FIG. 1; and,
FIG. 5 is a front elevational view of the chip of FIG. 1, the rear elevational view being a mirror image thereof.

1 Claim, 2 Drawing Sheets



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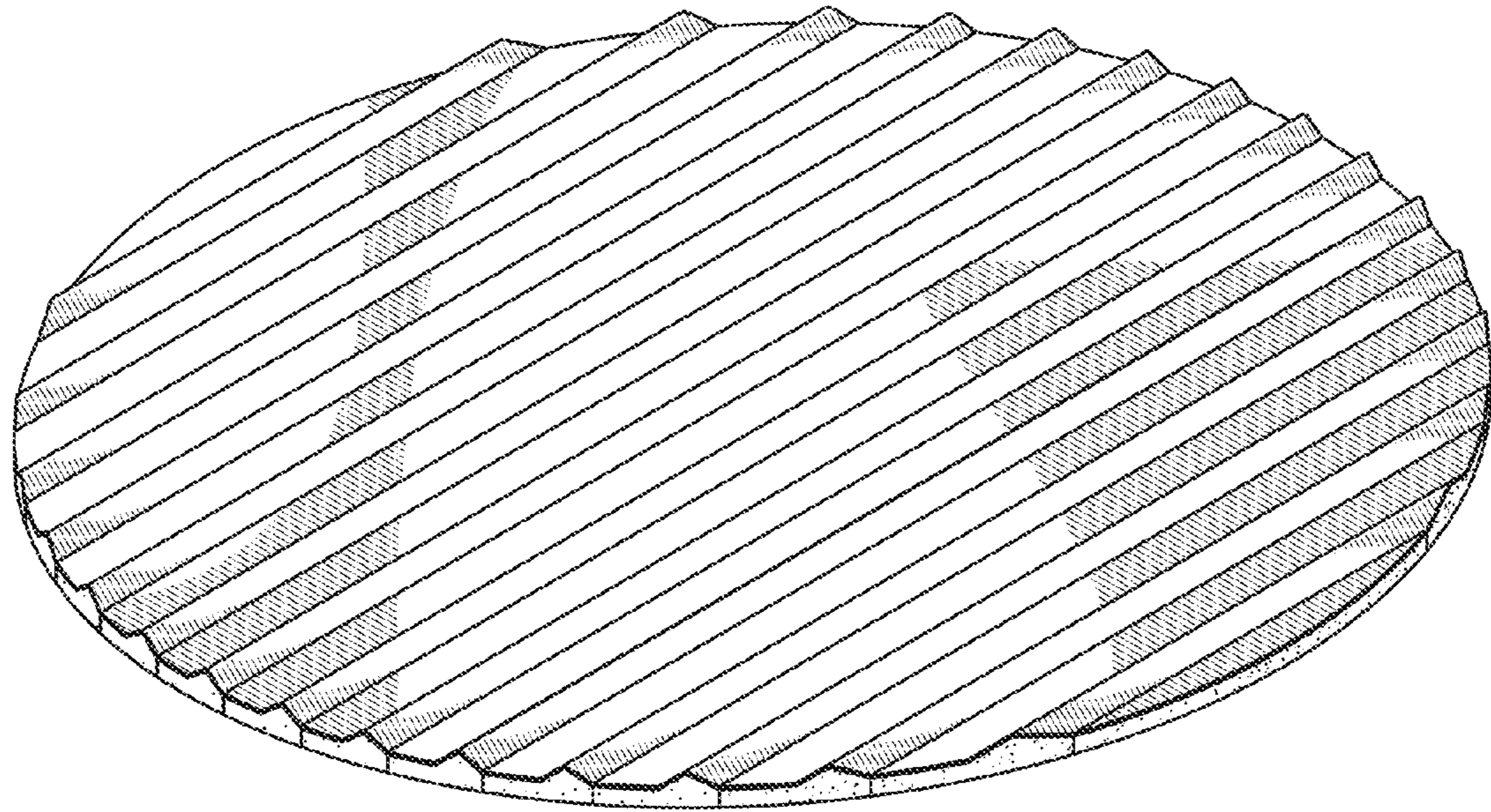


FIG. 1

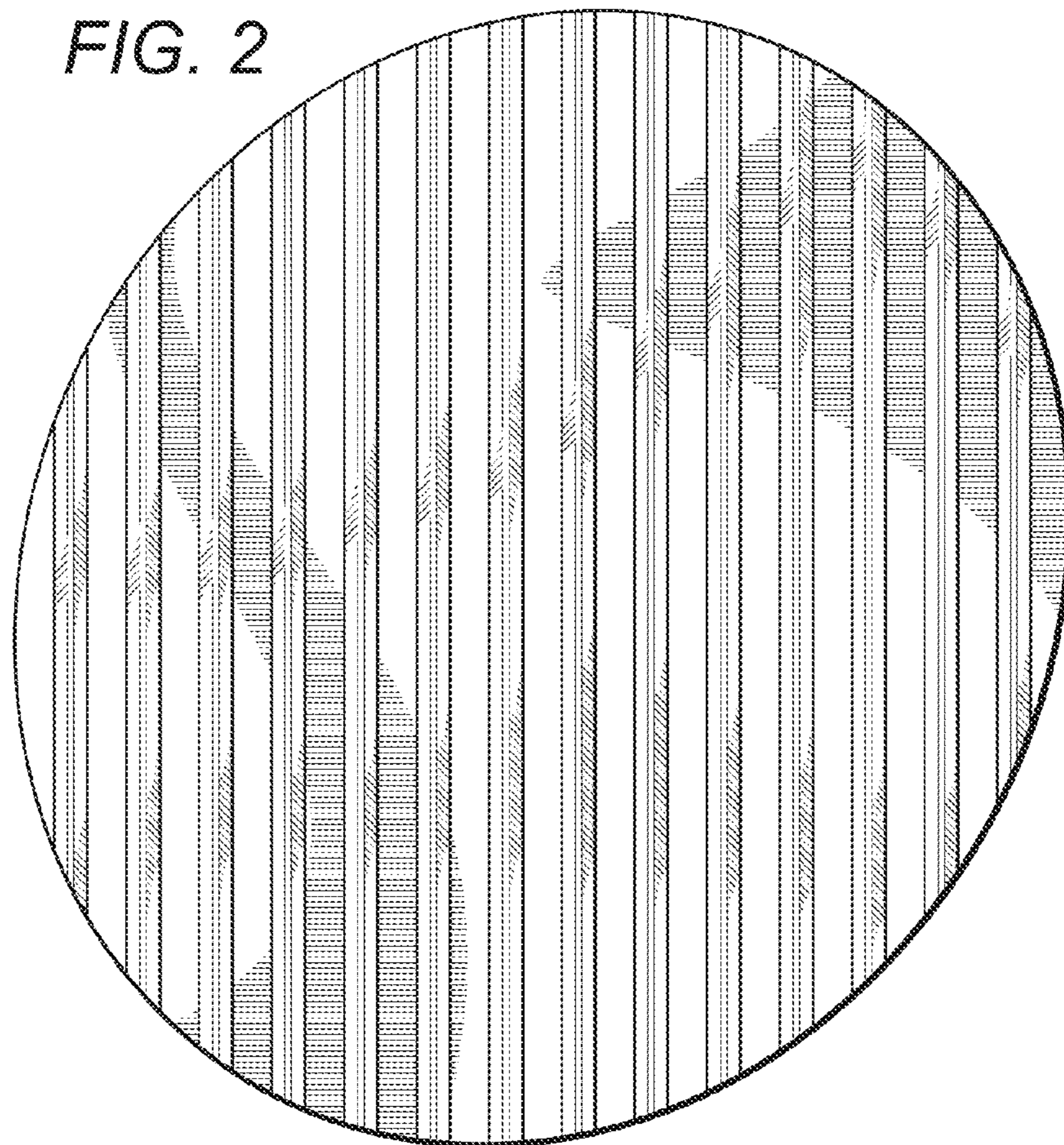


FIG. 2

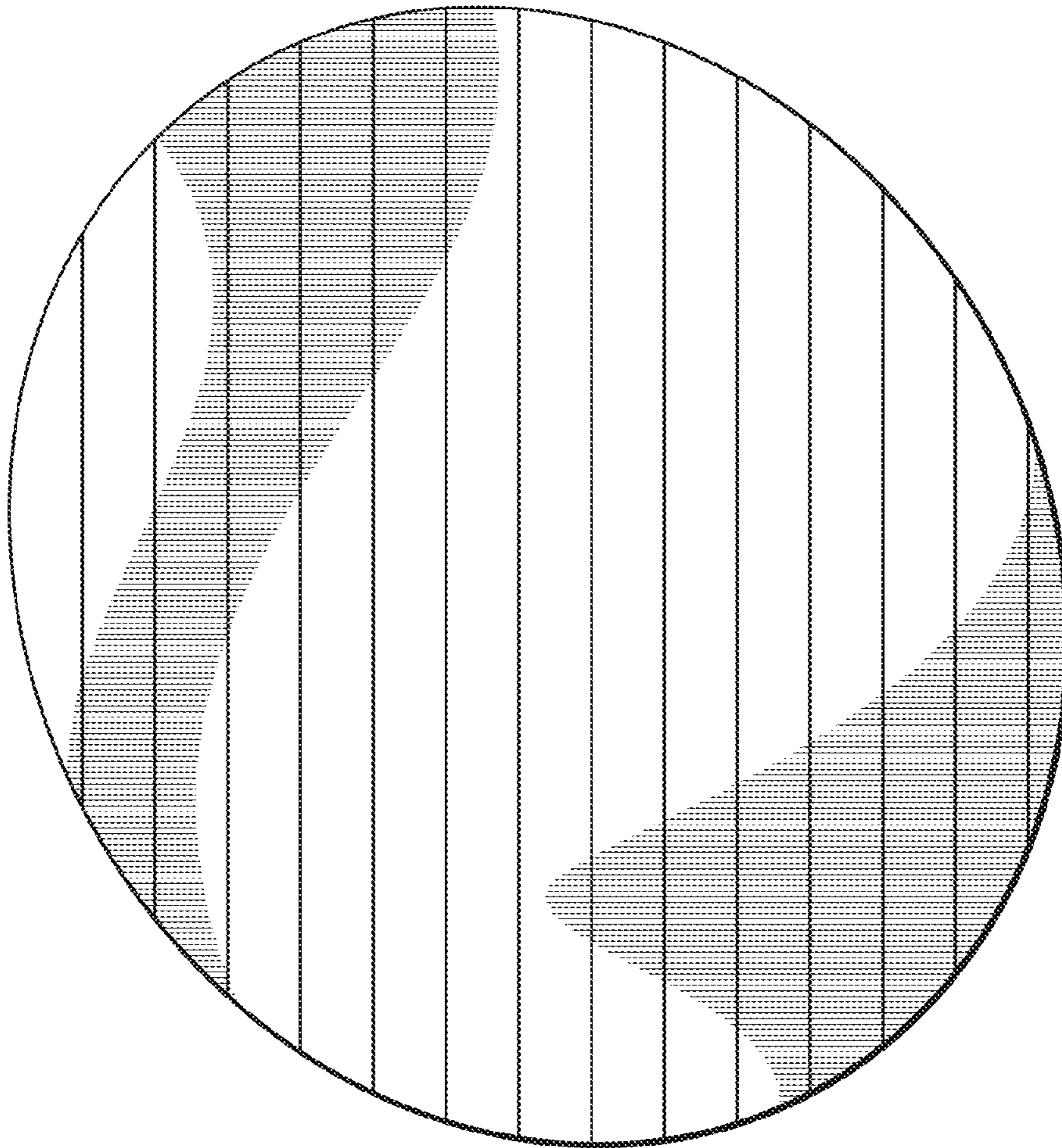


FIG. 3

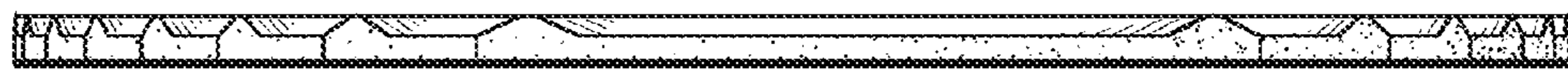


FIG. 4



FIG. 5