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(12) **United States Design Patent** (10) **Patent No.:** **US D765,066 S**
Cowles et al. (45) **Date of Patent:** **** *Aug. 30, 2016**

(54) **ELECTRONIC HOUSING**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

(21) Appl. No.: **29/480,293**

(22) Filed: **Jan. 24, 2014**

(51) **LOC (10) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/240**

(58) **Field of Classification Search**
USPC D14/135, 137, 139, 140-140.9, 155, D14/167, 240, 242, 243, 348, 349, 351, 354, D14/355, 356, 357, 358, 385, 433, 435, D14/435.1, 436, 474, 480, 496; D13/103, D13/108, 110, 147; D10/75
CPC H04L 12/00; H03K 17/00; H04W 88/00; H04W 88/005; H04W 88/02; H04W 88/08; H04W 88/085; H04W 88/10; H04W 88/12; H04W 88/14; H04B 1/38
See application file for complete search history.

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(Continued)

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(57) **CLAIM**

The ornamental design for an electronic housing, as shown and described.

DESCRIPTION

FIG. 1 shows a front perspective view of an electronic housing.

FIG. 2 shows a back perspective view of an electronic housing.

FIG. 3 shows a front elevational view of an electronic housing.

FIG. 4 shows a back elevation view of an electronic housing.

FIG. 5 shows a right side elevational view of an electronic housing.

FIG. 6 shows a top plan view of an electronic housing.

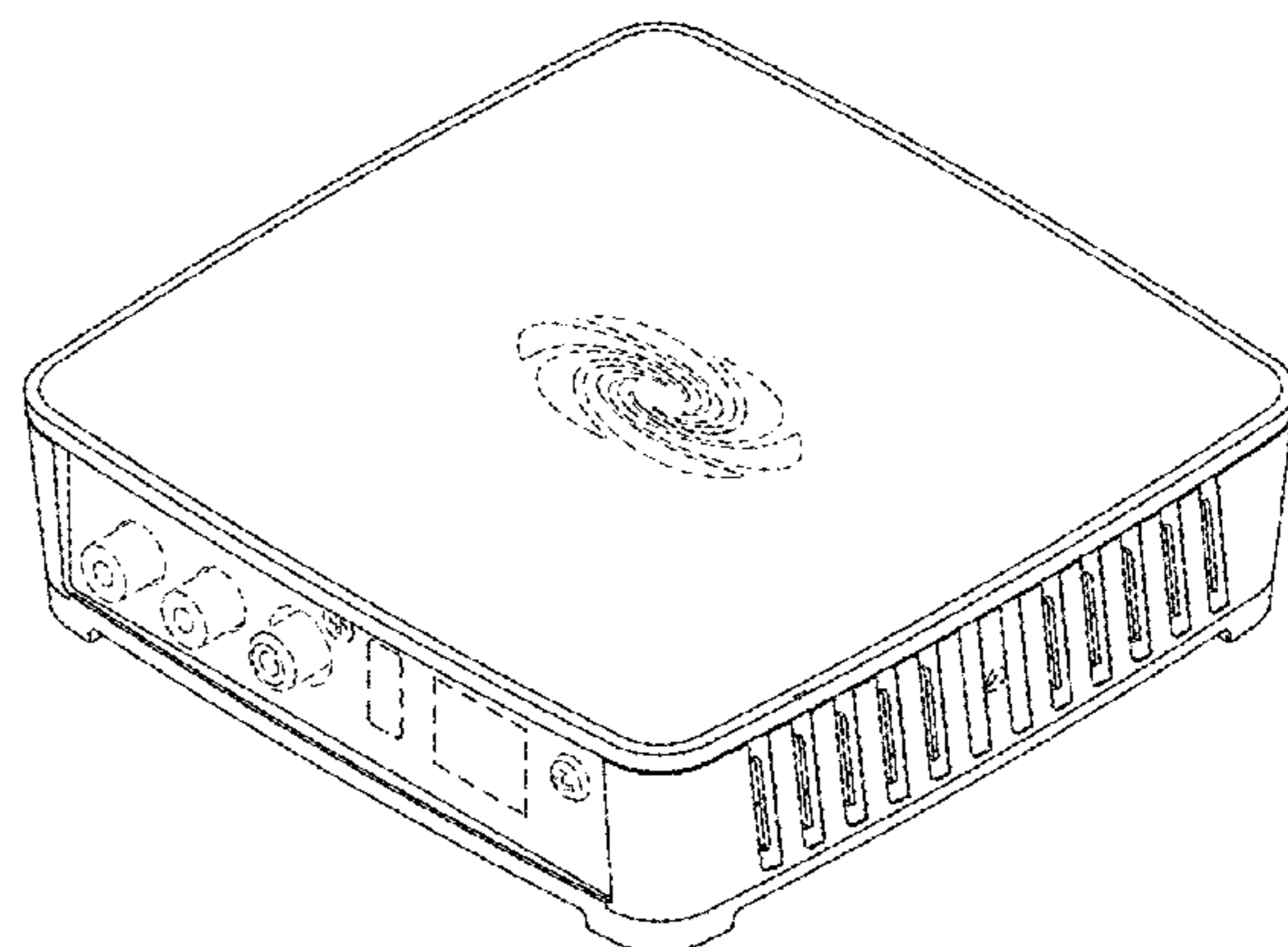
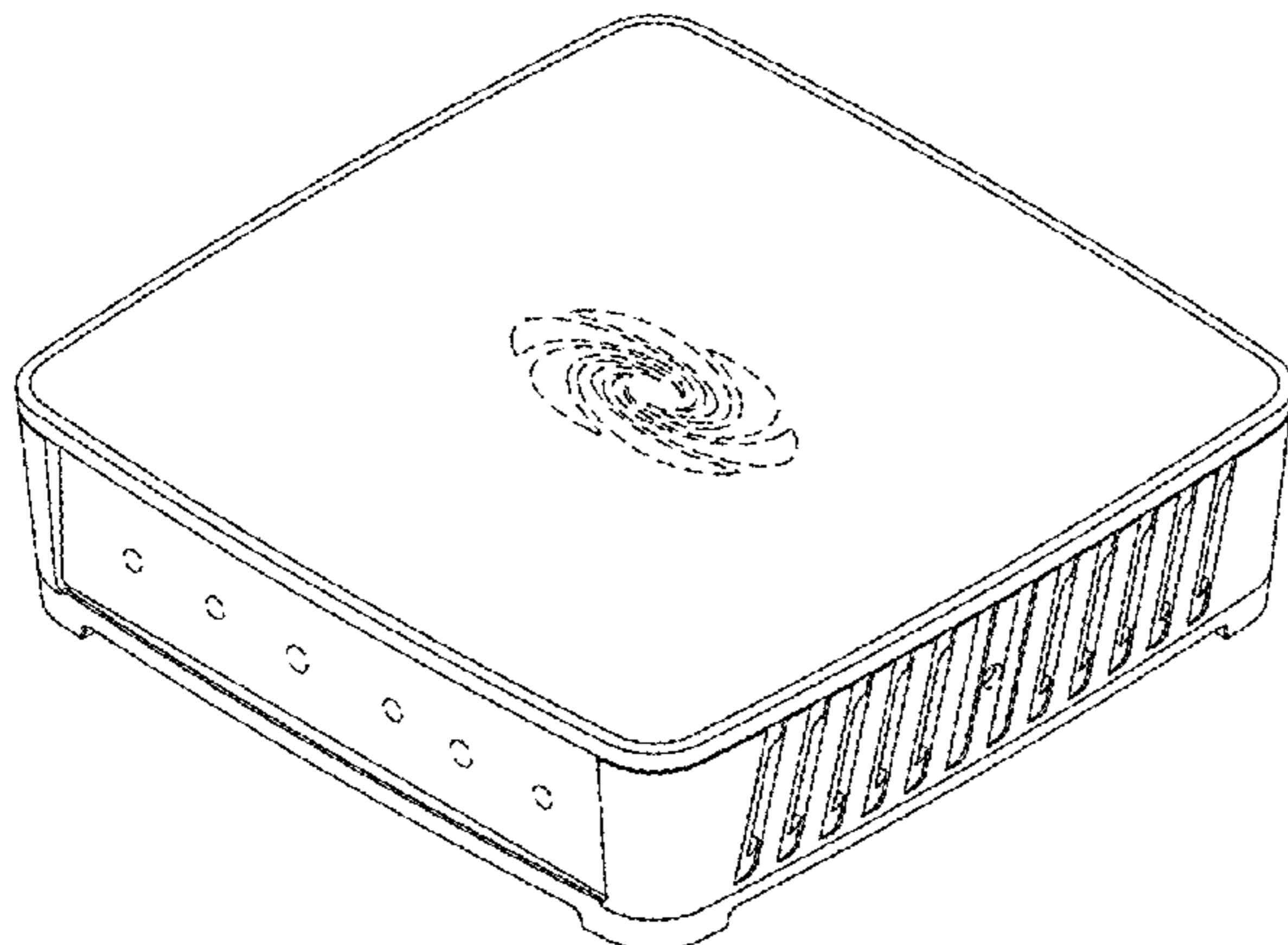
FIG. 7 shows a bottom plan view of an electronic housing; and,

FIG. 8 shows a bottom perspective view of an electronic housing.

The broken lines shown in the figures that are immediately adjacent to the claimed surfaces and define unshaded regions represent the bounds of the claim, while all other broken lines, including the logo at the top which is shown superimposed on a continuously claimed surface, show environmental subject matter; the broken lines form no part of the claimed design.

The “logo” shown in the drawings (e.g., FIGS. 1, 2 and 6) is a registered trademark of Crestron Electronics, Inc., Rockleigh, NJ, USA.

1 Claim, 8 Drawing Sheets



(56)

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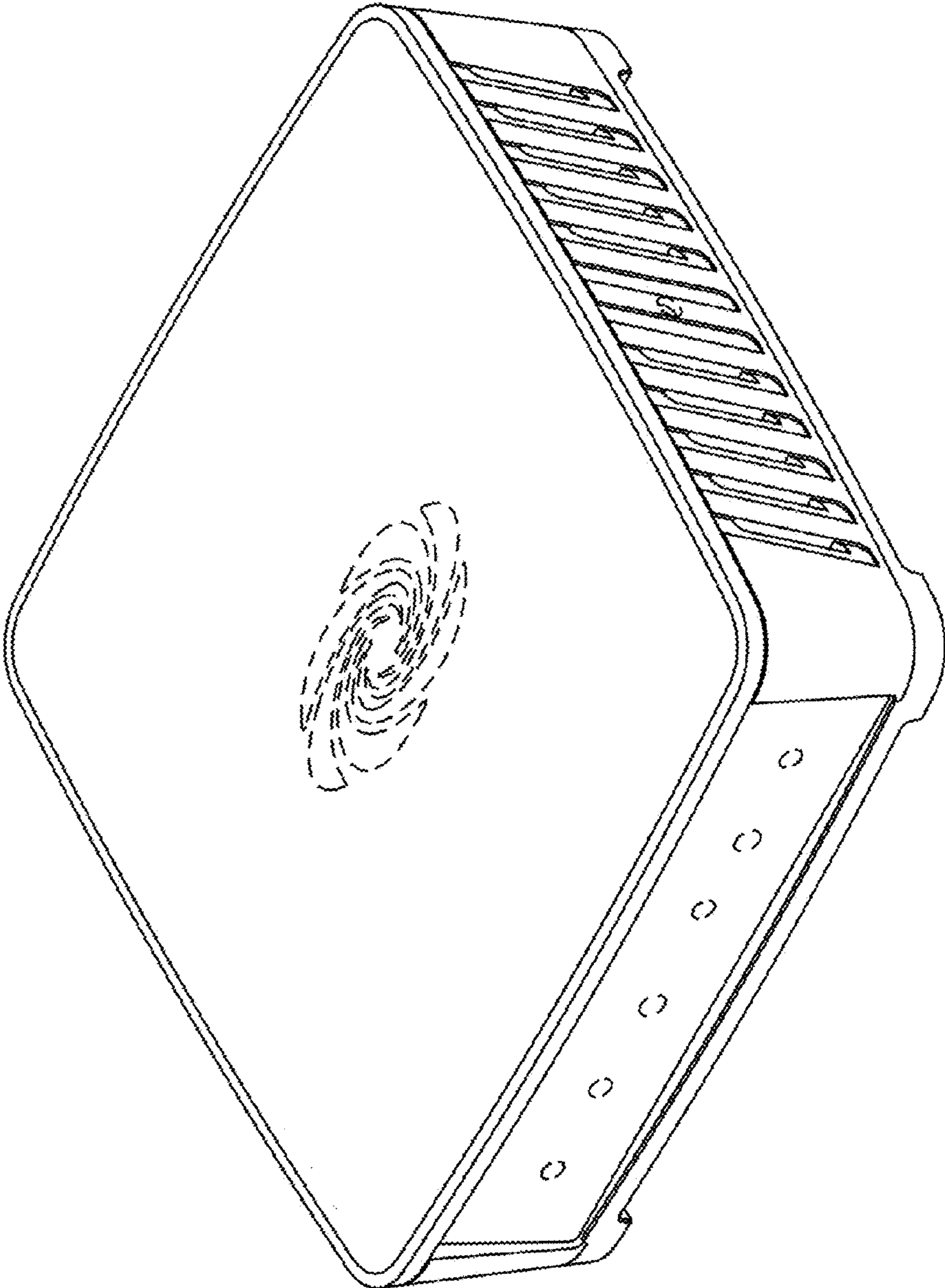


FIG. 1

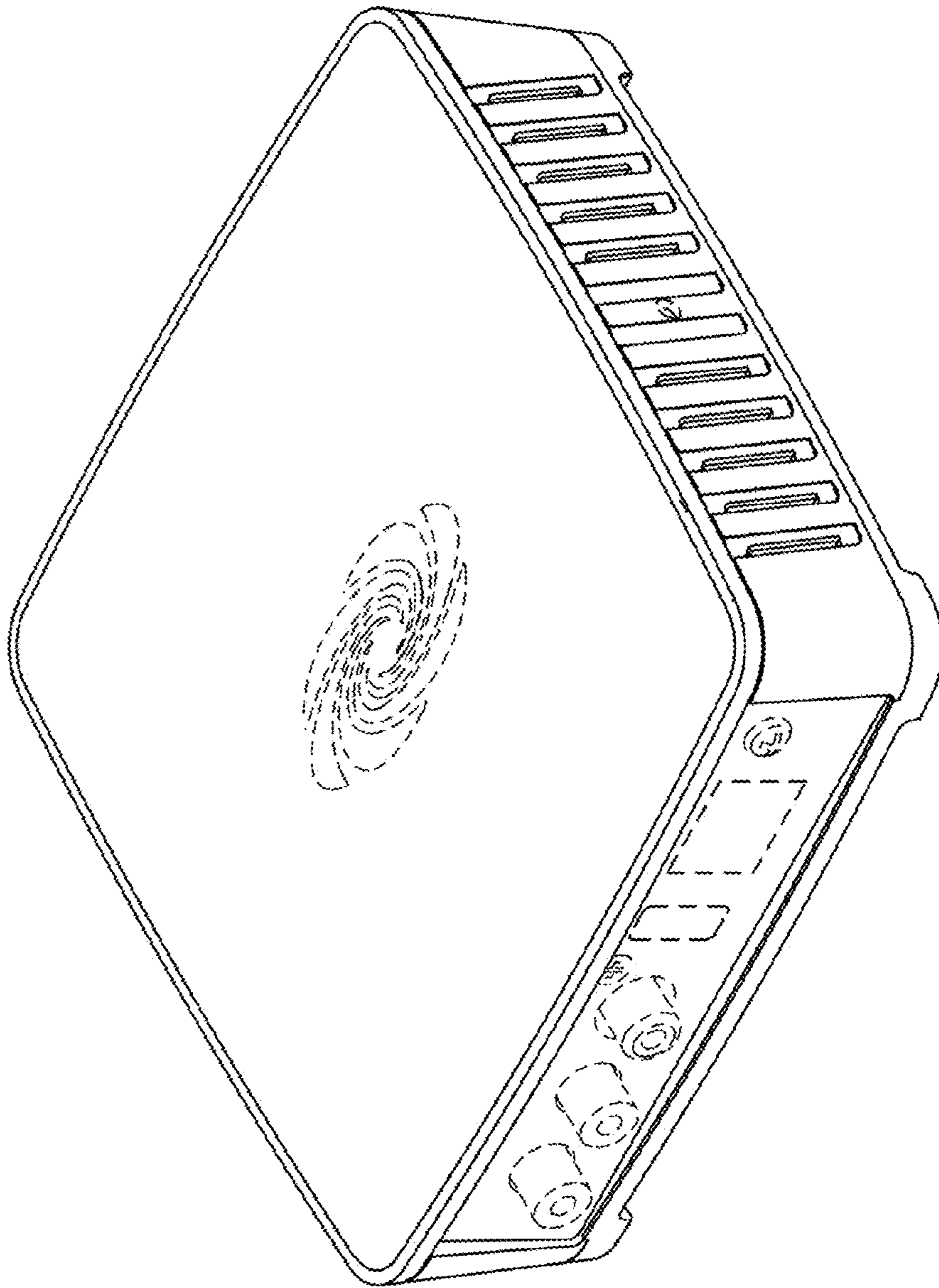


FIG. 2

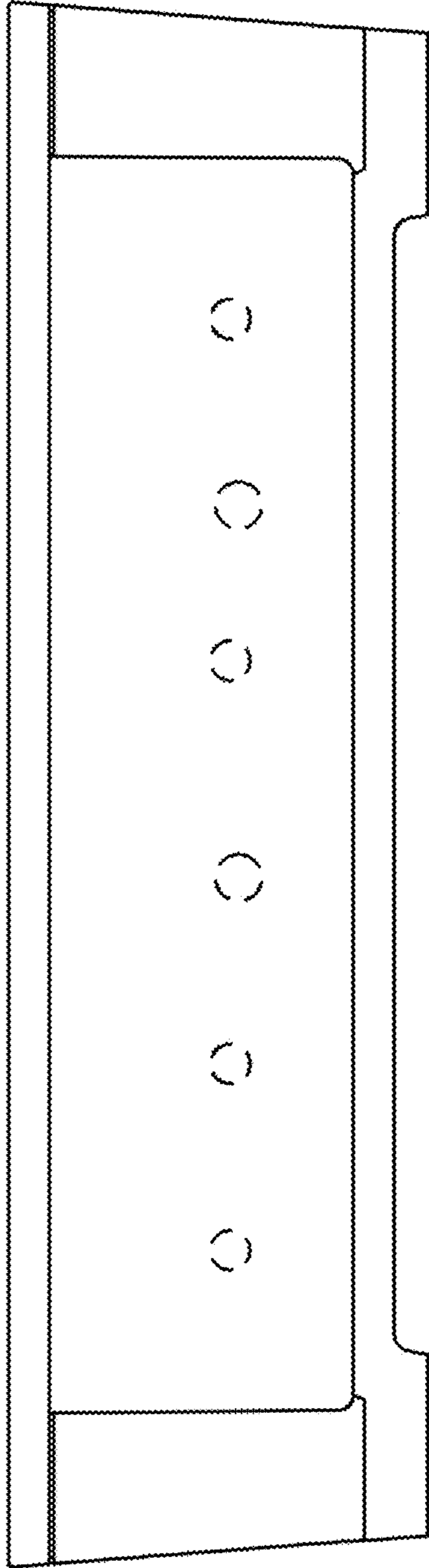


FIG. 3

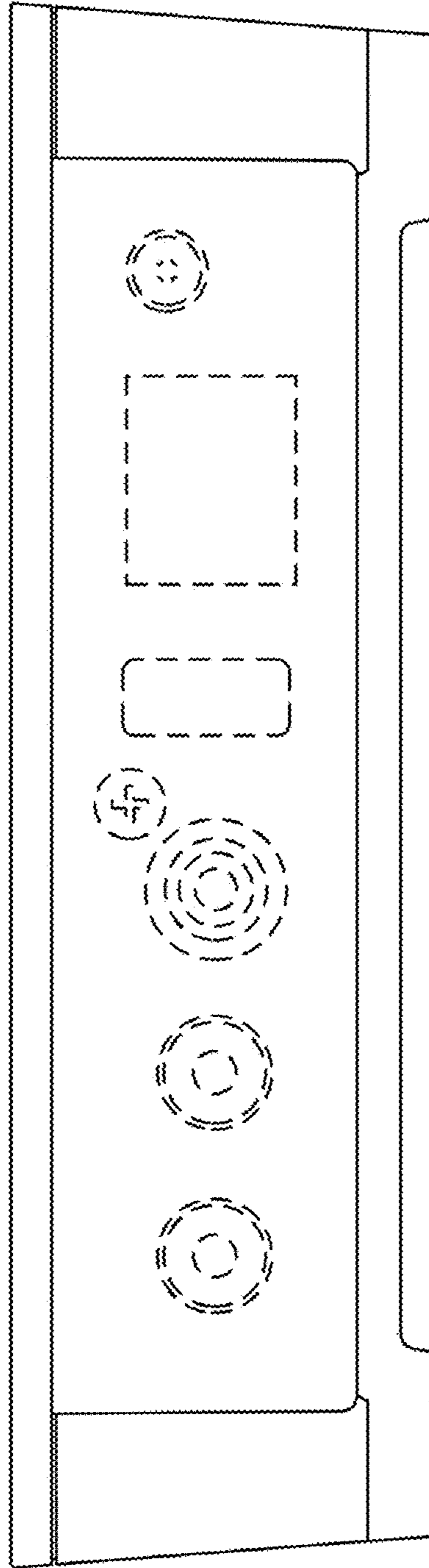


FIG. 4

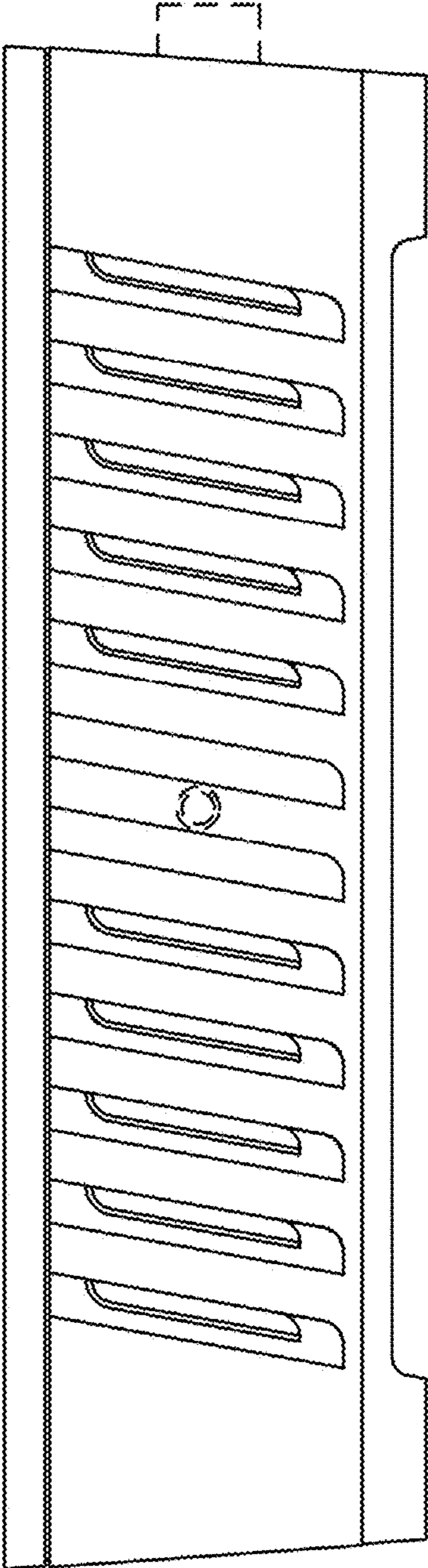


FIG. 5

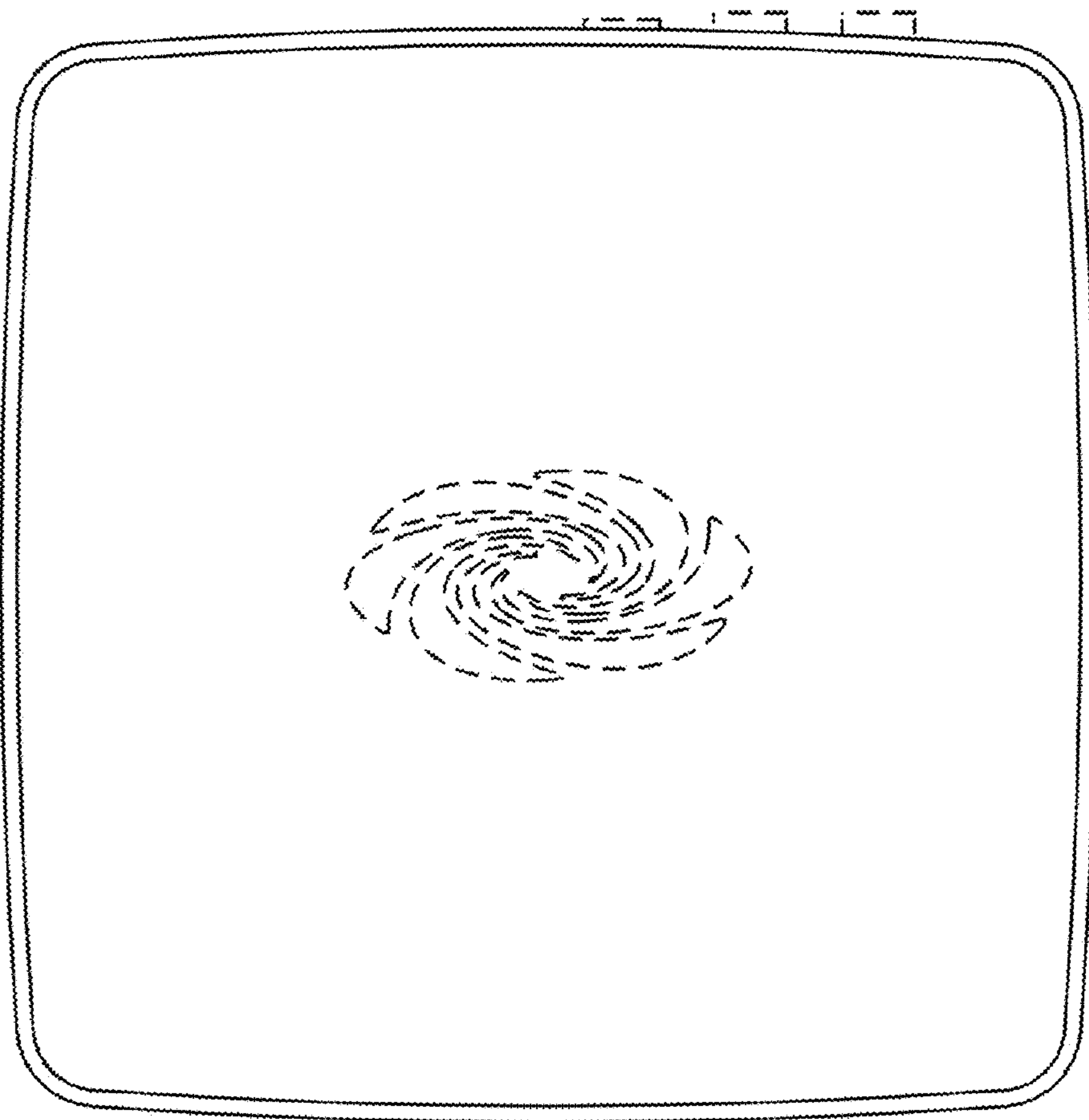


FIG. 6

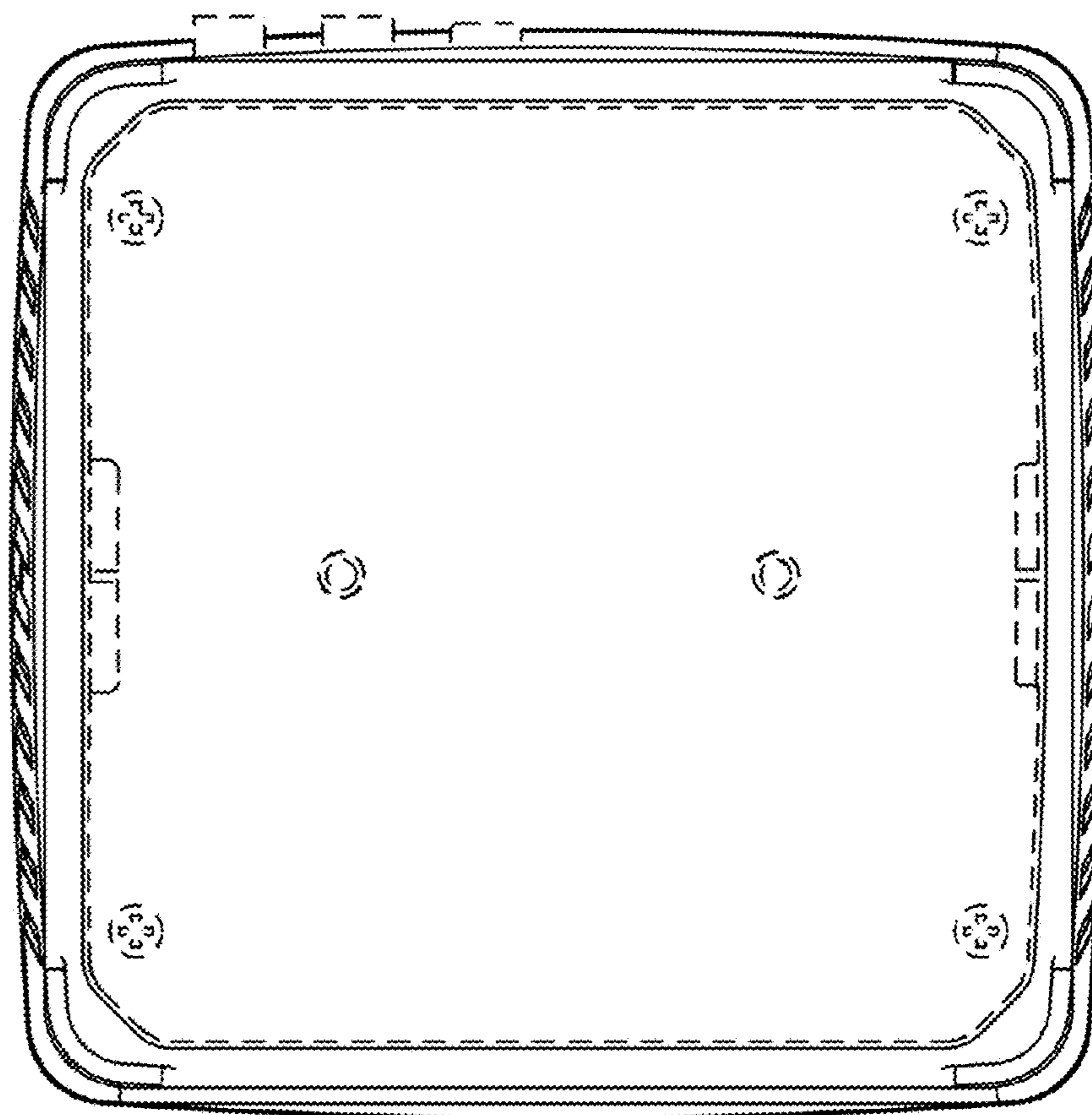


FIG. 7

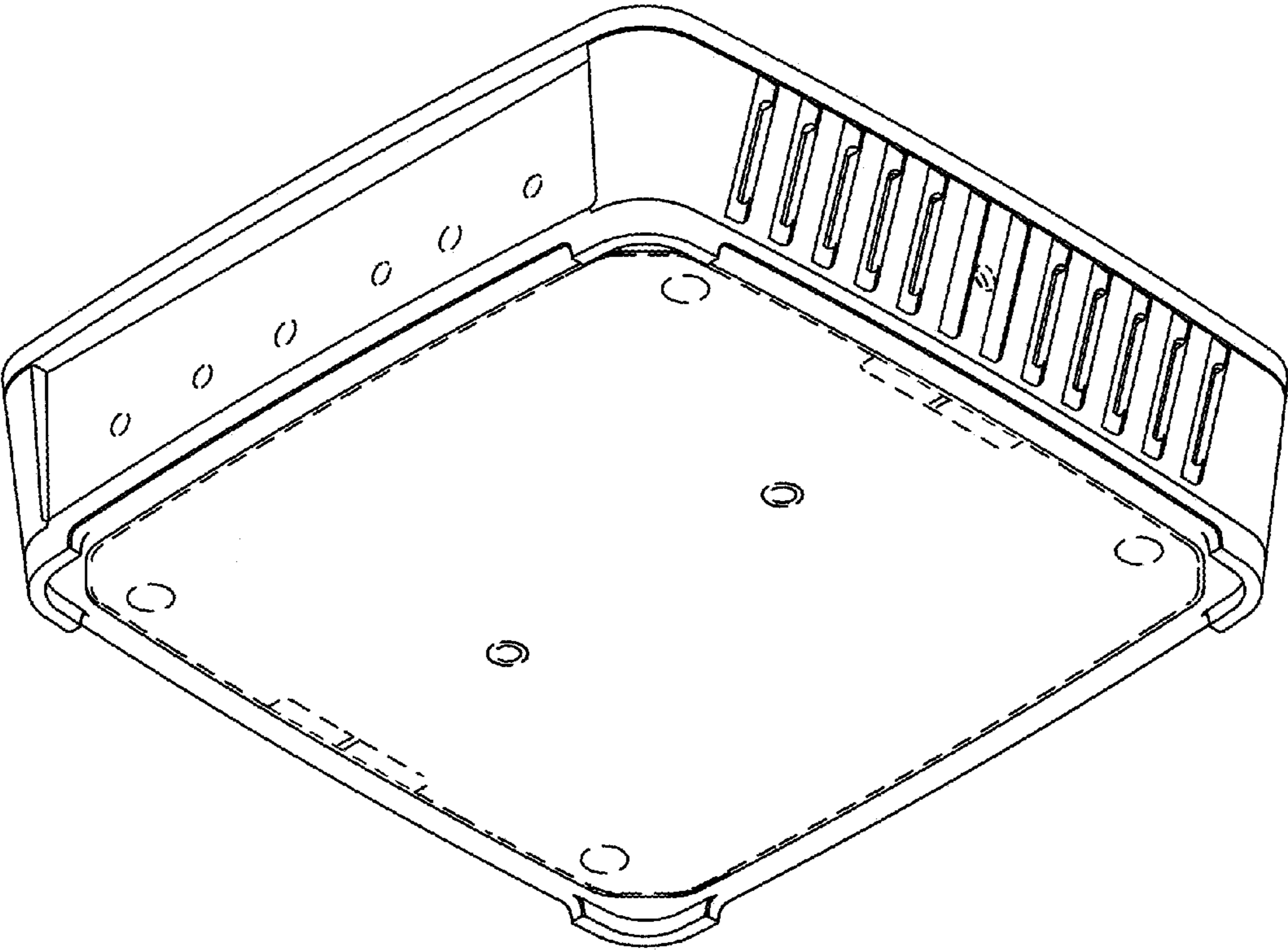


FIG. 8