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(12) **United States Design Patent**
Wengreen

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(54) **MOUNT FOR ELECTRONIC DEVICES**

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(**) Term: **15 Years**

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(52) **U.S. Cl.**

USPC **D14/238.1**; D6/567; D14/447

(58) **Field of Classification Search**

USPC D14/253, 251, 252, 447, 451, 452, 440, D14/434, 458, 432, 439, 457, 460, 238.1, D14/140, 142, 149, 217, 148, 140.8, 140.11, D14/209.1, 151, 140.9, 250, 238; D13/107, D13/108; D12/415, 223, 114, 420, 417, D12/407, 177; D3/218; D16/242-245, 250, D16/237, 203, 214, 219; D6/675, 672, 673, D6/675.4, 553, 676, 310, 406.3, 406.4, D6/406.5, 406.6, 407, 567

CPC B60R 2011/0008; B60R 11/02; B60R 11/0241; B60R 11/0258; B60R 11/0252; B60R 2011/0294; F16M 11/00; F16M 11/04; F16M 11/041; F16M 11/02; F16M 11/06; F16M 13/00; H04M 1/02; H04M 1/04; H04M 1/11; H04M 1/12; A47F 7/022; B62J 11/00; H05K 7/00; G03B 17/56

See application file for complete search history.

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(Continued)

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(57) **CLAIM**

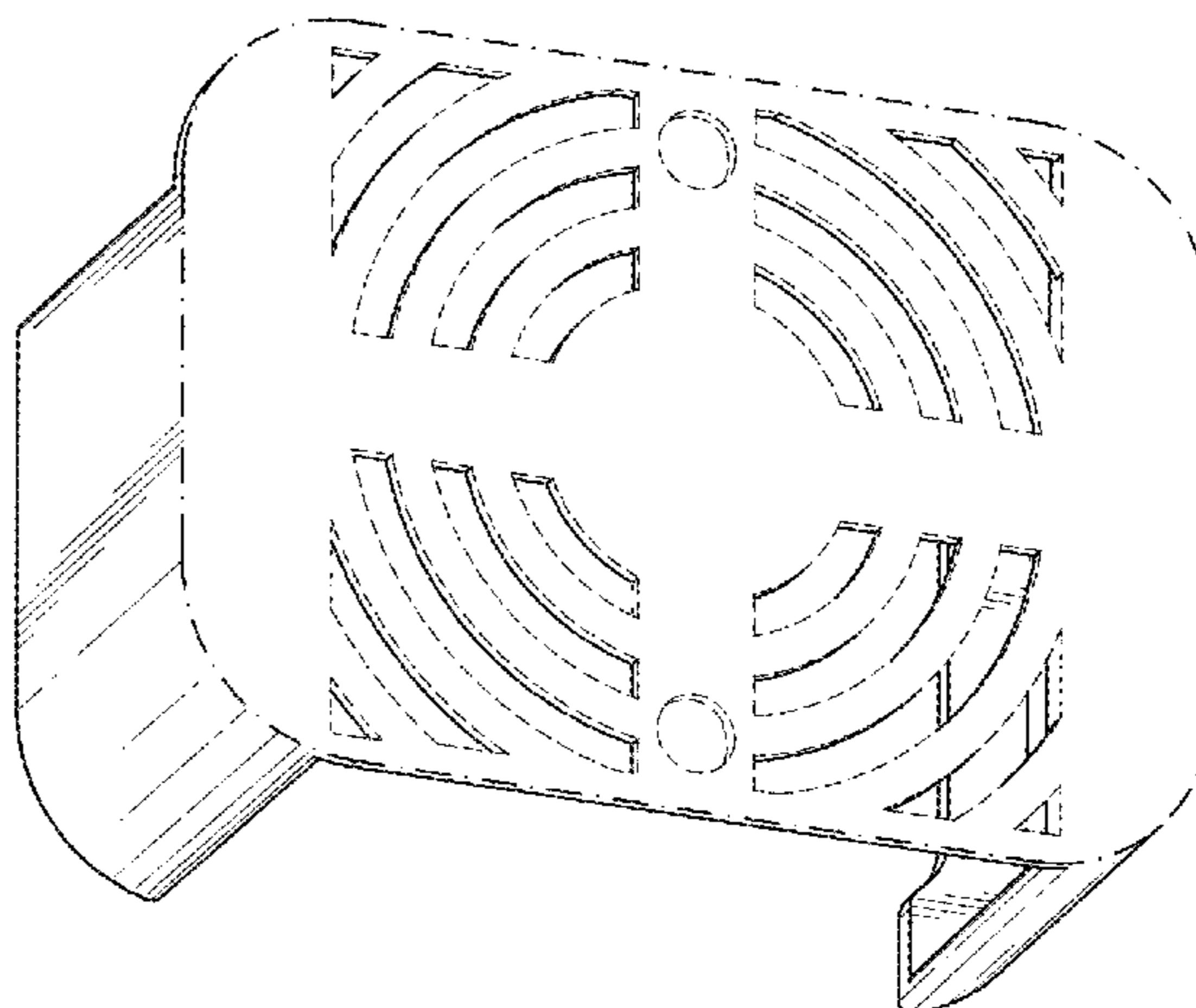
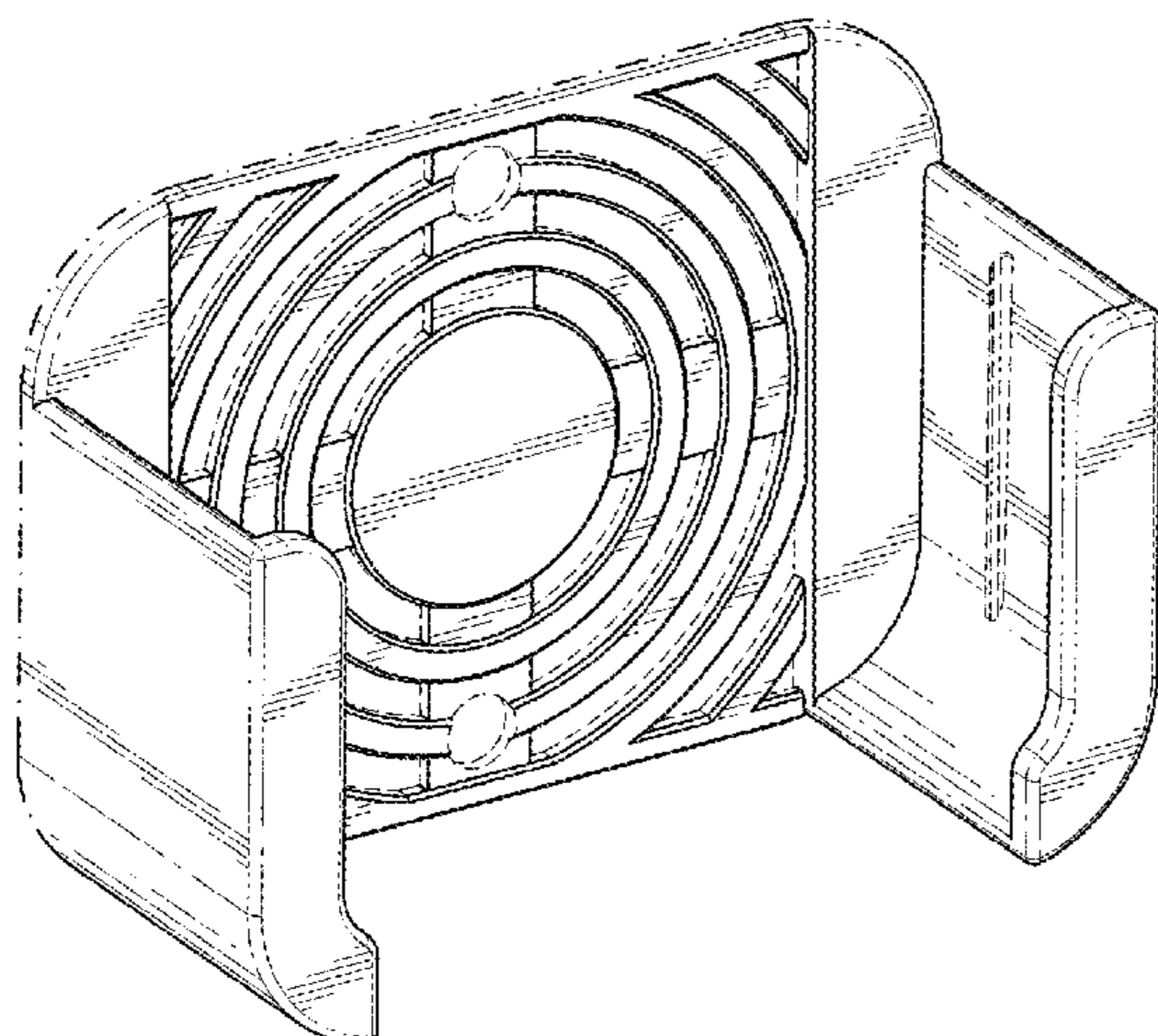
The ornamental design for a mount for electronic devices, as shown and described.

DESCRIPTION

FIG. 1 is a front, side, and top perspective view of a mount for electronic devices embodying the new design; FIG. 2 is a back, side, and bottom perspective view thereof; FIG. 3 is a front view thereof; FIG. 4 is a back view thereof; FIG. 5 is a top view thereof; FIG. 6 is a bottom view thereof; and, FIG. 7 is a first side view thereof, the second side view being a mirror image.

The dash-dot-dash broken lines in the drawings depict the boundaries of the claim and the dash-dash broken lines depict parts of the mount for electronic devices; all broken lines form no part of the claimed design.

1 Claim, 3 Drawing Sheets



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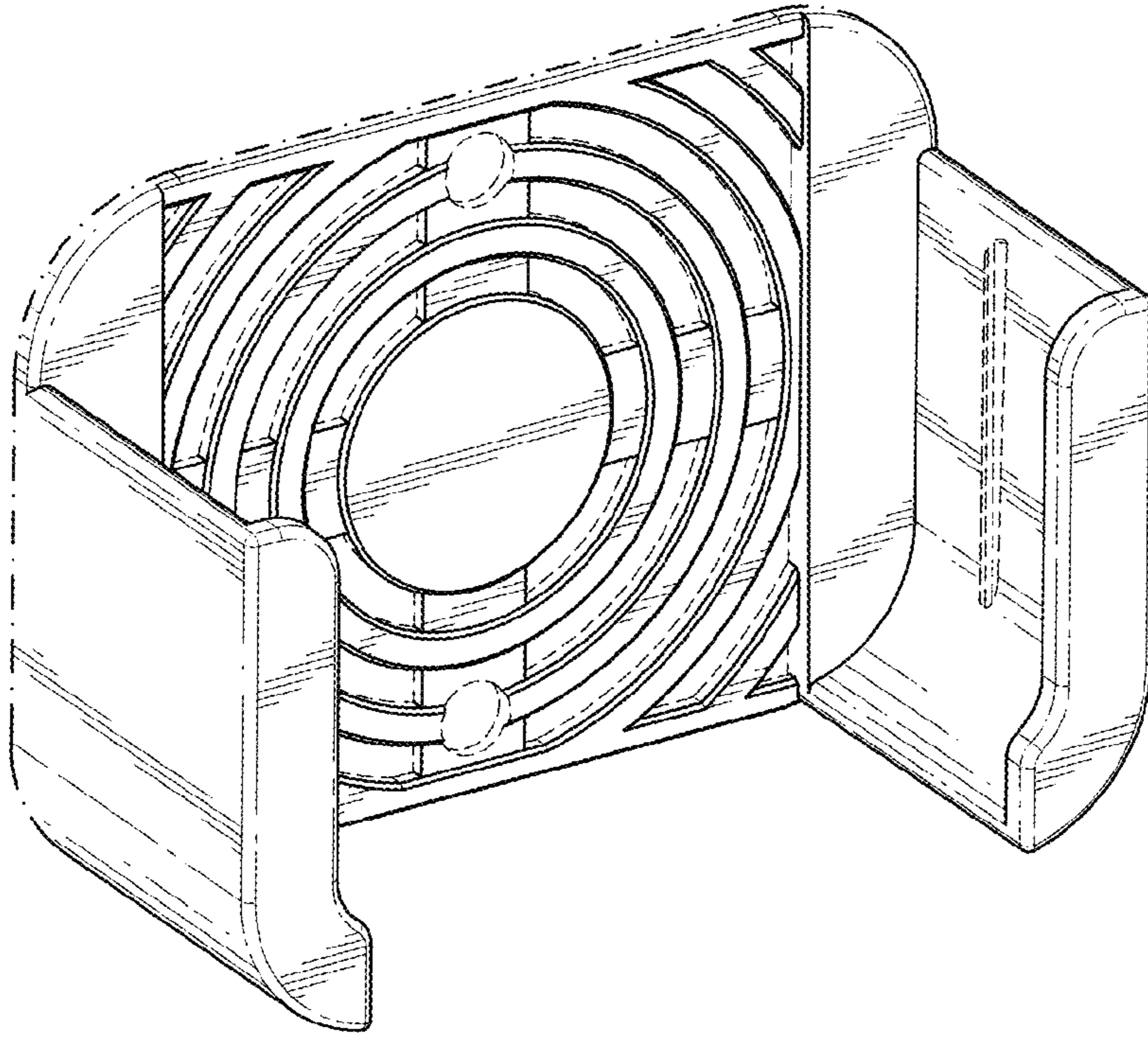


FIG. 1

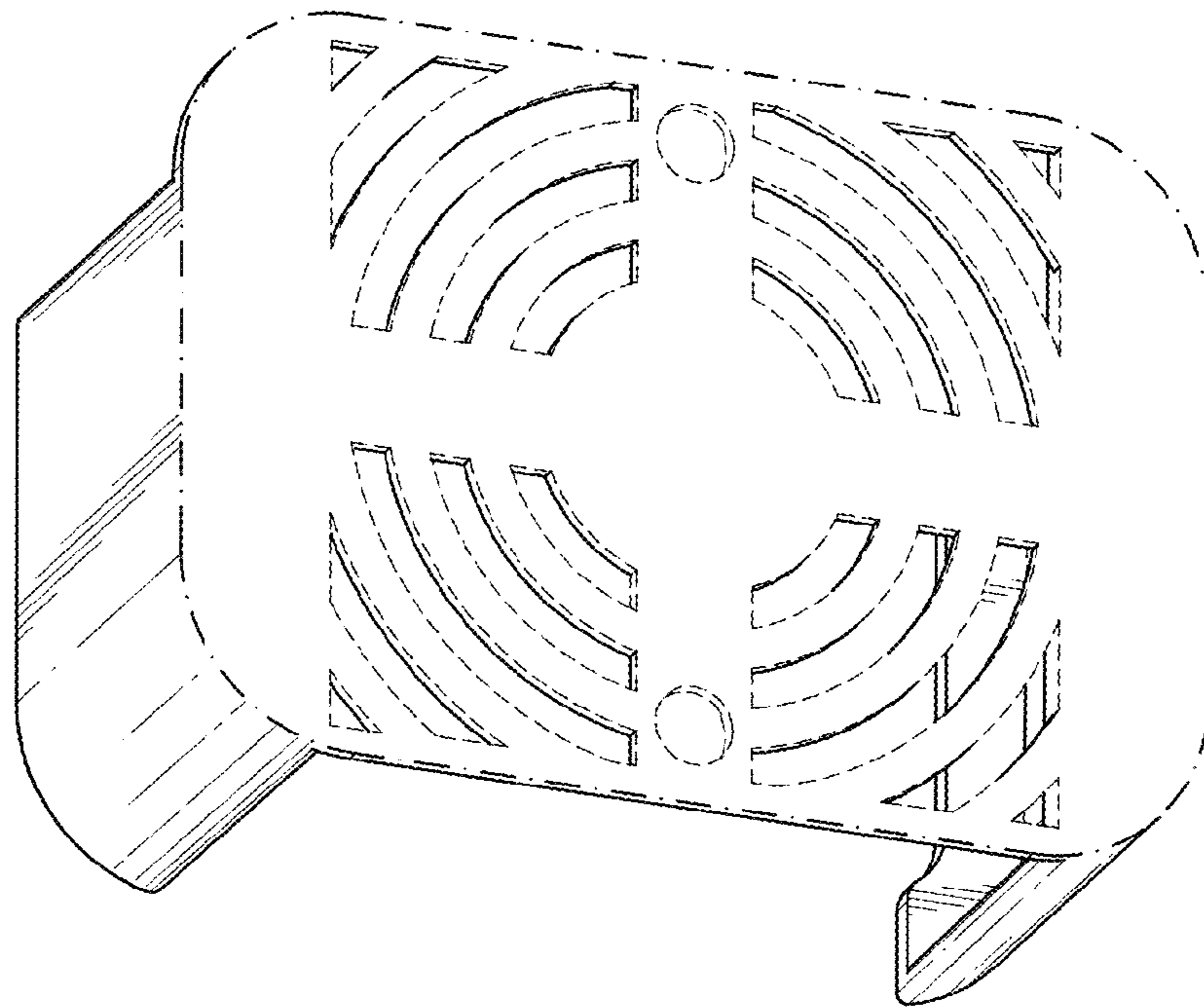


FIG. 2

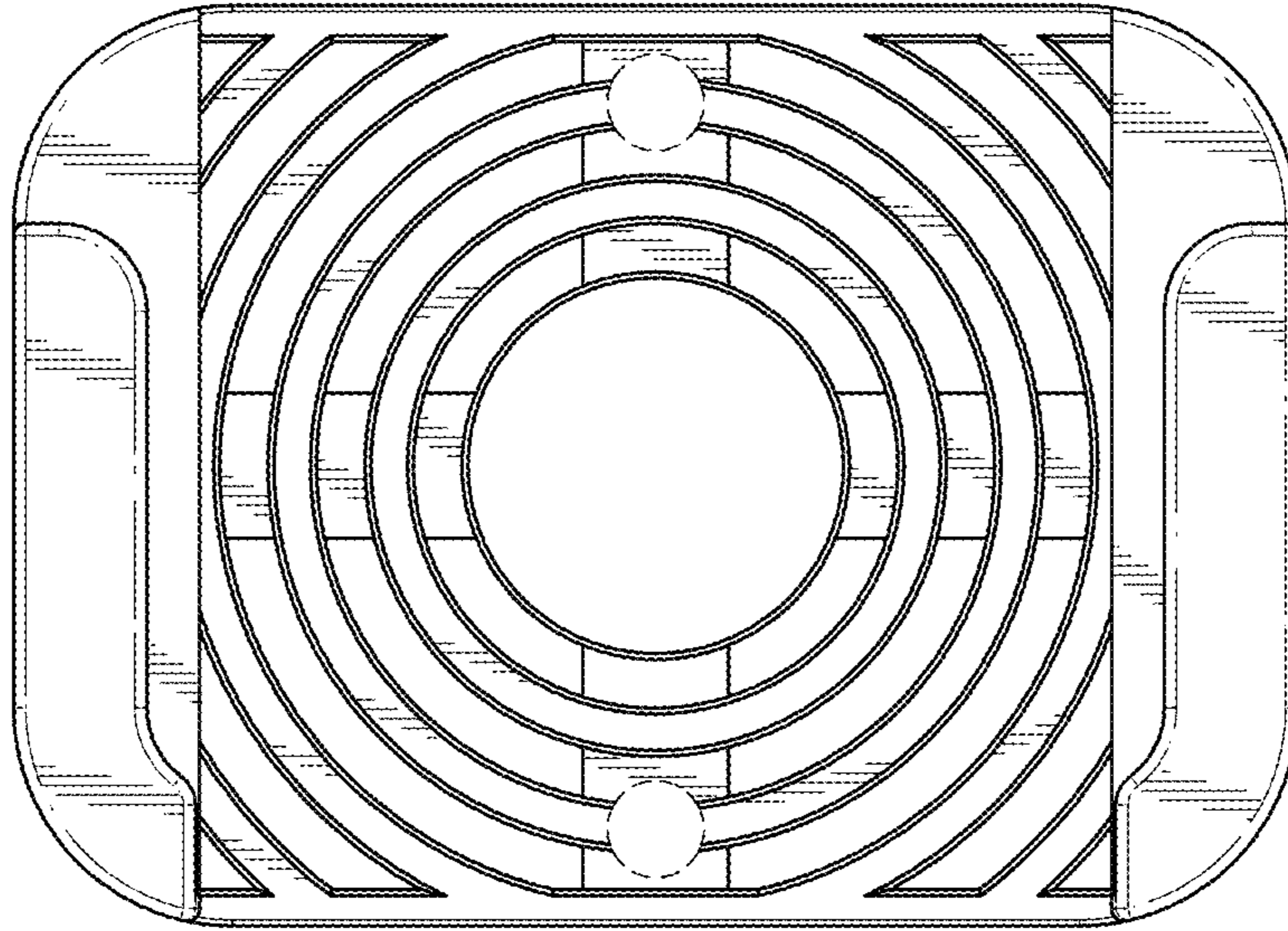


FIG. 3

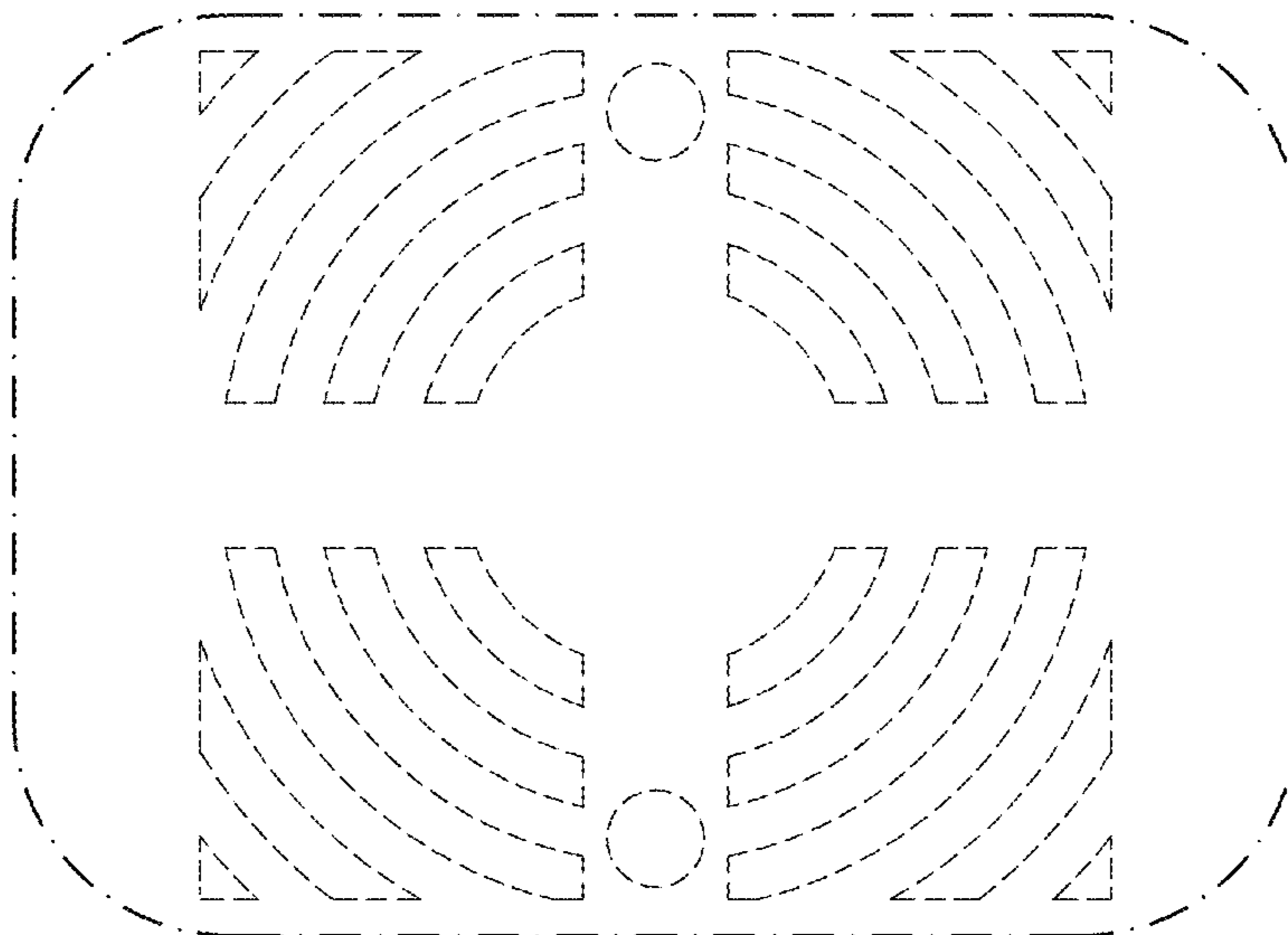


FIG. 4

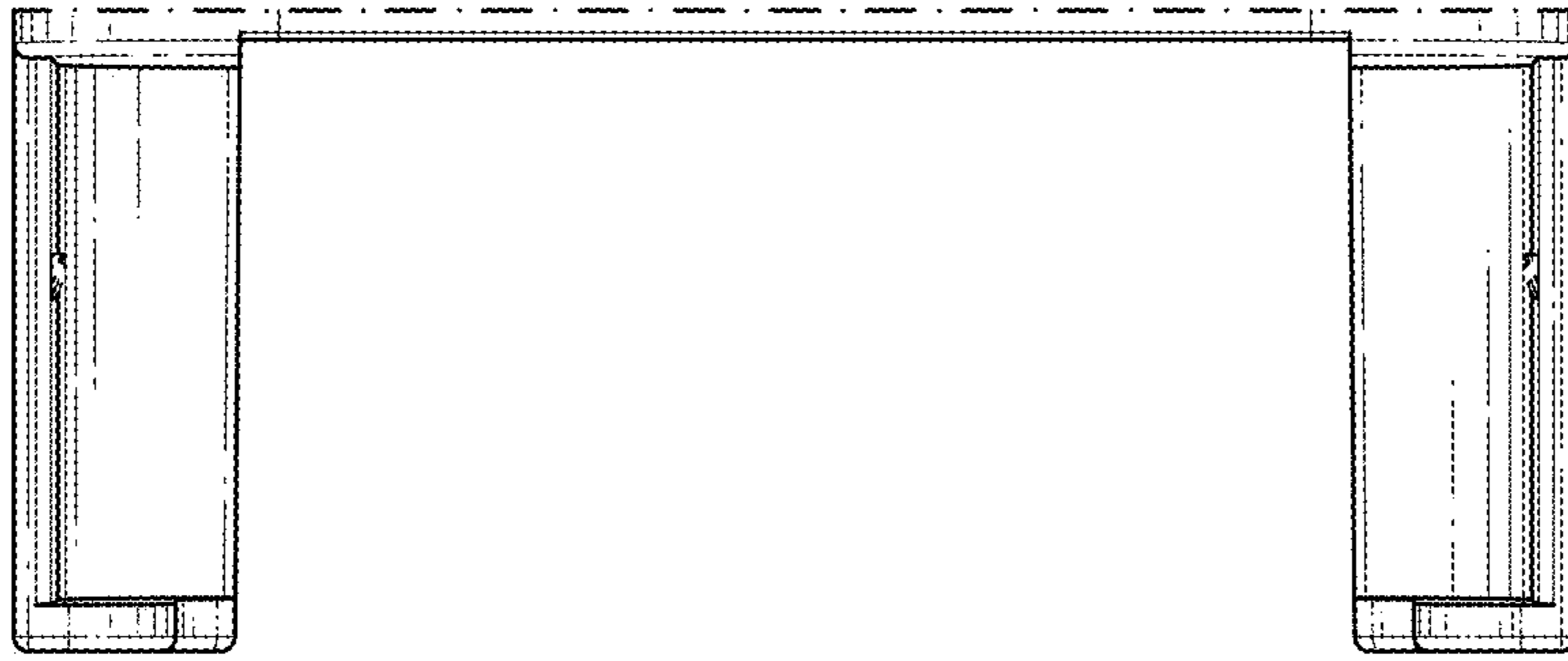


FIG. 5



FIG. 6

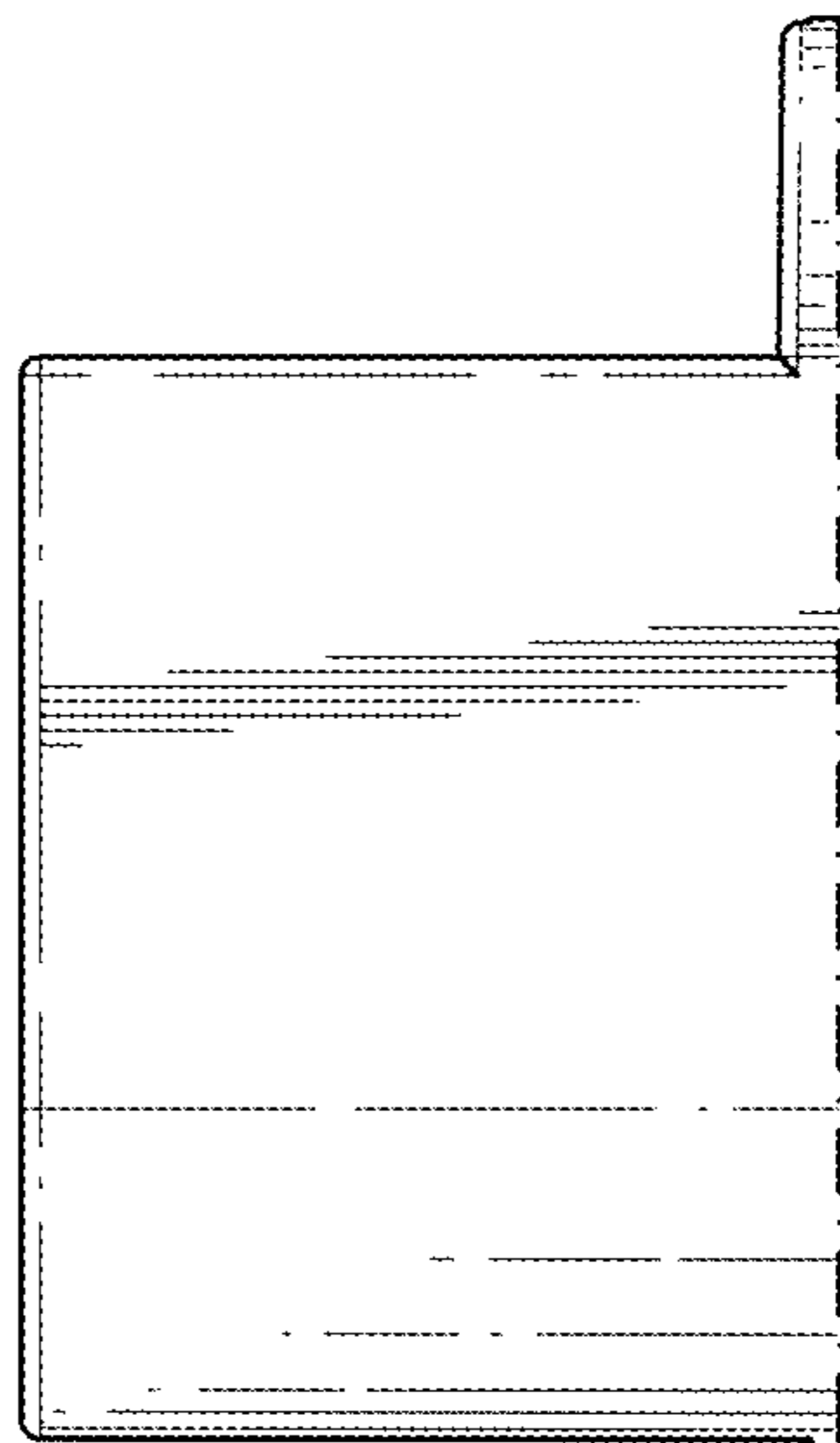


FIG. 7