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(12) **United States Design Patent**  
**Vosch et al.**

(10) **Patent No.:** **US D764,672 S**  
(45) **Date of Patent:** **\*\* Aug. 23, 2016**

- (54) **ELECTRODE PATCH ARRAY**
- (71) Applicants: **NuLine Sensors, LLC**, Lake Mary, FL (US); **Techmedic International, BV**, Langedijk (NL)
- (72) Inventors: **Michael J. Vosch**, Titusville, FL (US); **Benjamin M. Downey**, Sanford, FL (US); **Rutger Alexander Brest van Kempen**, Langedijk (NL)
- (73) Assignees: **NuLine Sensors, LLC**, Lake Mary, FL (US); **Techmedic International, BV**, Langedijk (NL)
- (\*\*) Term: **14 Years**
- (21) Appl. No.: **29/510,454**
- (22) Filed: **Nov. 28, 2014**
- (51) **LOC (10) Cl.** ..... **24-01**
- (52) **U.S. Cl.**  
USPC ..... **D24/187**
- (58) **Field of Classification Search**  
USPC ..... D24/168, 186–187, 200, 107  
CPC ..... A61B 5/04085; A61B 5/0432; A61B 5/0444; A61B 5/0448; A61B 5/6833  
See application file for complete search history.

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*Primary Examiner* — Wan Laymon

(74) *Attorney, Agent, or Firm* — Olive Law Group, PLLC

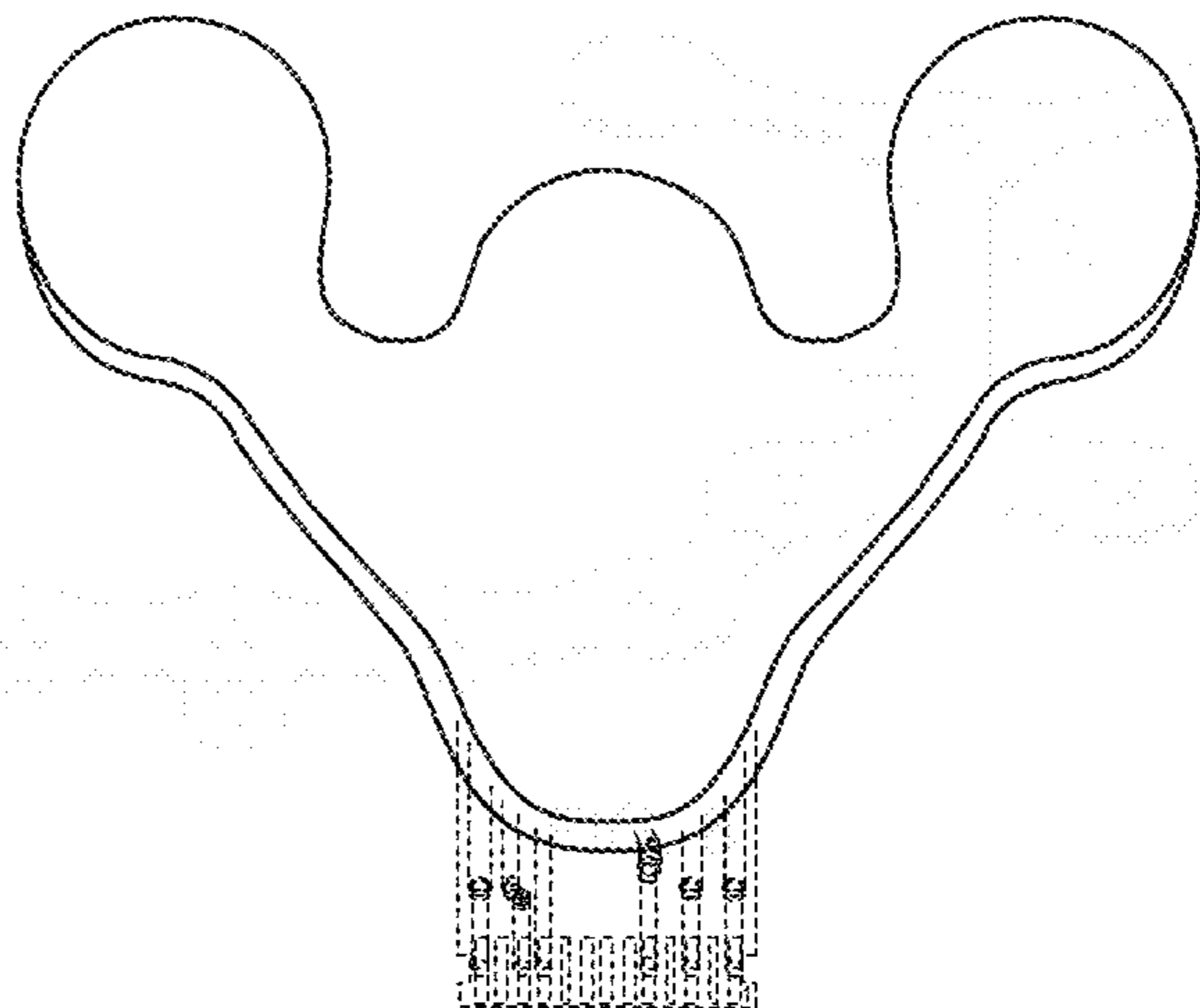
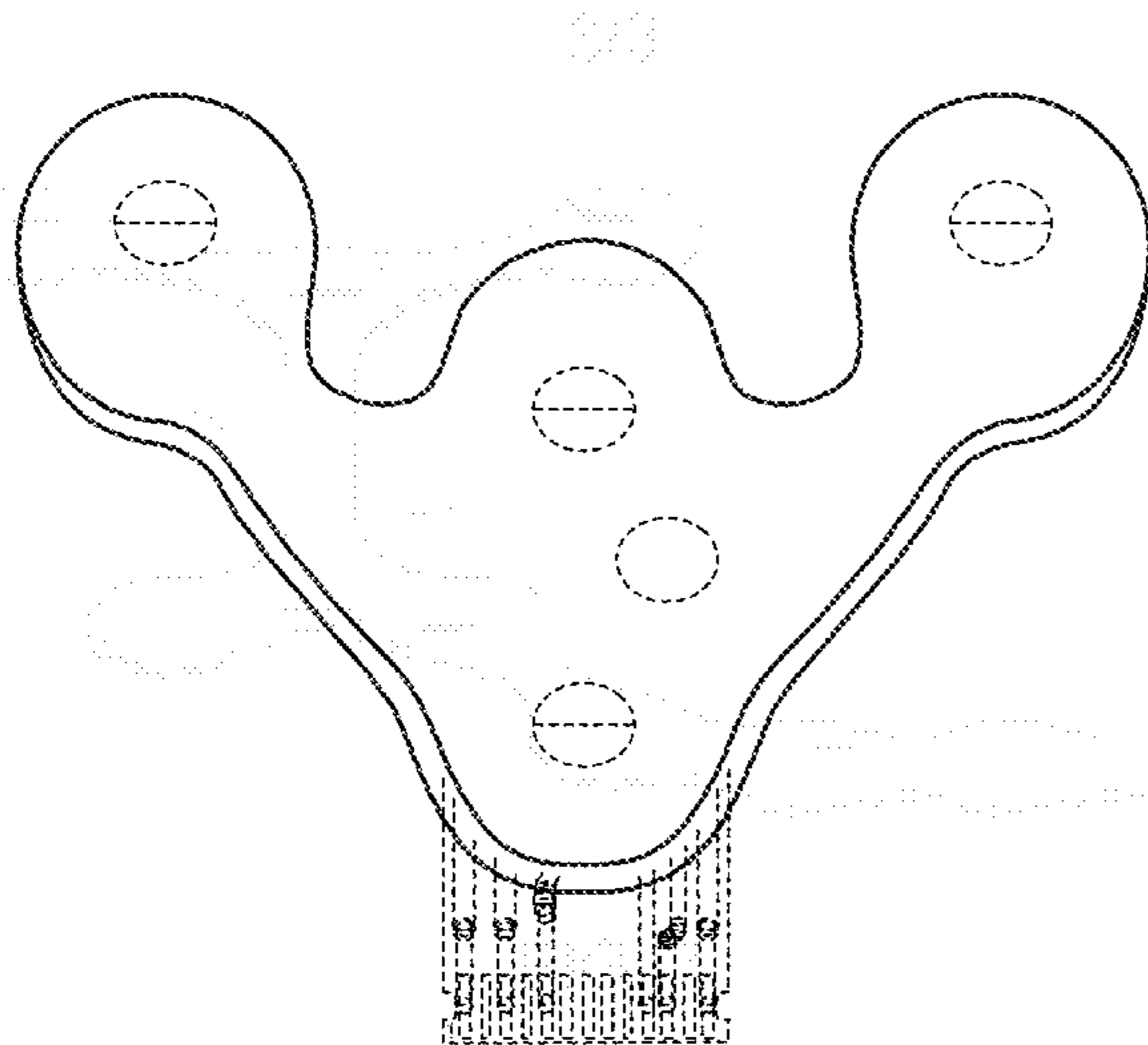
(57) **CLAIM**

We claim the ornamental design for the electrode patch array, as shown and described.

**DESCRIPTION**

FIG. 1 is a top view of an electrode patch array showing our new design;  
 FIG. 2 is a bottom view thereof;  
 FIG. 3 is a side view thereof;  
 FIG. 4 is another side view thereof that opposes the side view of FIG. 3;  
 FIG. 5 is a side view thereof;  
 FIG. 6 is another side view thereof that opposes the side view of FIG. 5;  
 FIG. 7 is a top perspective view thereof; and,  
 FIG. 8 is a bottom perspective view thereof.  
 The broken lines are included for purpose of illustrating portions of the electrode patch array that form no part of the claimed design.

**1 Claim, 4 Drawing Sheets**



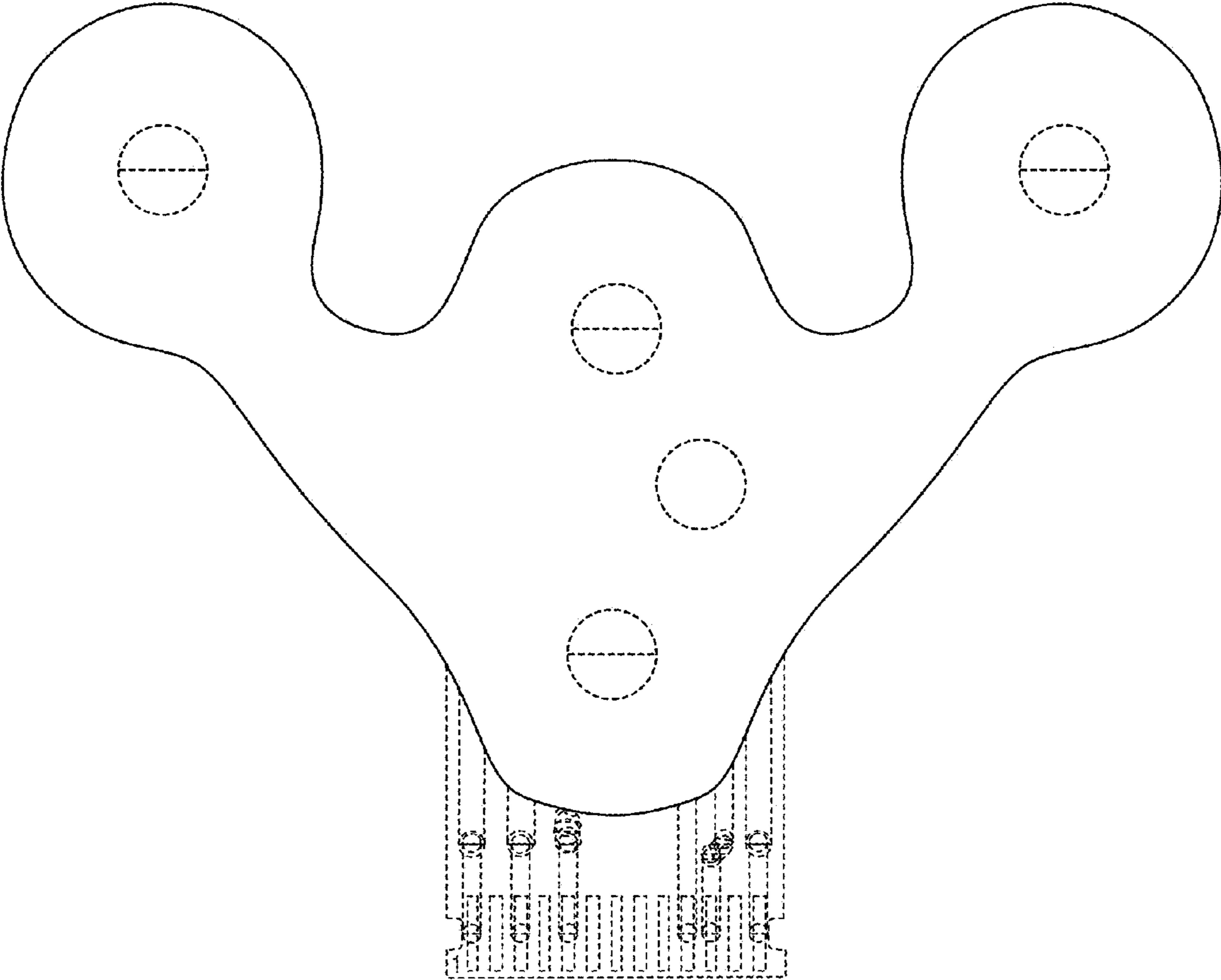


FIG. 1

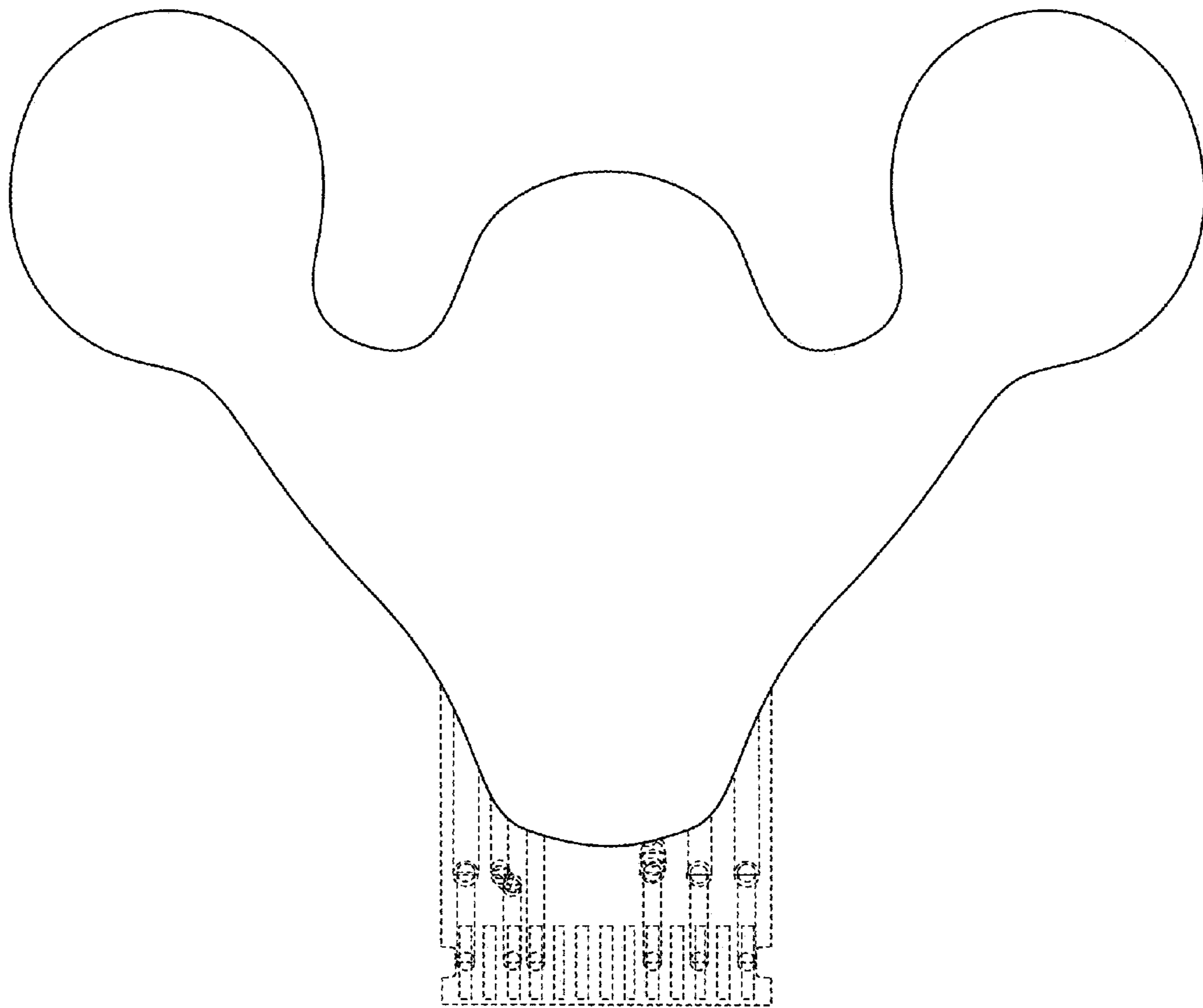


FIG. 2

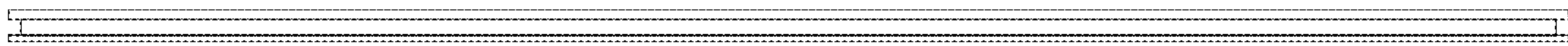


FIG. 3

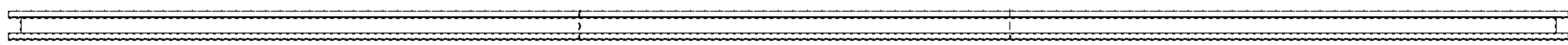


FIG. 4

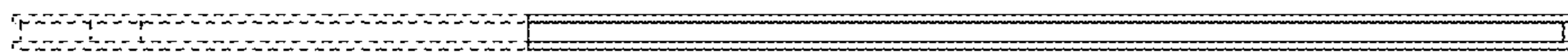


FIG. 5

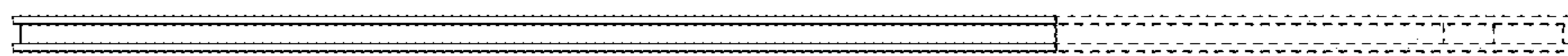


FIG. 6

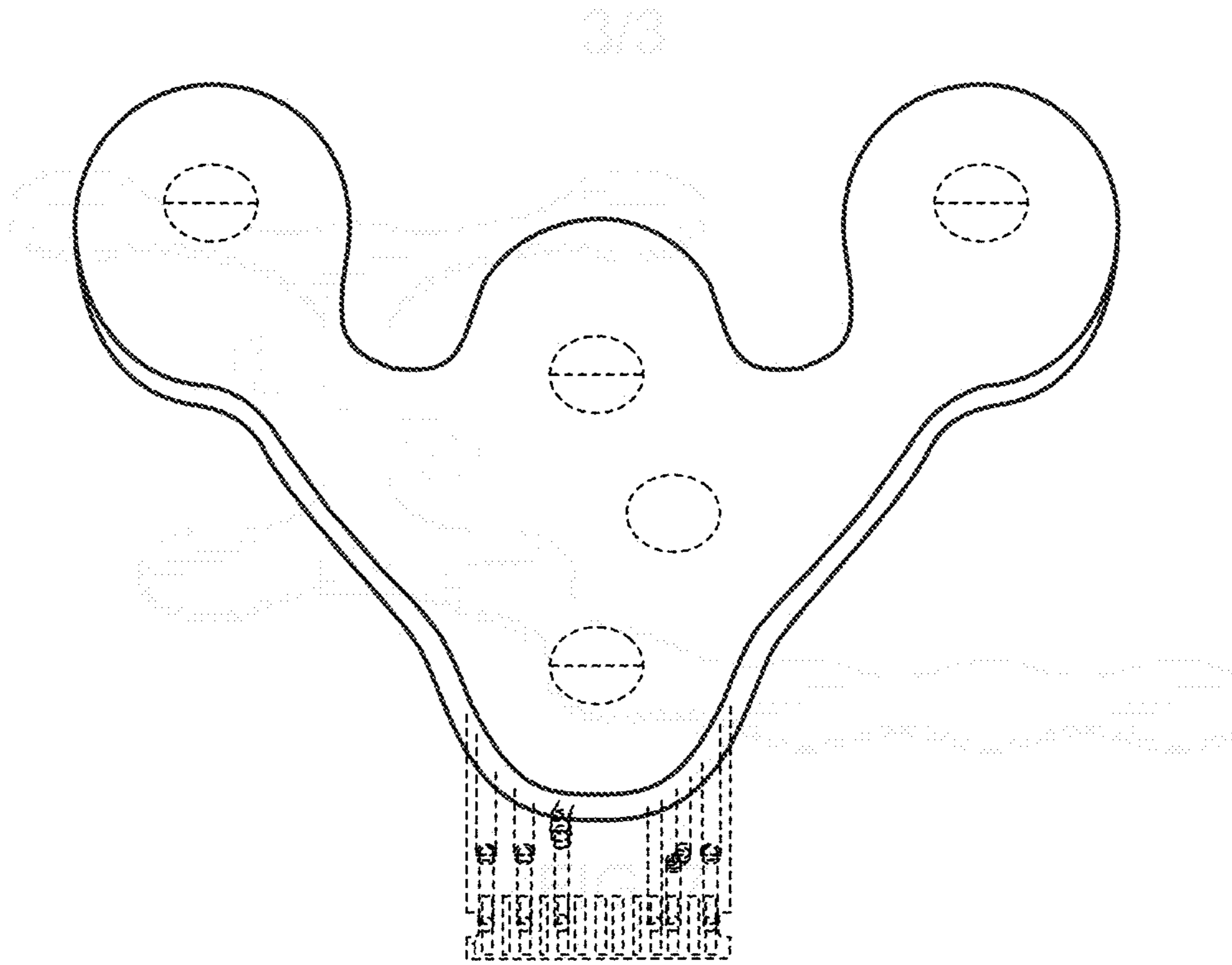


FIG. 7

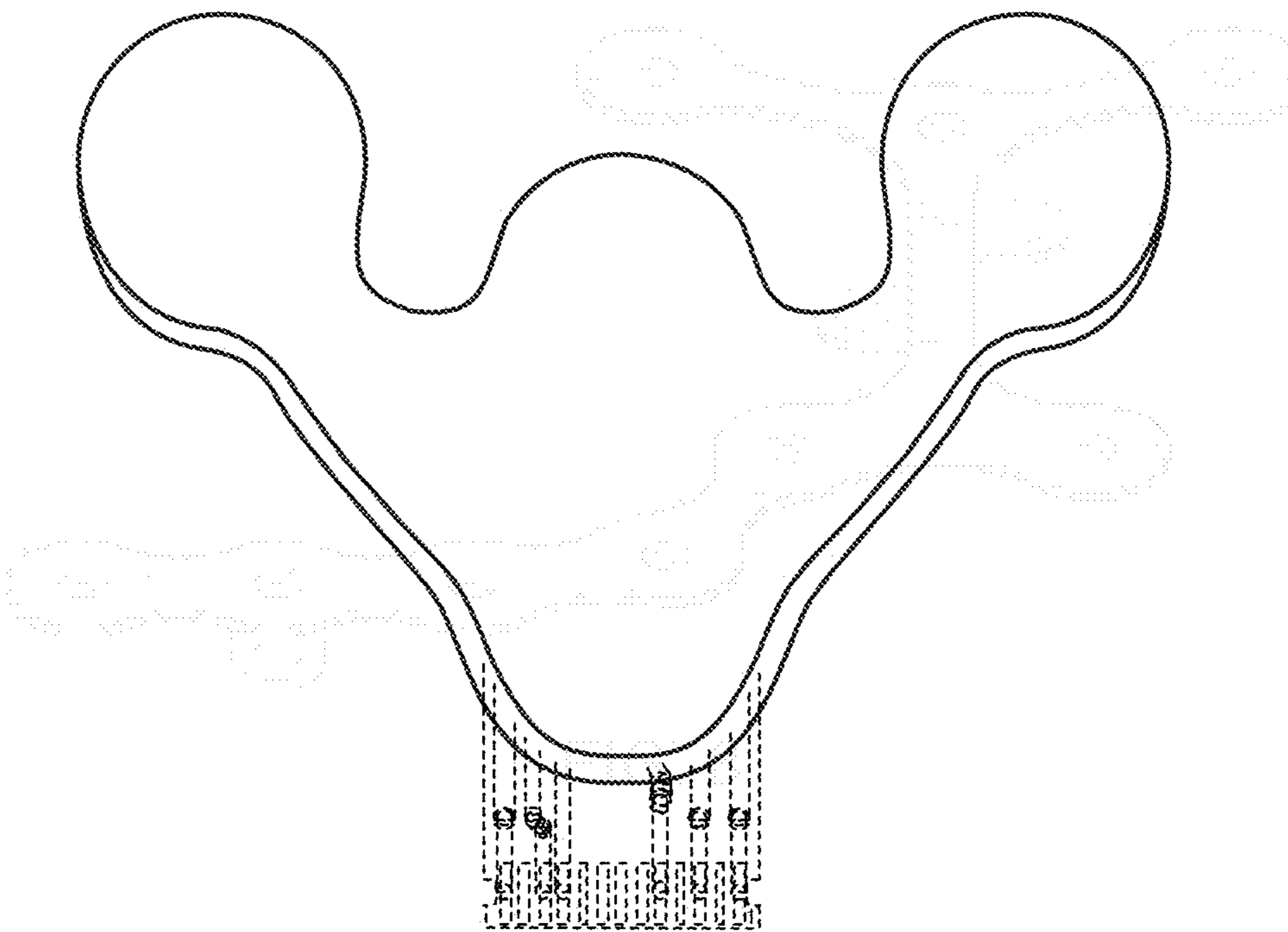


FIG. 8